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RJK0349DSP

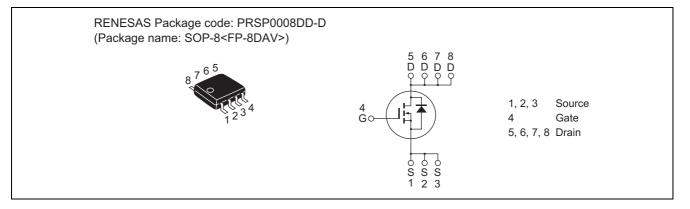
Silicon N Channel Power MOS FET Power Switching

REJ03G1659-0300 Rev.3.00 Jul 10, 2008

Features

- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance
- $R_{DS(on)} = 2.9 \text{ m}\Omega \text{ typ.}$ (at $V_{GS} = 10 \text{ V}$)
- Pb-free

Outline



Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	20	A
Drain peak current	Note1 I _{D(pulse)}	160	A
Body-drain diode reverse drain current	I _{DR}	20	A
Avalanche current	I _{AP} Note 2	20	A
Avalanche energy	E _{AR} Note 2	40	mJ
Channel dissipation	Pch Note3	2.5	W
Channel to ambient thermal impedance	θch-a ^{Note3}	50	°C/W
Channel temperature	Tch	150	٥°C
Storage temperature	Tstg	-55 to +150	۵°

Notes: 1. $PW \le 10 \ \mu s$, duty cycle $\le 1\%$

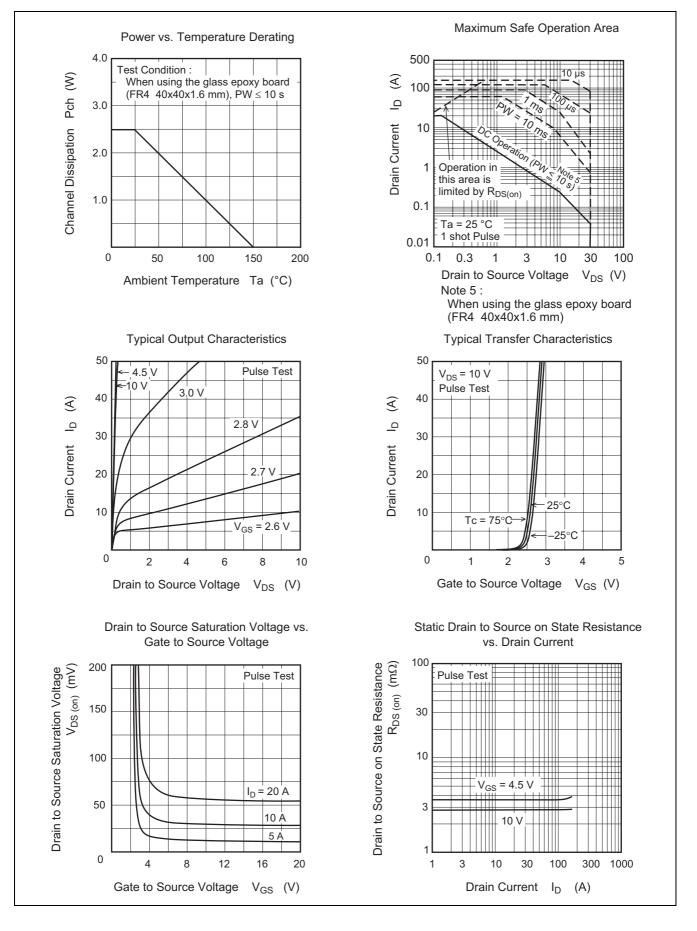
- 2. Value at Tch = 25°C, Rg \geq 50 Ω
- 3. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10s

Electrical Characteristics

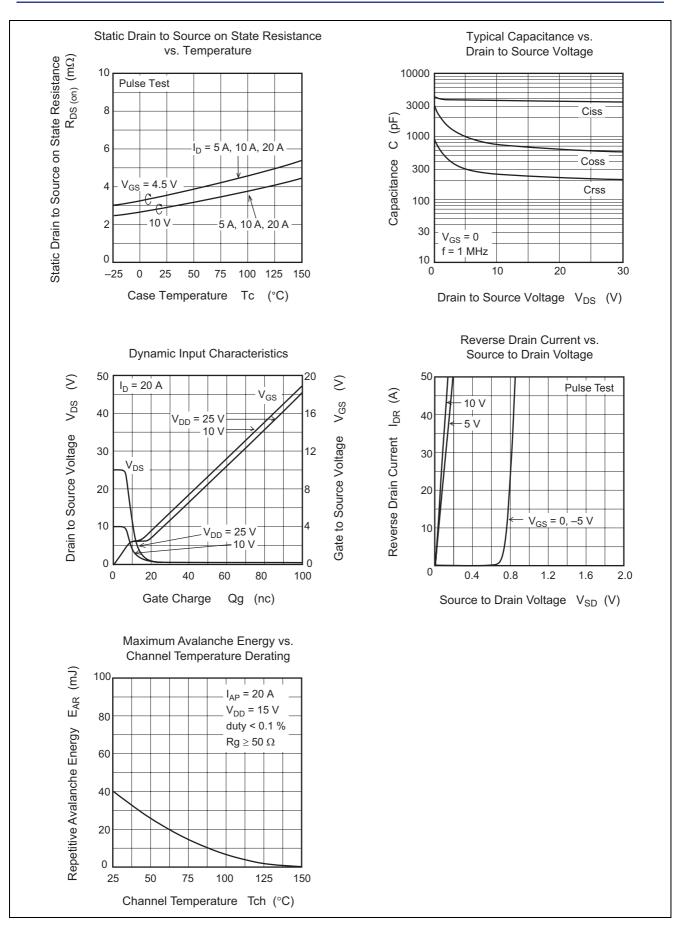
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	30	—	—	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	—	—	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 30 V, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	1.2	—	2.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	2.9	3.8	mΩ	$I_D = 10 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
resistance	R _{DS(on)}	_	3.6	5.0	mΩ	$I_D = 10 \text{ A}, V_{GS} = 4.5 \text{ V}^{Note4}$
Forward transfer admittance	y _{fs}	_	55	—	S	$I_D = 10 \text{ A}, V_{DS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss		3850	_	pF	V _{DS} = 10 V
Output capacitance	Coss		740	_	pF	V _{GS} = 0 f = 1 MHz
Reverse transfer capacitance	Crss		240	_	pF	
Gate Resistance	Rg		1.5	_	Ω	
Total gate charge	Qg		25	_	nC	V _{DD} = 10 V
Gate to source charge	Qgs		9.5	_	nC	V _{GS} = 4.5 V I _D = 20 A
Gate to drain charge	Qgd		5.3	_	nC	
Turn-on delay time	t _{d(on)}		11	_	ns	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$
Rise time	tr		4.7	_	ns	$V_{DD} \cong 10 \text{ V}$ $R_{L} = 1.00 \Omega$ $Rg = 4.7 \Omega$
Turn-off delay time	t _{d(off)}		58.5	_	ns	
Fall time	t _f	_	9.8	_	ns	
Body–drain diode forward voltage	V_{DF}	_	0.78	1.02	V	$I_F = 20 \text{ A}, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery	t _{rr}		30		ns	$I_F = 20 \text{ A}, V_{GS} = 0$
time						di _F / dt = 100 A/ μs

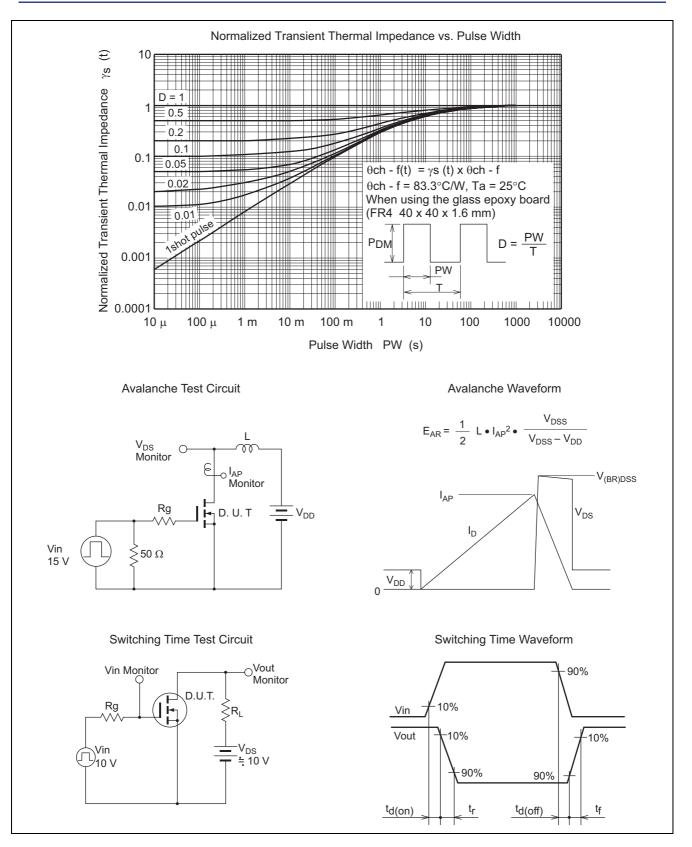
Notes: 4. Pulse test

Main Characteristics



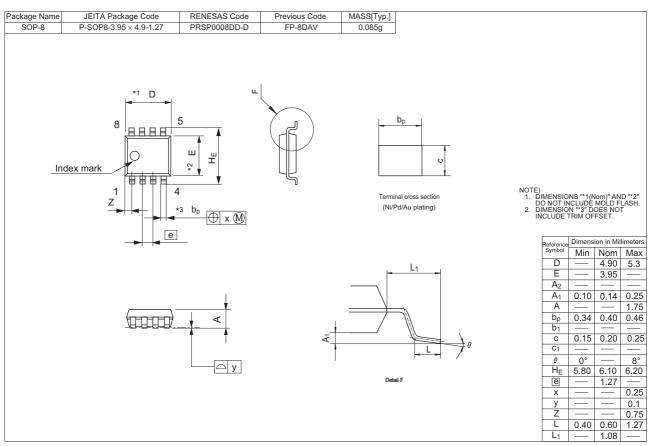
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Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK0349DSP-00-J0	2500 pcs	Taping

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April 1st, 2010 Renesas Electronics Corporation

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