

# R5F56316CDFP

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- Products
- Microcontroller and Microprocessor
- RX Family
- RX600
- RX63N, RX631

- Overview
- Documentation

- Product Spec
- Order Information
- Contact
- Package

## Product Spec

- Basic Information
- Memory
- CPU (Bit, Clock, DMA, External Bus etc)
- Timers
- Analog
- Interfaces
- Operating Conditions

Basic Information	
Product Status	Mass Production
Family	<a href="#">RX</a>
Series	<a href="#">RX600</a>
Group	<a href="#">RX631</a>

Memory	
Program memory	256 KB Flash memory
RAM	128 KB
Data Flash	32 KB

CPU (Bit, Clock, DMA, External Bus etc)	
CPU	RX (32-bit)
Max. Frequency	100 MHz
Sub-Clock (32.768 kHz)	YES
On-chip Oscillator Freq.	50 MHz, Low Speed Oscillator 125 kHz
PLL	YES
Real-Time Clock (RTC)	YES
Power-On Reset	YES
Low Voltage Detection	YES
Floating Point Unit	YES
DMA Remarks	DMAC x 4ch, EXDMAC x 2ch, DTC
External Address/Data Bus	YES
External Interrupt Pins	16
I/O Ports	79

Timers	
8-bit Timers	4 ch
16-bit Timers	16 ch
Watchdog Timers	2 ch
PWM Output	32
3-Phase PWM Output Function	YES

Analog	
A/D Converters	10-bit x 8 ch, 12-bit x 14 ch
D/A Converters	10-bit x 1 ch

Interfaces	
USB Ports	1
USB Host	YES
USB Peripheral	YES
USB Remarks	USB2.0 Full Speed & USB OTG (On-The-Go) Support
CSIs	9 ch
SPIs	11 ch
UARTs	9 ch
I2Cs	11 ch
IEBus	1 ch

Operating Conditions	
Operating Voltage	2.7 to 3.6 V
Operating Temperature	-40 to 85 °C (Ambient Temperature)

[Back To Top](#)

## Order Information

Orderable Part No. 	Order	Production Status	Packing	Terminal material (Base)	Terminal material (Surface)	EU RoHS	RoHS-pdf (China-RoHS)
R5F56316CDFP#V0	Contact	Mass Production	Tray	Copper Alloy	Sn plated	Compliant	<a href="#">English</a>

[Back To Top](#)

## Package

Package	
Pins	100
Pin pitch	0.5 mm
Package Type	LFQFP
Body Size	14x14 mm <sup>2</sup>
Package drawing	<a href="#">plqp0100kb_a</a>
Mount pad drawing	<a href="#">fig0009e</a>
Mounting height	1.7 mm
Mass	0.6 g
Renesas code	PLQP0100KB-A (old: FP-100UV)
JEITA code	P-LFQFP100-14x14-0.50

[Back To Top](#)

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