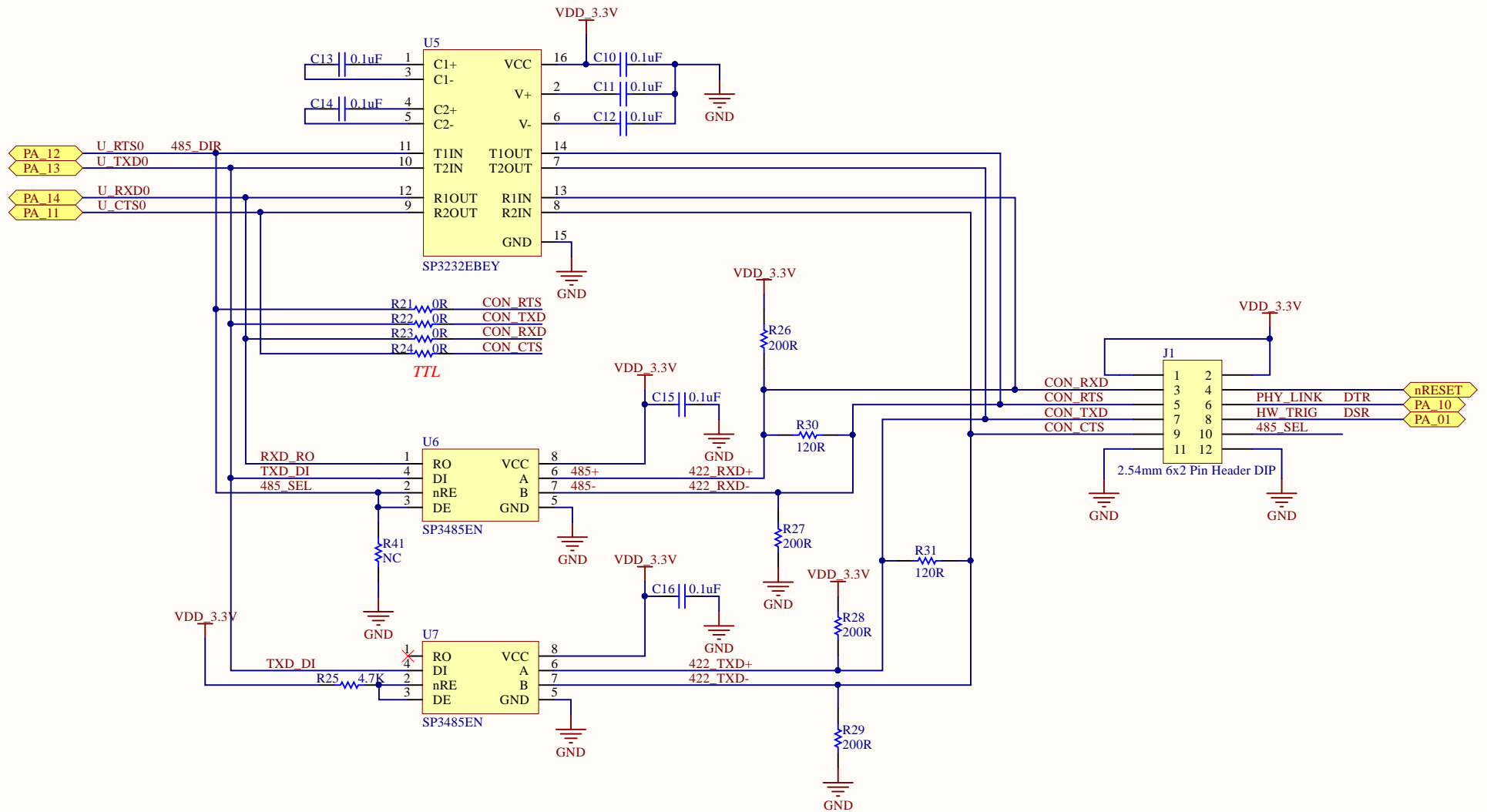
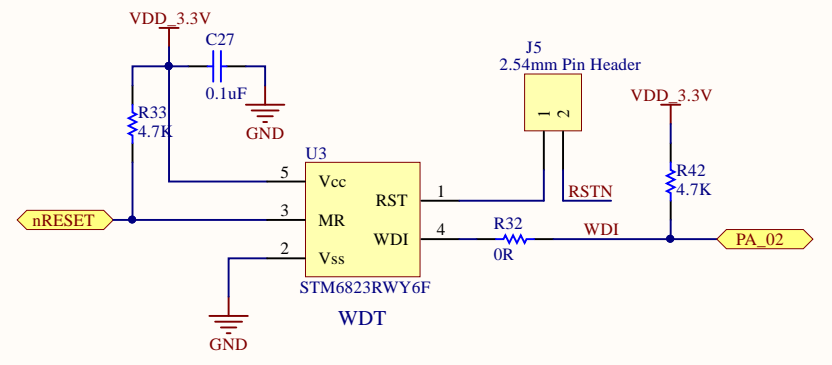
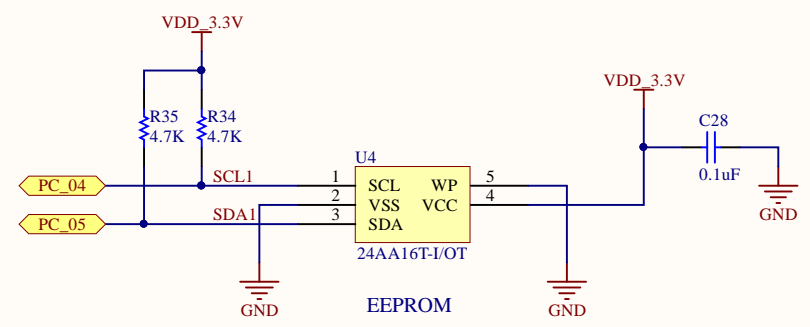
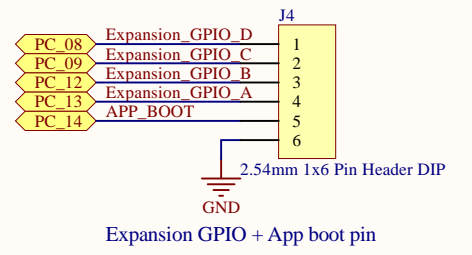
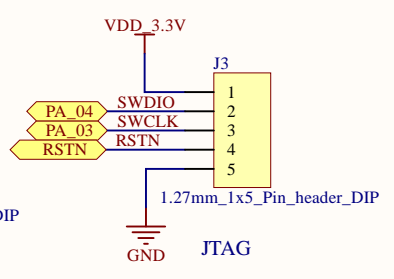
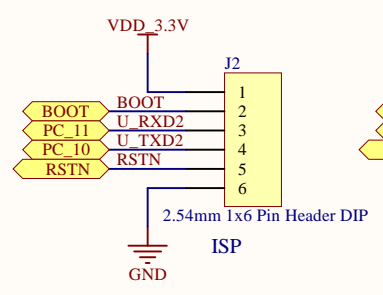
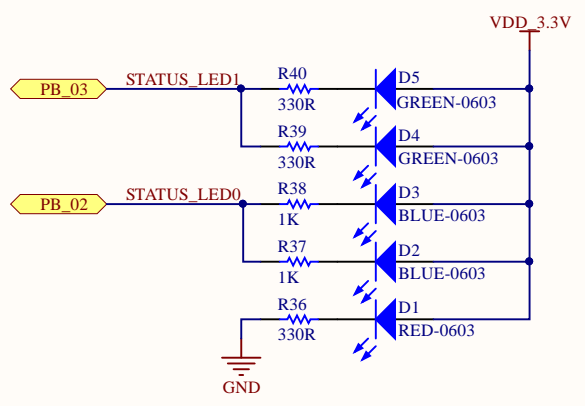
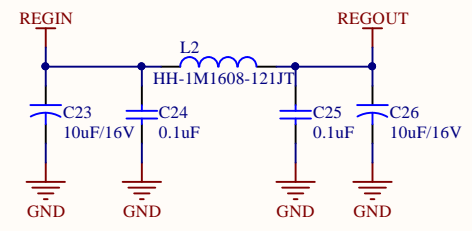
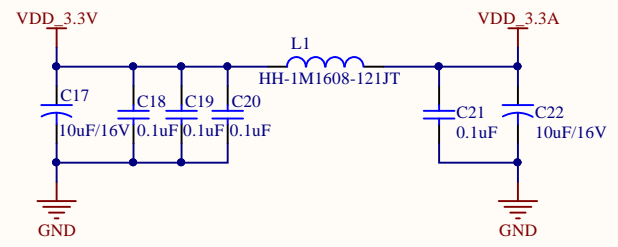


PA_09 (LED0) pin is PHY_LINK_STATUS(GPIO) check pin
 PA_10 is PHY_LINK_STATUS output pin

Title WIZ750SR		WIZnet Co., Ltd. 5F, Humax Village Hwangseaul-ro, Bundang-gu Seongnam-si, Gyeonggi-do South Korea		Cannot open file C:\Users\Public\Documents\Altium\AD14\Templates\IOP_IoT logo.png
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Title WIZ750SR		WIZnet Co., Ltd. 5F, Humax Village Hwangseaul-ro, Bundang-gu Seongnam-si, Gyeonggi-do South Korea		Cannot open file C:\Users\Public\Documents\Altium\AD14\Templates\IOP_IoT logo.png
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