





N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D T _C = +25°C
20V	$550 \text{ m}\Omega$ @ V _{GS} = 4.5V	0.54 mA

Features and Benefits

- Low On-Resistance: R_{DS(ON)}
- Low Gate Threshold Voltage
- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability
- PPAP Capable (Note 4)

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

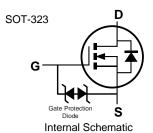
- Engine Management Systems
- DC-DC Converters
- Body Control Electronics

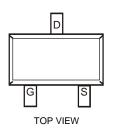
Mechanical Data

- Case: SOT-323
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 (§3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)









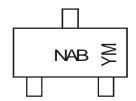
Ordering Information (Note 5)

Part Number	Case	Packaging		
DMN2004WKQ-7	SOT-323	3,000/Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



 $\begin{aligned} &\mathsf{NAB} = \mathsf{Product} \; \mathsf{Type} \; \mathsf{Marking} \; \mathsf{Code} \\ &\mathsf{YM} = \mathsf{Date} \; \mathsf{Code} \; \mathsf{Marking} \\ &\mathsf{Y} \; \mathsf{or} \; \overline{\mathsf{Y}} = \mathsf{Year} \; (\mathsf{ex:} \; \mathsf{D} = \mathsf{2016}) \\ &\mathsf{M} = \mathsf{Month} \; (\mathsf{ex:} \; \mathsf{9} = \mathsf{September}) \end{aligned}$

Date Code Key

 Jaio Oddo Hoj												
Year	201	6	2017		2018	20	19	2020		2021	2	2022
Code	D		Е		F	(3	Н		l		J
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Chara	acteristic		Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	20	V
Gate-Source Voltage			V_{GSS}	±8	V
Drain Current (Note 6)	Steady State	$T_A = +25$ °C $T_A = +85$ °C	I _D	540 390	mA
Pulsed Drain Current (Note 7)			I _{DM}	1.5	A

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 6)	P_{D}	200	mW
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

6. Device mounted on FR-4 PCB. Notes:

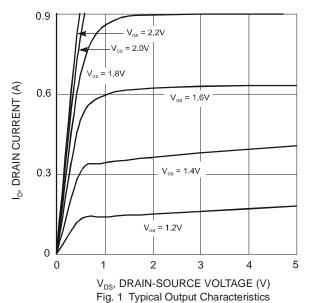
7. Pulse width $\leq 10 \mu S$, Duty Cycle $\leq 1\%$.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 8)								
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_{D} = 10\mu A$		
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	$V_{DS} = 16V, V_{GS} = 0V$		
Gate-Source Leakage	I _{GSS}	_	_	±1	μA	$V_{GS} = \pm 4.5V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 8)								
Gate Threshold Voltage	$V_{GS(th)}$	0.5	_	1.0	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$		
			0.4	0.55 0.70 0.9	Ω	$V_{GS} = 4.5V, I_D = 540mA$		
Static Drain-Source On-Resistance	R _{DS (ON)}	_	0.5 0.7			$V_{GS} = 2.5V, I_D = 500mA$		
	, ,					$V_{GS} = 1.8V, I_D = 350mA$		
Forward Transfer Admittance	Y _{fs}	200	_	_	ms	$V_{DS} = 10V, I_D = 0.2A$		
Diode Forward Voltage (Note 8)	V _{SD}	0.5	_	1.4	V	$V_{GS} = 0V, I_S = 115mA$		
DYNAMIC CHARACTERISTICS(Note 9)								
Input Capacitance	C _{iss}	_	_	150	pF	\/ 40\/\\ 0\/		
Output Capacitance	Coss	_	_	25	pF	$V_{DS} = 16V, V_{GS} = 0V$ - f = 1.0MHz		
Reverse Transfer Capacitance	C _{rss}	_	_	20	pF	1 - 1.0IVII IZ		

- Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.





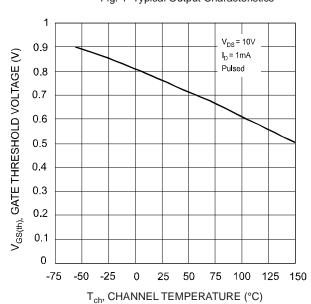


Fig. 3 Gate Threshold Voltage vs. Channel Temperature

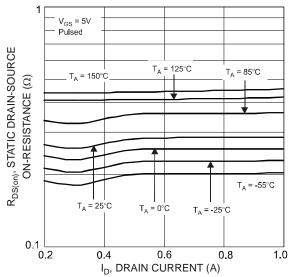


Fig. 5 Static Drain-Source On-Resistance vs. Drain Current

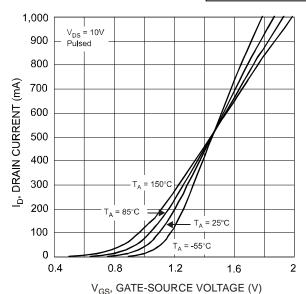


Fig. 2 Reverse Drain Current vs. Source-Drain Voltage

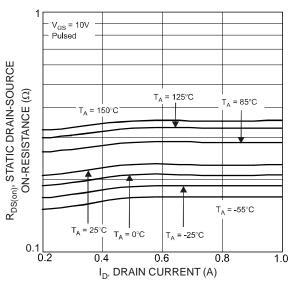


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current

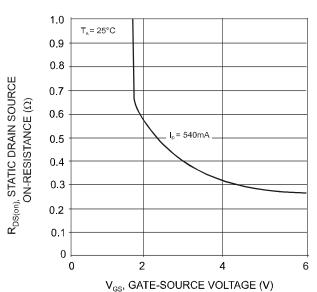


Fig. 6 Static Drain-Source, On-Resistance vs. Gate-Source Voltage



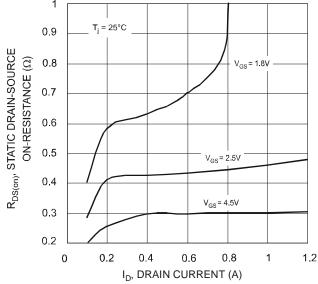
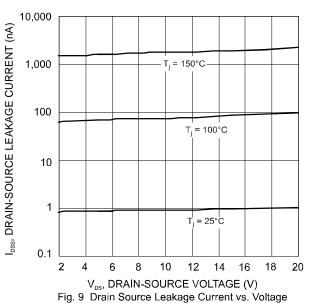


Fig. 7 On-Resistance vs. Drain Current and Gate Voltage



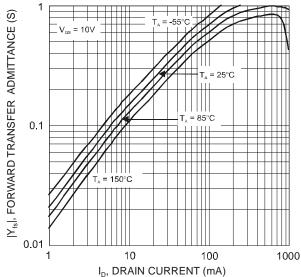


Fig. 11 Forward Transfer Admittance vs. Drain Current

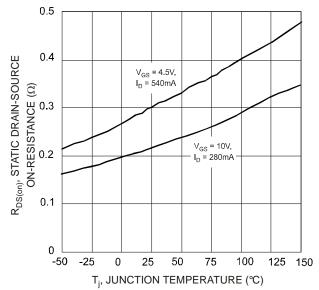


Fig. 8 Static Drain-Source, On-Resistance vs. Temperature

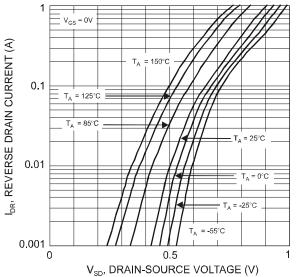
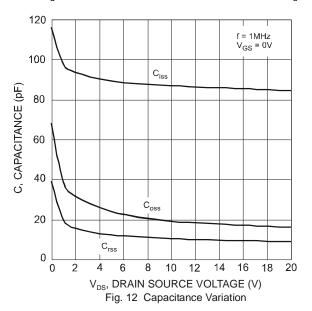


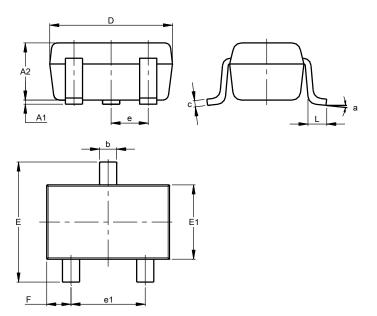
Fig. 10 Reverse Drain Current vs. Source-Drain Voltage





Package Outline Dimensions

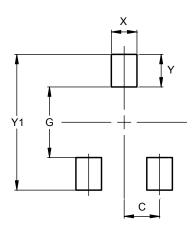
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT323							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.25	0.40	0.30				
С	0.10	0.18	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	C	.650 B	SC				
e1	1.20	1.40	1.30				
F	0.375	0.475	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value
Dimensions	(in mm)
С	0.650
G	1.300
Х	0.470
Y	0.600
Y1	2 500



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