

### NTE1902 Integrated Circuit 3 Terminal Positive Voltage Regulator 9V, 100mA

#### Features:

- Output Current up to 100mA
- No External Components
- Internal Thermal Overload Protection
- Internal Short–Circuit Current Limiting
- Output Voltage Tolerances of ±5% over the Temperature Range

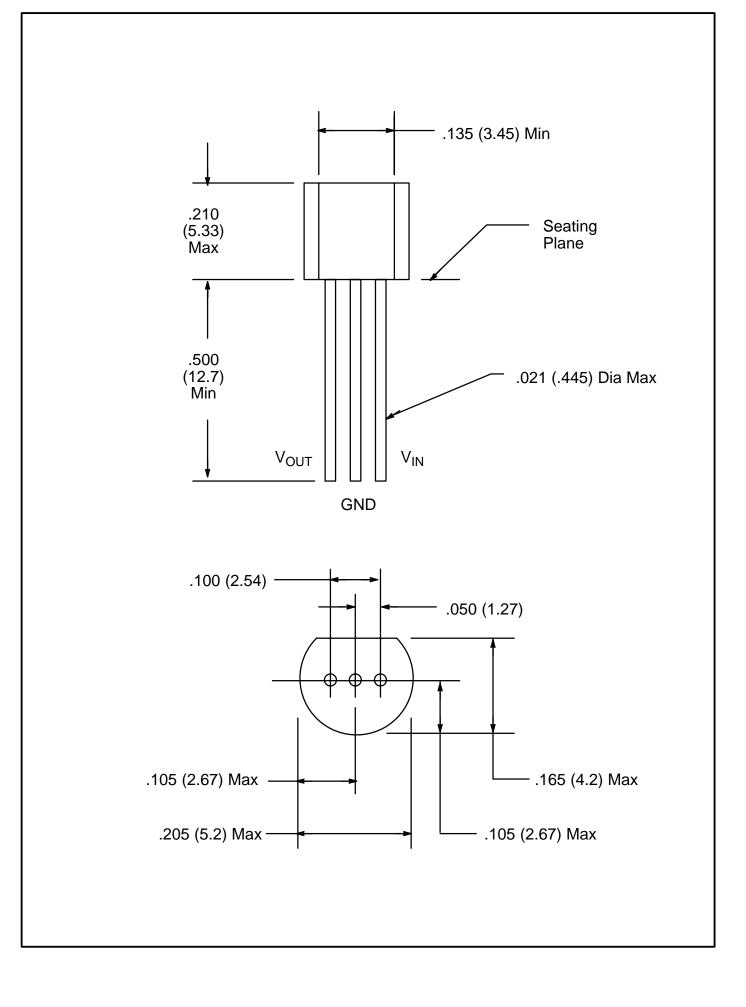
#### Absolute Maximum Ratings:

Input Voltage, V <sub>IN</sub>	
Internal Power Dissipation, P <sub>D</sub>	. Internally Limited
Operating Junction Temperature Range, T <sub>J</sub>	0° to +125°C
Storage Temperature Range, T <sub>stg</sub>	–55° to +150°C
Lead Temperature (During Soldering, 10sec), T <sub>L</sub>	+260°C

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Parameter	Symbol	Test Conditions			Тур	Max	Unit
Output Voltage	V <sub>O</sub>	$T_J = +25^{\circ}C$		8.64	9.00	9.36	V
		$1mA \leq I_O \leq 70mA, \ 11.5V \leq V_{IN} \leq 24V$		8.55	9.00	9.45	V
Line Regulation	Reg <sub>Line</sub>	$T_J = +25^{\circ}C$	$11.5V \le V_{IN} \le 24V$	-	90	200	mV
			$13V \le V_{IN} \le 24V$	-	100	150	mV
Load Regulation	Reg <sub>Load</sub>	$T_J = +25^{\circ}C$	$1mA \le I_O \le 100mA$	-	20	90	mV
			$1mA \le I_O \le 40mA$	-	10	45	mV
Quiescent Current	I <sub>B</sub>			-	2.1	5.5	mA
Quiescent Current Change	I <sub>B</sub>	With line, $11.5V \le V_{IN} \le 24V$ With load, $1mA \le I_O \le 40mA$		-	-	1.5	mA
				-	-	0.1	mA
Output Noise Voltage	V <sub>N</sub>	$T_A = +25^{\circ}C$ , f = 10Hz to 10kHz		-	70	-	μV
Temperature Coefficient of $V_{OUT}$		I <sub>OUT</sub> = 5mA		-	-0.9	-	mV/°C
Ripple Rejection	RR	$T_J$ = +25°C, 15V ≤ V <sub>IN</sub> ≤ 25V, f = 120Hz		38	44	-	dB
Dropout Voltage	V <sub>DO</sub>	$T_J = +25^{\circ}C$		-	1.4	I	V
Peak Output/Short Circuit Current	I <sub>pk</sub> /I <sub>OS</sub>	$T_J = +25^{\circ}C$			140	I	mA

Note 1. The maximum steady state usable output current and input voltage ar very dependent on the heat sink and/or lead length of the package. The data above represents pulse test conditions with junction temperatures as indicated at the initiation of the test.



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