| SP | FC | IFI | CA ₁ | ΓΙΩ | NS |
|----|----|-----|-----------------|-----|-----|
| J | | | | | 140 |

CUSTOMER . PTC

SAMPLE CODE · SH240320T-063-L-Q

MASS PRODUCTION CODE . PH240320T-063-L-Q

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 005

DRAWING NO. (Ver.) . LMD-PH240320T-063-L-Q (Ver:002)

PACKAGING NO. (Ver.) · PKG-PH240320T-063-L-Q (Ver:002)

Customer Approved

Date:

| Approved | Checked | Designer |
|-----------------|-----------------|-------------------|
| 廖志豪 Rex Liao | 廖志豪 Rex Liao | 張慶源 Yuan Chang |
| Nox Elac | Nox Lido | POWERT |

2014.03.31

- Preliminary specification for design input
- Specification for sample approval

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History of Version

| Date (mm / dd / yyyy) | Ver. | Edi. | Description | Page | Design by |
|--------------------------|------|------|------------------------------------------------|---------------|-----------|
| 2012/08/06 | 01 | 001 | New Drawing | - | Yuan |
| 2012/11/01 | 01 | 002 | New Sample | - | Yuan |
| 2013/07/08 | 01 | 003 | Modify Optical Characteristics | 6 | Yuan |
| 2013/12/27 | 01 | 004 | Modify LCM Packaging Specifications | Appendix | Yuan |
| 2014/03/27 | 01 | 005 | Modify Optical Characteristics Modify Drawing | 6 Appendix | Yuan |
| | | | | | |
| | | | | <i>→</i> | |
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Appendix: LCM Drawing

LCM Packaging Specifications

Note: For detailed information please refer to IC data sheet:

Primacy(TFT LCD): ILITEK: ILI9341



1. SPECIFICATIONS

1.1 Features

Main LCD panel

| Item | Standard Value |
|-----------------------------|--------------------------------------------------------------|
| Display Type | 240(R · G · B) * 320 Dots |
| LCD Type | Normally white , Transmissive type |
| Screen size(inch) | 2.8 inch |
| Viewing Direction | 12 O'clock |
| Color configuration | RGB-Strip |
| Backlight | LED Backlight |
| Interface | 16-bit 80-system I/F |
| Other(controller/driver IC) | ILITEK: ILI9341 |
| | THIS PRODUCT CONFORMS THE ROHS OF PTC |
| ROHS | Detail information please refer website : |
| | http://www.powertip.com.tw/news.php?area_id_view=1085560481/ |

1.2 Mechanical Specifications

| Item | Standard Value | Unit |
|-------------------|----------------------------------|------|
| Outline Dimension | 50.0(W) * 69.2 (L) * 3.05 (H)max | mm |

LCD panel

| Item | Standard Value | Unit |
|-------------|---------------------|------|
| Active Area | 43.2 (W) * 57.6 (L) | mm |



1.3 Absolute Maximum Ratings

Module

| Item | Symbol | Condition | Min. | Max. | Unit |
|--------------------------------|-----------------|-----------|------|---------|------|
| Custom Davier Custolic Valtage | VCC - | | -0.3 | +4.6 | V |
| System Power Supply Voltage | VGH ~ VGL - | | -0.3 | +32 | V |
| Input Voltage | VIN | - | -0.3 | VCC+0.3 | V |
| Operating Temperature | T_OP | - | -20 | +70 | °C |
| Storage Temperature | T _{ST} | - | -30 | +80 | °C |
| Storage Humidity | H_D | Ta 40 °C | 20 | 90 | %RH |

1.4 DC Electrical Characteristics

Module GND = 0V, Ta = $25^{\circ}C$

| | | | | | • | |
|-----------------------|-----------------|------------------------------------------|---------|------|---------|------|
| Item | Symbol | Condition | Min. | Тур. | Max. | Unit |
| Power Supply Voltage1 | VCC | - | - | 2.8 | - | V |
| Input High Voltage | V _{IH} | - | 0.7 VCC | - | VCC | V |
| Input Low Voltage | V _{IL} | - | GND | - | 0.3 VCC | V |
| Output High Voltage | V _{OH} | IOH=-0.1mA | 0.8*VDD | - | VDD | V |
| Output Low Voltage | V _{OL} | IOL=0.1mA | GND | - | 0.2*VDD | V |
| Supply Current | ICC | VCC = 2.8V Pattern=full display *1 | - | 9 | 12 | mA |

Note1:Maximum current display



1.5 Optical Characteristics

TFT LCD Module

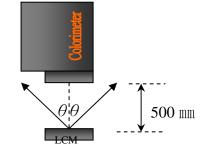
VCC = 2.8V, Ta=25°C

| Item | | Symbol | Condition | Min. | Тур. | Max. | unit | - |
|----------------------------|---------|--------|---------------------------|------|------|------|-------------------|-------|
| Response tin | ne | Tr+ Tf | Ta = 25°C θX, θY = 0° | 1 | 31 | 47 | ms | Note2 |
| | Тор | θΥ+ | | - | 60 | - | | |
| Viewing angle | Bottom | θΥ- | CR ≥ 10 | - | 60 | - | Dog | Note4 |
| viewing angle | Left | θX- | CR 2 10 | - | 60 | - | Deg. | Note4 |
| | Right | θX+ | | - | 60 | - | | |
| Contrast rati | 0 | CR | Ta = 25°C θX , θY = 0° | 500 | 600 | 1 | 1 | Note3 |
| | White | X | | 0.25 | 0.30 | 0.35 | | |
| | vvriite | Υ | | 0.28 | 0.33 | 0.38 | | |
| 0 1 (0)5 | | Х | | 0.58 | 0.63 | 0.68 | | |
| Color of CIE Coordinate | | | Υ | | 0.3 | 0.35 | 0.4 | |
| (With B/L) | Croon | Х | - | 0.29 | 0.34 | 0.39 | _ | |
| (111.11.27.2) | Green | Υ | | 0.56 | 0.61 | 0.66 | | |
| | Blue | Х | | 0.09 | 0.14 | 0.19 | | Note1 |
| | Diue | Υ | | 0.02 | 0.07 | 0.12 | | |
| Average Brighti | ness | | | | | | | |
| Pattern=white display | | IV | IF=80 mA | 230 | 255 | - | cd/m ² | |
| (With B/L) *1 | | | | | | | | |
| Uniformity (With B/L)*2 | | △B | IF=80 mA | 70 | - | - | % | |

Note 1:

- *1 : △B=B(min) / B(max) * 100%
- *2 : Measurement Condition for Optical Characteristics:
 - a: Environment: 25 ±5 / 60±20%R.H, no wind, dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: $500 \pm 50 \text{ mm}$, $(\theta = 0^{\circ})$
 - c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
 - d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%





Colorimeter=BM-7 fast

To be measured at the center area of panel with a viewing cone of 1° by Topcon

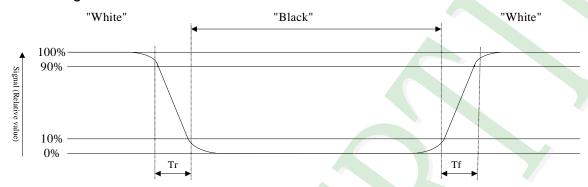


luminance meter BM-7, after 10 minutes operation (module)

Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.

Refer to figure as below:



Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

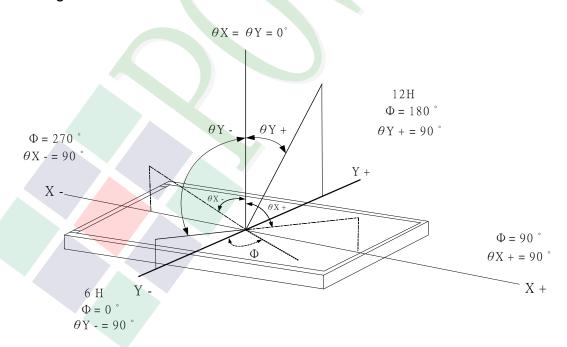
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

Note4: Definition of viewing angle:

Refer to figure as below:





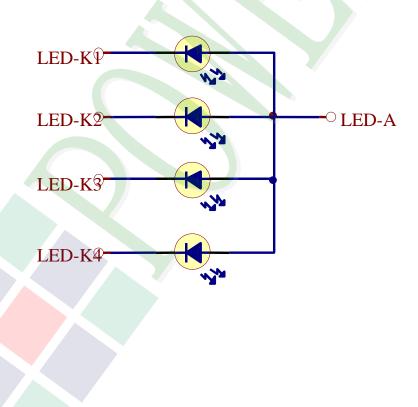
1.6 Backlight Characteristics

Maximum Ratings

| Item | Symbol | Conditions | Min. | Max. | Unit |
|-------------------|--------|------------|------|-------|------|
| Power Dissipation | PD | Ta =25°ℂ | _ | 0.288 | W |

Electrical / Optical Characteristics

| Item | Symbol | Conditions | Min. | Тур. | Max. | Unit |
|----------------------------------|--------|------------|-------|------|------|-------------------|
| Forward Voltage | VF | | 2.8 | _ | 3.6 | V |
| Average Brightness (without LCD) | IV | IF= 80 mA | 5000 | 5500 | - | cd/m ² |
| CIE Color Coordinate | X | | 0.26 | 0.28 | 0.33 | |
| (Without LCD) | Y | | 0.26 | 0.28 | 0.33 | 1 |
| Color | | | White | | | |



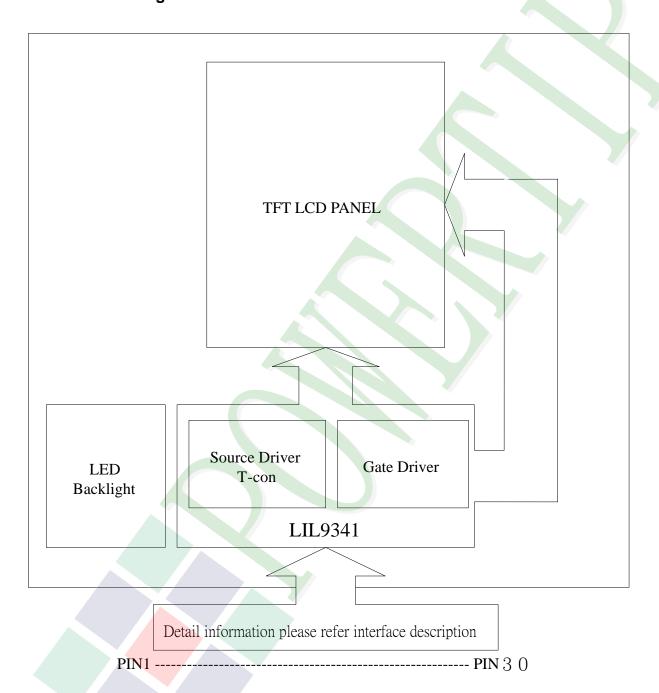


2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





2.2 Interface Pin Description

| Pin No. | Symbol | Function |
|---------|---------|------------------------------------------------------------------------------|
| 1 | LEDK1-4 | Power supply for LED Backlight Cathode input |
| 2 | LEDA | Power supply for LED Backlight Anode input |
| 3 | GND | Signal ground.(0V) |
| 4 | RESET | Reset input pin for TFT LCD. When RESET is "L", initialization is executed. |
| 5 | DB17 | |
| 6 | DB16 | |
| 7 | DB15 | |
| 8 | DB14 | |
| 9 | DB13 | |
| 10 | DB12 | |
| 11 | DB11 | |
| 12 | DB10 | Bi-directional data bus |
| 13 | DB8 | Di-uliectional data bus |
| 14 | DB7 | |
| 15 | DB6 | |
| 16 | DB5 | |
| 17 | DB4 | |
| 18 | DB3 | |
| 19 | DB2 | |
| 20 | DB1 | |
| 21 | RD | Read signal input , active at Low. |
| 22 | WR/SCL | Write signal input , active at Low. |
| 23 | RS | When RS = 0: Command. When RS = 1: Display data. |
| 24 | CS | Chip select signal , Active at "L" |
| 25 | XR/X+ | NC |

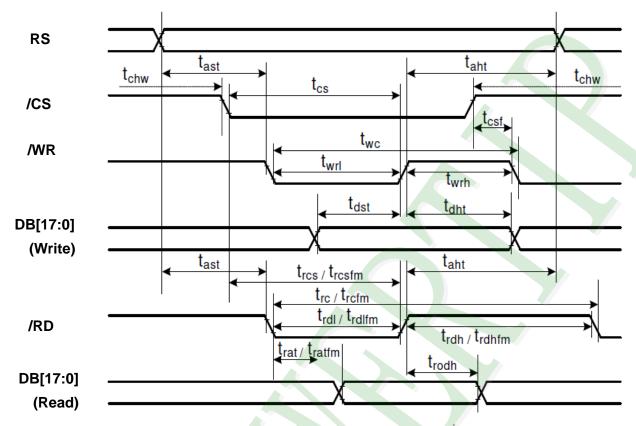


| Pin No. | Symbol | Function |
|---------|----------|----------------------------------------------|
| 26 | YD/Y- | |
| 27 | XL/X- | NC |
| 28 | YU/Y+ | |
| 29 | GND | Signal ground.(0V) |
| 30 | 2.8 /VCC | Power supply for the internal logic circuit. |





2.3 Timing Characteristics

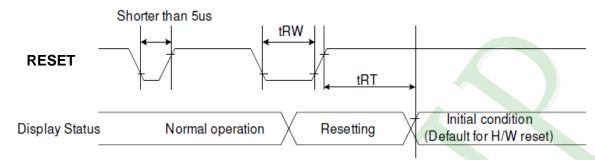


| Signal | Symbol | Parameter | min | max | Unit | Description |
|----------------------|--------|------------------------------------|-----|-----|------|---------------------|
| Be | tast | Address setup time | 0 | - | ns | |
| RS | taht | Address hold time (Write/Read) | 0 | - | ns | |
| | tchw | CSX "H" pulse width | 0 | - | ns | |
| | tcs | Chip Select setup time (Write) | 15 | - | ns | |
| /cs | trcs | Chip Select setup time (Read ID) | 45 | - | ns | |
| | trcsfm | Chip Select setup time (Read FM) | 355 | - | ns | |
| | tcsf | Chip Select Wait time (Write/Read) | 10 | - | ns | |
| | twc | Write cycle | 66 | - | ns | |
| /WR | twrh | Write Control pulse H duration | 15 | - | ns | |
| | twrl | Write Control pulse L duration | 15 | - | ns | |
| | trcfm | Read Cycle (FM) | 450 | - | ns | |
| /RD(FM) | trdhfm | Read Control H duration (FM) | 90 | - | ns | |
| | trdlfm | Read Control L duration (FM) | 355 | - | ns | |
| | trc | Read cycle (ID) | 160 | - | ns | |
| /RD(ID) | trdh | Read Control pulse H duration | 90 | - | ns | |
| | trdl | Read Control pulse L duration | 45 | - | ns | |
| DB[47.0] | tdst | Write data setup time | 10 | - | ns | |
| DB[17:0] DB[17:0] | tdht | Write data hold time | 10 | - | ns | For maximum CL=30pF |
| DB[8:0] | trat | Read access time | - | 40 | ns | For minimum CL=8pF |
| DB[7:0] | tratfm | Read access time | - | 340 | ns | TOT MINIMUM OL=OPI |
| | trod | Read output disable time | 20 | 80 | ns | |

Note: Ta = -30 to 70 $^{\circ}$ C, VCC=1.65V to 3.3V, VCI=2.5V to 3.3V, GND=0V



Reset Timing



| Signal | Symbol | Parameter | Min | Max | Unit |
|--------|--------------------|----------------------|-----|---------------------|------|
| RESX | tRW | Reset pulse duration | 10 | 7 | uS |
| | tRT Reset cancel — | | | 5 (note 1,5) | mS |
| | thi | neset cancer | | 120 (note 1,6,7) | mS |

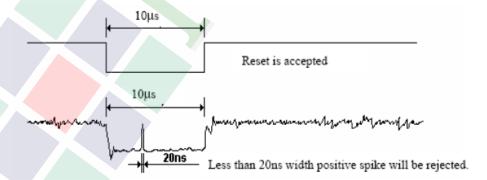
Note 1: The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NV memory to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.

Note 2: Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below: -

| RESX Pulse | Action | |
|----------------------|----------------|--|
| Shorter than 5us | Reset Rejected | |
| Longer than 10us | Reset | |
| Between 5us and 10us | Reset starts | |

Note 3: During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In -mode.) And then return to Default condition for Hardware Reset.

Note 4: Spike Rejection also applies during a valid reset pulse as shown below:



- Note 5: When Reset applied during Sleep In Mode.
- Note 6: When Reset applied during Sleep Out Mode.
- Note 7: It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.



2.4 Reference Initial code

MOV ADDRH,#00H MOV ADDRL,#CBH **CALL** WRITE_COMMAND MOV ADDRL,#39H **CALL** WRITE_DATA MOV ADDRL,#2CH **CALL** WRITE_DATA MOV ADDRL,#00H **CALL** WRITE_DATA MOV ADDRL,#34H **CALL** WRITE DATA MOV ADDRL,#02H **CALL** WRITE DATA

MOV ADDRL,#CFH **CALL** WRITE_COMMAND MOV ADDRL,#00H **CALL** WRITE_DATA MOV ADDRL,#C1H **CALL** WRITE_DATA MOV ADDRL,#30H **CALL** WRITE DATA

MOV ADDRL,#E8H **CALL** WRITE_COMMAND MOV ADDRL,#85H **CALL** WRITE_DATA MOV ADDRL,#00H **CALL** WRITE DATA MOV ADDRL,#78H **CALL** WRITE_DATA MOV ADDRH.#00H MOV ADDRL,#EAH **CALL** WRITE COMMAND MOV ADDRL,#00H **CALL** WRITE DATA MOV ADDRL,#00H **CALL** WRITE_DATA

MOV ADDRH,#00H MOV ADDRL,#EDH **CALL** WRITE COMMAND MOV ADDRL.#64H **CALL** WRITE DATA MOV ADDRL,#03H **CALL** WRITE DATA MOV ADDRL,#12H **CALL** WRITE_DATA MOV ADDRL,#81H **CALL** WRITE_DATA

MOV ADDRH,#00H
MOV ADDRL,#F7H
CALL WRITE_COMMAND
MOV ADDRL,#20H
CALL WRITE DATA



MOV ADDRH,#00H
MOV ADDRL,#C0H
CALL WRITE_COMMAND
MOV ADDRL,#23H
CALL WRITE_DATA

MOV ADDRH,#00H
MOV ADDRL,#C1H
CALL WRITE_COMMAND
MOV ADDRL,#10H
CALL WRITE_DATA

MOV ADDRH,#00H
MOV ADDRL,#C5H
CALL WRITE_COMMAND
MOV ADDRL,#2BH
CALL WRITE_DATA
MOV ADDRL,#2BH
CALL WRITE_DATA

MOV ADDRH,#00H
MOV ADDRL,#C7H
CALL WRITE_COMMAND
MOV ADDRL,#C0H
CALL WRITE_DATA

MOV ADDRH,#00H
MOV ADDRL,#36H
CALL WRITE_COMMAND
MOV ADDRL,#40H
CALL WRITE_DATA

MOV ADDRH,#00H
MOV ADDRL,#B1H
CALL WRITE_COMMAND
MOV ADDRL,#00H
CALL WRITE_DATA
MOV ADDRL,#1BH
CALL WRITE_DATA

MOV ADDRH,#00H
MOV ADDRL,#B6H
CALL WRITE_COMMAND
MOV ADDRL,#0AH
CALL WRITE_DATA
MOV ADDRL,#02H
CALL WRITE_DATA

MOV ADDRH,#00H
MOV ADDRL,#F2H
CALL WRITE_COMMAND
MOV ADDRL,#00H
CALL WRITE_DATA

MOV ADDRH,#00H MOV ADDRL,#26H CALL WRITE_COMMAND MOV ADDRL,#01H



| | Unditi. |
|------|--------------------------|
| | WRITE_DATA |
| MOV | ADDRH,#00H |
| | ADDRL,#3AH |
| | WRITE_COMMAND |
| | ADDRL,#55H |
| | WRITE_DATA |
| | |
| | ADDRH,#00H |
| | ADDRL,#21H |
| CALL | WRITE_COMMAND |
| MOV | ADDRH,#00H |
| | ADDRL,#E0H |
| | WRITE_COMMAND |
| | ADDRL,#0FH |
| CALL | WRITE_DATA |
| MOV | ADDRL,#31H |
| | WRITE_DATA |
| | ADDRL,#2BH |
| | WRITE_DATA |
| | ADDRL,#0CH |
| | WRITE_DATA |
| | ADDRL,#0EH |
| | WRITE_DATA |
| | ADDRL,#08H |
| | WRITE_DATA |
| | ADDRL,#4EH |
| | WRITE_DATA |
| | ADDRL,#F1H WRITE_DATA |
| | ADDRL,#37H |
| | WRITE_DATA |
| | ADDRL,#07H |
| | WRITE DATA |
| | ADDRL,#10H |
| | WRITE_DATA |
| | ADDRL,#03H |
| CALL | WRITE_DATA |
| MOV | ADDRL,#0EH |
| | WRITE_DATA |
| | ADDRL,#09H |
| | WRITE_DATA |
| | ADDRL,#00H |
| CALL | WRITE_DATA |
| | ADDRH,#00H |
| | ADDRL,#E1H |
| | WRITE_COMMAND |
| | ADDRL,#00H |
| | WRITE_DATA |
| | ADDRL,#0EH |
| | WRITE_DATA |
| | ADDRL,#14H |
| | WRITE_DATA |
| | ADDRL,#03H WRITE_DATA |
| MOV | WKIIE_DATA ADDDI #11H |

MOV

ADDRL,#11H



CALL WRITE_DATA MOV ADDRL,#07H **CALL** WRITE_DATA MOV ADDRL,#31H **CALL** WRITE_DATA MOV ADDRL,#C1H **CALL** WRITE_DATA MOV ADDRL,#48H **CALL** WRITE_DATA MOV ADDRL,#08H **CALL** WRITE_DATA MOV ADDRL,#0FH **CALL** WRITE_DATA MOV ADDRL,#0CH **CALL** WRITE_DATA MOV ADDRL,#31H **CALL** WRITE_DATA MOV ADDRL,#36H **CALL** WRITE_DATA MOV ADDRL,#0FH **CALL** WRITE_DATA

MOV ADDRH,#00H MOV ADDRL,#11H **CALL** WRITE_COMMAND

CALL DELAY CALL DELAY CALL DELAY CALL DELAY

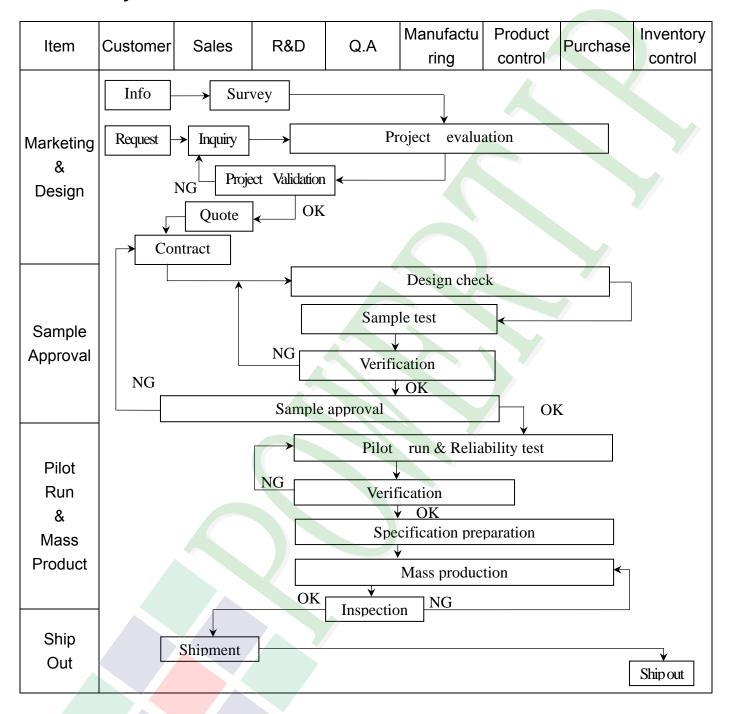
MOV ADDRH,#00H MOV ADDRL,#29H

CALL WRITE_COMMAND

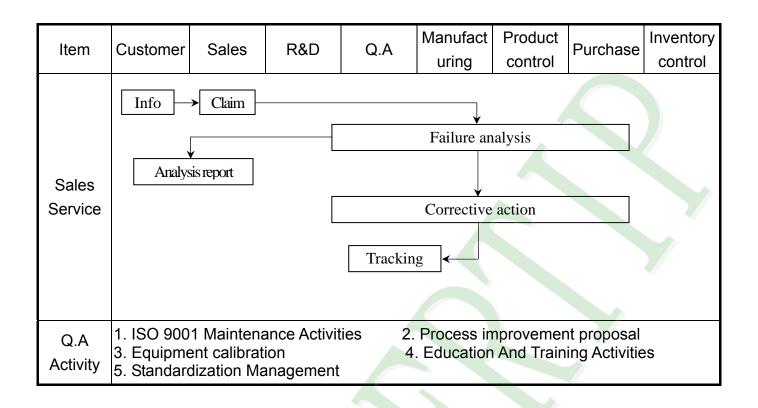


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

◆Scope : The document shall be applied to TFT-LCD Module for less than 3, 5" (Ver.B01).

◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

◆Equipment: Gauge、MIL-STD、Powertip Tester、Sample

◆Defect Level: Major Defect AQL: 0, 4 ; Minor Defect AQL: 1, 5

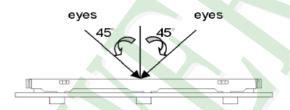
♦OUT Going Defect Level: Sampling.

◆Standard of the product appearance test:

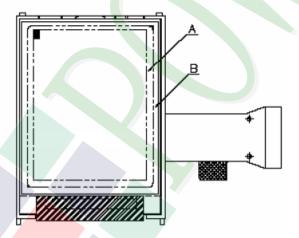
a. Manner of appearance test:

(1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.

(2). The test direction is base on about around 45° of vertical line.



(3). Definition of area.



A area: viewing area

B area : Outside of viewing area

(4). Standard of inspection: (Unit: mm)



◆Specification For TFT-LCD Module Less Than 3, 5":

| NO | Item | | | Criteri | on | Level | |
|----|--------------------|--------------------------------------------------------------------|---------------------------|-------------------------|-------------------------------|-------|--|
| | | 1. 1The part number is inconsistent with work order of production. | | | | | |
| 01 | Product condition | 1. 2 Mi | 1. 2 Mixed product types. | | | | |
| | | 1. 3 Ass | sembled | in inverse direction. | | Major | |
| 02 | Quantity | 2. 1The | e quantit | y is inconsistent witl | h work order of production. | Major | |
| 03 | Outline dimension | | oduct dii agram. | mension and structu | are must conform to structure | Major | |
| | | 4. 1 Mi | issing line | e character and icon | | Major | |
| | | 4. 2 No function or no display. | | | | | |
| 04 | Electrical Testing | 4. 3 Display malfunction. | | | | | |
| | | 4. 4 LCD viewing angle defect. | | | | Major | |
| | | 4. 5 Cu | irrent co | nsumption exceeds p | product specifications. | Major | |
| | | , | | | | | |
| | | | | Item | Acceptance (Q'ty) | | |
| | Dot defect | | | Bright Dot | ≦ 2 | | |
| | 2000000 | | Dot | Dark Dot | ≦ 3 | | |
| ٥٦ | (Bright dot \ | | Defect | Joint Dot | ≦ 2 | | |
| 05 | Dark dot) | | | Total | ≦ 3 | Minor | |
| | | 5. 1 Inspection pattern: full white, full black, Red, Green and | | | | | |
| | On -display | blue screens. | | | | | |
| | | 5. 2 It i | is defined | l as dot defect if defe | ect area $>1/2$ dot. | | |
| | | 5. 3 Th | e distanc | e between two dot d | lefect ≧5 mm. | | |



◆Specification For TFT-LCD Module Less Than 3.5″:

| NO | Item | Criterion | | | | |
|-----|-----------------------------------|---------------------------------------------|--------------|------------|----------|--|
| | | 6. 1 Round type (Non-display or display) : | | | | |
| | | Dimension | Acceptance | e (Q'ty) | | |
| | Disabas salita | (diameter ÷ Φ) | A area | B area | | |
| | Black or white dot \ scratch \ | $\Phi \le 0.15$ | Ignore | | | |
| | contamination | $0.15 < \Phi \leq 0.20$ | 2 | | | |
| | Round type | $0.20 < \Phi \leq 0.30$ | 2 | Ignore | | |
| | → _X | $\Phi > 0.30$ | 0 | | | |
| 06 | <u>Y</u> | Total | 3 | | Minor | |
| | $\Phi = (x+y)/2$ | 6. 2 Line type(Non-display or o | display) : | | 1,222.01 | |
| | Line type | Dimension | Accepta | nce (Q'ty) | | |
| | ∠ | Length (L) Width (W) | A area | B area | | |
| | | W ≤ 0 | .03 Ignore | | | |
| | → _L | $L \le 5.0$ $0.03 < W \le 0.$ | .05 3 | | | |
| | | W >0 | .05 As round | Ignore | | |
| | | Total | 3 | | | |
| | | | | | | |
| | | Dimension (diameter : Φ) | Acceptance | | | |
| | | | A area | B area | | |
| 0.7 | Polarizer | $\Phi \leq 0.20$ | Ignore | | 3.51 | |
| 07 | Bubble | $0.20 < \Phi \leq 0.50$ | 3 | Ignore | Minor | |
| | | $\Phi > 0.50$ | 0 | Ignore | | |
| | | Total | 3 | | | |



◆Specification For TFT-LCD Module Less Than 3.5″:

| NO | Item | Criterion | | Level |
|----|--------------------|----------------------------------------------------------------|-----------------------------------------------------------------------|-------|
| | | Z : The thickness of crack | Y : The width of crack. W : terminal length a : LCD side length | |
| | | 8. 1 General glass chip: 8. 1. 1 Chip on panel surface and cra | ack between panels: | |
| | | Y Z | Z Y | |
| 08 | The crack of glass | SP Y | SP [NG] | Minor |
| | | Seal width | Y | |
| | | X Y | z | |
| | | ≦ a Crack can't enter viewing area | ≦1/2 t | |
| | | ≤ a Crack can't exceed the half of SP width. | 1/2 t < Z ≤2 t | |
| 4 | | | | |



◆Specification For TFT-LCD Module Less Than 3.5″:

| NO | Item | Criterion (| Level |
|----|--------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------|
| | | Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 8. 1. 2 Corner crack: | |
| | | X Y Z $\leq 1/5 \text{ a}$ Crack can't enter $Z \leq 1/2 \text{ t}$ | |
| 08 | The crack of glass | viewing area Solution $2 = 1/2 t$ Viewing area Solution $2 = 1/2 t$ Solution $2 = 1$ | Minor |
| | g | 8.2 Protrusion over terminal: | _ |
| | | 8. 2. 1 Chip on electrode pad: X X X X X X X X X X X X X X X X X X | |
| | | X Y Z | |
| | | Front $\leq a$ $\leq 1/2 W$ $\leq t$ | |
| | | Back \leq a \leq W \leq 1/2 t | |



◆Specification For TFT-LCD Module Less Than 3, 5":

| Symbols: X: The length of crack Z: The thickness of crack T: The thickness of glass 8. 2. 2 Non-conductive portion: X Y Z X Y Z Sample of crack W: terminal length a: LCD side length Solution W X Y Z Sample of crack W: terminal length a: LCD side length Solution W X Y Z Sample of crack W: terminal length a: LCD side length Solution Solution W X Y Z Sample of crack W: terminal length a: LCD side length Solution Solution W Pitch X Y Z Sample of crack W: terminal length a: LCD side length Solution Solution W W X Y Z Sample of crack W: terminal length a: LCD side length Solution Solution W Pitch X Y Z Sample of crack W: terminal length a: LCD side length Solution W W X Y Z Sample of crack W: terminal length a: LCD side length Solution Solution W W Pitch X Y Z Sample of crack W: terminal length a: LCD side length Solution Solution Solution W X Y Z Sample of crack W: terminal length a: LCD side length Solution | Minor |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------|



◆Specification For TFT-LCD Module Less Than 3.5":

| NO | Item | Criterion | Level |
|----|-----------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------|-------|
| | | 9. 1 Backlight can't work normally. | Major |
| 09 | Backlight elements | 9. 2 Backlight doesn't light or color is wrong. | Major |
| | | 9. 3 Illumination source flickers when lit. | Major |
| | General appearance | 10. 1 Pin type · quantity · dimension must match type in structure diagram. | Major |
| | | 10, 2 No short circuits in components on PCB or FPC. | Major |
| 10 | | 10. 3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts. | Major |
| 10 | | 10. 4 Product packaging must the same as specified on packaging specification sheet. | Minor |
| | | 10.5 The folding and peeled off in polarizer are not acceptable. | Minor |
| | | 10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC) is ≤ 1.5 mm. | Minor |



4. RELIABILITY TEST

4.1 Reliability Test Condition

| NO. | TEST ITEM | TEST CONDITION | | | |
|-----|-----------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------|--|--|
| 1 | High Temperature Storage Test | Keep in +80 ±2°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs. | | | |
| 2 | Low Temperature Storage Test | Keep in -30 ±2°C 96 hrs Surrounding temperature, then storage at no | ormal condition 4hrs. | | |
| 3 | High Temperature / High Humidity Storage Test | Keep in +60 $^{\circ}$ C / 90% R.H duration for 96 h Surrounding temperature, then storage at no (Excluding the polarizer) | | | |
| 4 | Temperature Cycling Storage Test | $-30^{\circ}\text{C} \rightarrow +25^{\circ}\text{C} \rightarrow +80^{\circ}\text{C} \rightarrow +25^{\circ}\text{C}$ (30mins) (5mins) (30mins) (5mins) 10 Cycle Surrounding temperature, then storage at normal condition 4hrs. | | | |
| 5 | ESD Test | Apply 2 KV with 5 times Apply 25 | discharges at least 1 sec) | | |
| 6 | Vibration Test (Packaged) | Sine wave 10 55 Hz frequency (1 min/sweep) The amplitude of vibration :1.5 mm Each direction (X \ Y \ Z) duration for 2 Hrs | | | |
| 7 | Drop Test (Packaged) | Packing Weight (Kg) Drop 0 ~ 45.4 45.4 ~ 90.8 90.8 ~ 454 Over 454 Drop Direction: %1 corner / 3 edges / 6 sides | Height (cm) 122 76 61 46 | | |



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320 ± 10 and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25° C $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

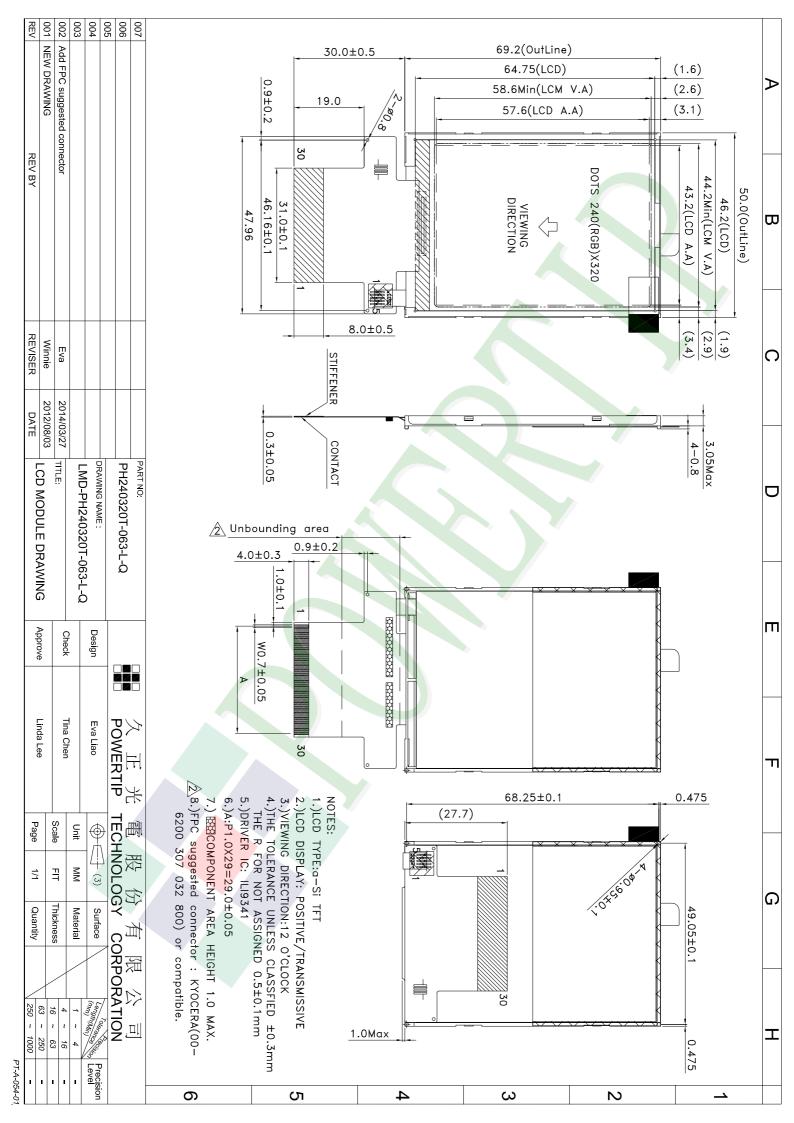
5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Approve Contact Check Ver.002 LCM包裝規格書 Linda Lee Tina Chen Eva Liao PKG-PH240320T-063-L-O Documents NO. LCM Packaging Specifications 1.包裝材料規格表 (Packaging Material): (per carton) No. Item Model Dimensions (mm) 1Pcs Weight Quantity Total Weight 1 成品 (LCM) PH240320T-063-L-Q 69.2 X 50 0.021 432 9.072 2 抗靜電氣泡(1)Bubble Bag 100 X 80 0.5184 BAG100080BWABA 0.0012 432 3 A2-1隔板(2)A2-1 Partition BX29500072BZBA 295 X 72 X 3.0 0.0109 56 0.6104 4 B2-1隔板(4)B2-1 Partition 245 X 72 X 3.0 0.3008 BX24500072BZBA /2 0.0094 32 5 氣泡紙(4)Bubble Sheet BAG280240BWABA 280 X 240 0.006 16 0.096 6 C2內盒(5)Product Box BX31025580AABA 310 X 255 X 86 8 1.28 0.16 7 外紙箱(6)Carton BX52732536CCBA 527 X 325 X 360 0.83 1 0.83 8 9 Kg±10% 2.一 整箱總重量 (Total LCD Weight in carton): 12.7 3.單箱數量規格表 (Packaging Specifications and Quantity): (1)Quantity Of Spacer: A2-1隔板 X 7 2\B2-1隔板 X (2)Total LCM quantity in carton: quantity per box x no of boxes 432 (4) 氣泡紙 **Bubble Sheet** (1)抗靜電氣泡袋+LCM Antistatic Bag+LCM (3) B2-1隔板 B2-2 Partition (2) A2-1隔板 À2-1 Partition ∜ (4) 氣泡紙 **Bubble Sheet** (6)外紙箱 Carton (5) C2內盒 Product Box 特 記 事 項 (REMARK) 4. LCM排放示意圖(前後間隔不放置): 4. LCM placed as figure showing: (First and last slot should be empty) Ø 模組(LCM) X 3pcs.