4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

Product Summary

V_{BR} (Min)	I_{PP} (Max)	C_T (Typ)
4.5V	20A	2.4pF

Features

- Clamping Voltage: 7.5V at 12A 100ns, TLP 6V at 5A 8µs/20µs
- IEC 61000-4-2 (ESD): Air – 30kV, Contact – 30kV
- IEC 61000-4-4 (EFT): 80A (5/50ns)
- IEC 61000-4-5 (Lighting): 20A (8/20µs)
- 4 Channels of ESD Protection
- Low Channel Input Capacitance of 2.4pF Typical
- TLP Dynamic Resistance: 0.15Ω
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Description

The D5V0P4URL6SO is a high performance device suitable for protecting four high-speed I/Os. These devices are assembled in SOT26 packages and have high ESD surge capability and low capacitance.

Applications

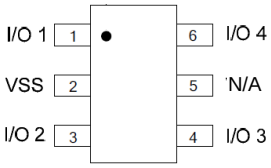
Typically used at high-speed ports such as USB 2.0, IEEE1394 (FireWire®, iLink™), Serial ATA, DVI, HDMI and PCI.

Mechanical Data

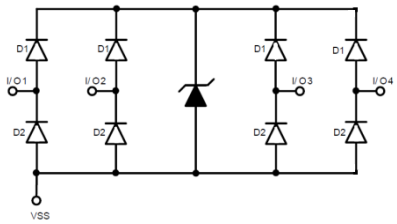
- Case: SOT26
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Schematic
- Terminals – Finish – Matte Tin Pleated Leadframe. Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.016 grams (Approximate)

SOT26

Top View



Device Schematic

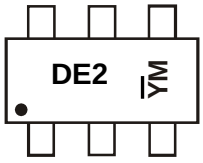


Ordering Information (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
D5V0P4URL6SO-7	Standard	DE2	7	8	3,000/Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



DE2 = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: A = 2015)
M = Month (ex: 9 = September)
Note: "▬" Represents Internal Code

Date Code Key

Year	2015	2016	2017	2018	2019	2020
Code	C	D	E	F	G	H

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

FireWire is a registered trademark of Apple Computer, Inc.
iLink is a trademark of CEM Corporation.

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current, per IEC 61000-4-5	I_{PP}	20	A	I/O to V_{SS}, 8/20µs
Peak Pulse Power, per IEC 61000-4-5	P_{PP}	180	W	I/O to V_{SS}, 8/20µs
ESD Protection – Contact Discharge, per IEC 61000-4-2	$V_{ESD\_CONTACT}$	30	kV	I/O to V_{SS}
ESD Protection – Air Discharge, per IEC 61000-4-2	V_{ESD_AIR}	30	kV	I/O to V_{SS}
Operating Temperature	T_{OP}	-55 to +85	°C	—
Storage Temperature	T_{STG}	-55 to +150	°C	—

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation Typical (Note 5)	P_D	300	mW
Thermal Resistance, Junction to Ambient Typical (Note 5)	$R_{\theta JA}$	417	°C/W

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse Working Voltage	V_{RWM}	—	—	3.3	V	I_R=1mA, I/O to V_{SS}
Reverse Current (Note 6)	I_R	—	—	1	µA	V_R = 3.3V, I/O to V_{SS}
Reverse Breakdown Voltage	V_{BR}	4.5	—	8.0	V	I_R = 1mA, I/O to V_{SS}
Forward Clamping Voltage	V_F	—	0.8	1.2	V	I_F = 15mA, V_{SS} to I/O
Reverse Clamping Voltage (Note7)	V_C	—	6	—	V	I_{PP} = 5A, I/O to V_{SS}, 8/20µs
ESD Clamping Voltage	V_{ESD}	—	7.5	—	V	TLP, 12A, t_P = 100ns, I/O to V_{SS}
Dynamic Reverse Resistance	R_{DIF-R}	—	0.15	—	Ω	TLP, 12A, t_P = 100ns, I/O to V_{SS}
Channel Input Capacitance	$C_{I/O}$	—	2.4	3	pF	$V_{I/O}$ = 1.65V, V_{SS} = 0V, f = 1MHz
Delta $C_{I/O}$	$C_{I/OMAX}$–$C_{I/OMIN}$	—	0.04	—	pF	$C_{I/OMAX}$–$C_{I/OMIN}$

Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. website at http://www.diodes.com/package-outlines.html.
6. Short duration pulse test used to minimize self-heating effect.
7. Clamping voltage value is based on an 8x20µs peak pulse current (I_{PP}) waveform.

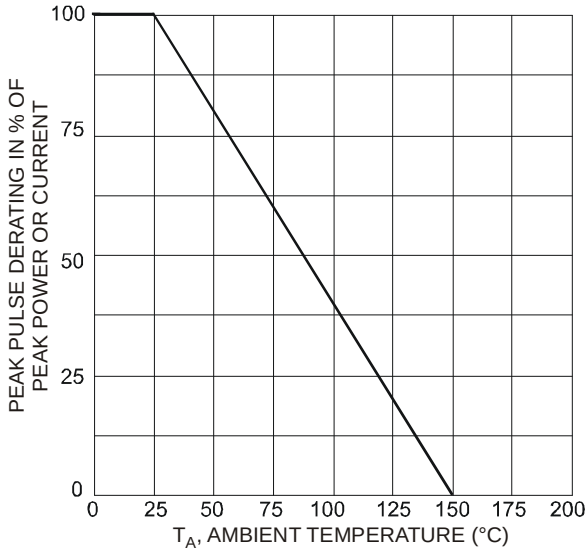


Figure 1 Pulse Derating Curve

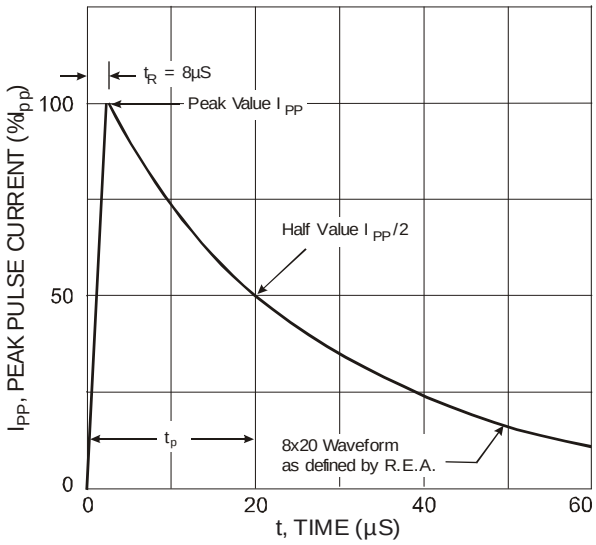


Figure 2 Pulse Waveform

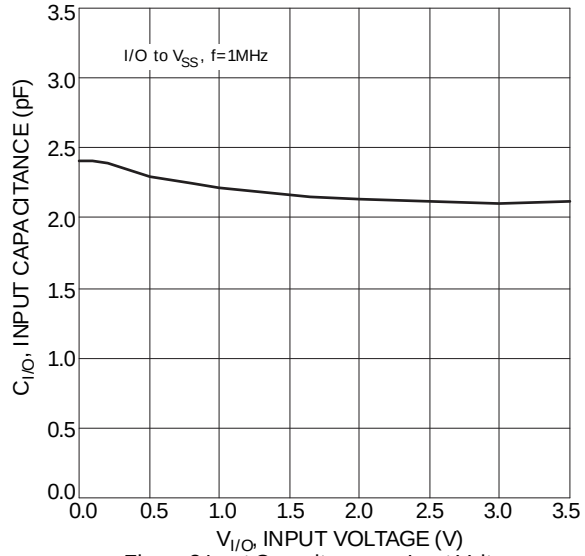


Figure 3 Input Capacitance vs. Input Voltage

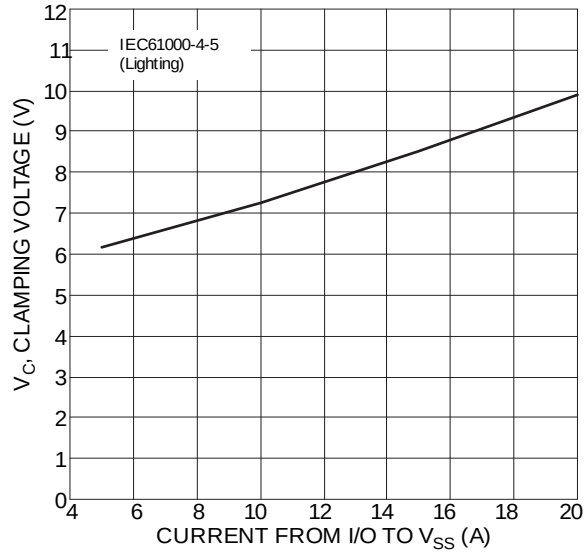


Figure 4 Clamping Voltage Characteristic

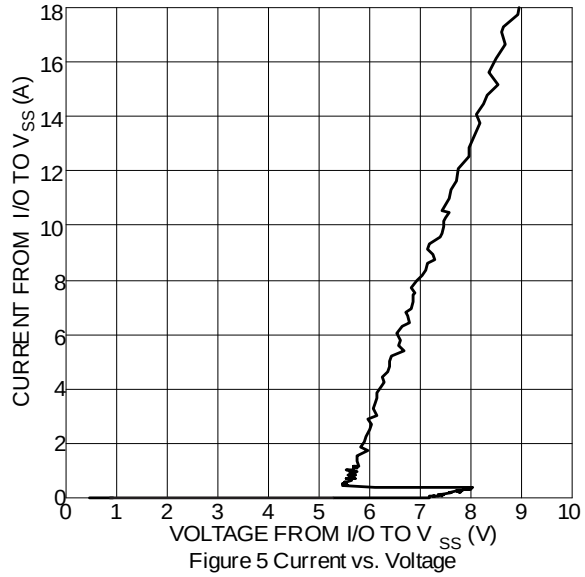


Figure 5 Current vs. Voltage

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

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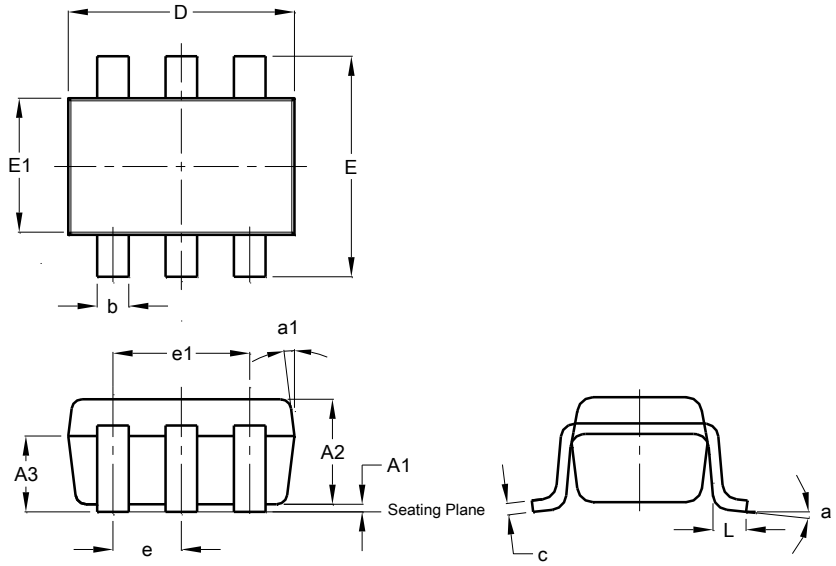
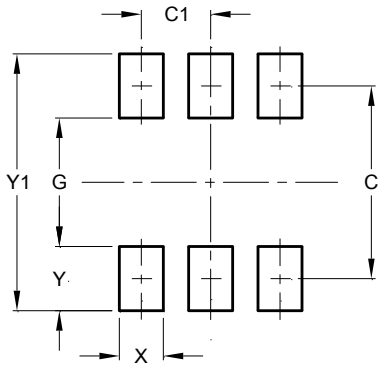


SOT26			
Dim	**Min**	**Max**	**Typ**
A1	0.013	0.10	0.05
A2	1.00	1.30	1.10
A3	0.70	0.80	0.75
b	0.35	0.50	0.38
c	0.10	0.20	0.15
D	2.90	3.10	3.00
e	–	–	0.95
e1	–	–	1.90
E	2.70	3.00	2.80
E1	1.50	1.70	1.60
L	0.35	0.55	0.40
a	–	–	8°
a1	–	–	7°
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26



Dimensions	Value (in mm)
C	2.40
C1	0.95
G	1.60
X	0.55
Y	0.80
Y1	3.20

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