V10P45S-M3

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Vishay General Semiconductor

# SMD Photovoltaic Solar Cell Protection Schottky Rectifier

Ultra Low  $V_F = 0.34$  V at  $I_F = 5$  A

# **FEATURES**

- Very low profile typical height of 1.1 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- · Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

### **TYPICAL APPLICATIONS**

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

### **MECHANICAL DATA**

Case: TO-277A (SMPC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102 M3 suffix meets JESD 201 class 2 whisker test

<b>MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V10P45S	UNIT	
Device marking code		1045S		
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	45	V	
Maximum DC forward current	I <sub>F</sub> <sup>(1)</sup>	10	Α	
	I <sub>F</sub> <sup>(2)</sup>	4.4		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	180	А	
Junction temperature in DC forward current without reverse bias, $t \leq 1 \ h$	T <sub>J</sub> <sup>(3)</sup>	≤ 200	°C	
Operating junction temperature range	T <sub>OP</sub>	-40 to +150	°C	
Storage temperature range	T <sub>STG</sub>	-40 to +175	°C	

Notes

<sup>(1)</sup> Mounted on 30 mm x 30 mm aluminum PCB

<sup>(2)</sup> Free air, mounted on recommended copper pad area

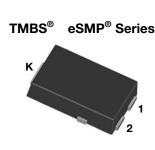
<sup>(3)</sup> Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test

Revision: 28-Nov-13

1

RoHS

COMPLIANT HALOGEN FREE







10 A

45 V

180 A

0.41 V

150 °C

TO-277A (SMPC)

Single die

**PRIMARY CHARACTERISTICS** 

I<sub>F(AV)</sub>

V<sub>RRM</sub>

 $I_{FSM}$ 

 $V_F$  at  $I_F = 10 A$ 

T<sub>OP</sub> max. Package

Diode variation

V10P45S-M3



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage	I <sub>F</sub> = 5.0 A	– T <sub>A</sub> = 25 °C	- V <sub>F</sub> <sup>(1)</sup>	0.42	-	V	
	I <sub>F</sub> = 10 A			0.48	0.57		
	I <sub>F</sub> = 5.0 A	T <sub>A</sub> = 125 °C		0.34	-		
	I <sub>F</sub> = 10 A			0.41	0.50		
Reverse current	V - 45 V	$V_{\rm R} = 45 \text{ V}$ $T_{\rm A} = 25 \text{ °C}$ $T_{\rm A} = 125 \text{ °C}$	I <sub>R</sub> <sup>(2)</sup>	21	800	μA	
	v <sub>R</sub> = 45 v			9	35	mA	

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V10P45S	UNIT	
Turning thermal registering	R <sub>0JA</sub> <sup>(1)</sup>	75	°C/W	
Typical thermal resistance	R <sub>0JM</sub> <sup>(2)</sup>	4		

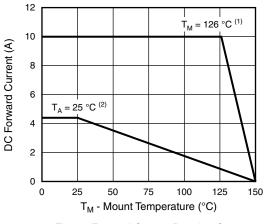
### Notes

 $^{(1)}$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  - junction to ambient

 $^{(2)}$  Mounted on 30 mm x 30 mm aluminum PCB; thermal resistance  $R_{\theta JM}$  - junction to mount

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V10P45S-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel		
V10P45S-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel		

# RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)





### Notes

- $^{(1)}$  Mounted on 30 mm x 30 mm aluminum PCB; T<sub>M</sub> measured at the terminal of cathode band (R<sub>0JM</sub> = 4 °C/W)
- $^{(2)}$  Free air, mounted on recommended copper pad area (R $_{\rm 0JA}$  = 75 °C/W)

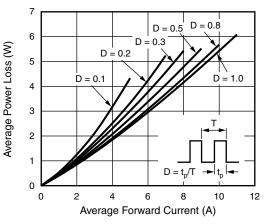


Fig. 2 - Forward Power Loss Characteristics

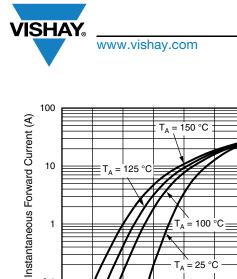
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T<sub>A</sub> = 125

0.2

10

1

0.1

0

0.1

Instantaneous Forward Voltage (V) Fig. 3 - Typical Instantaneous Forward Characteristics

0.3

0.4

100 <sup>6</sup>C

0.5

0.6

0.7

=

25

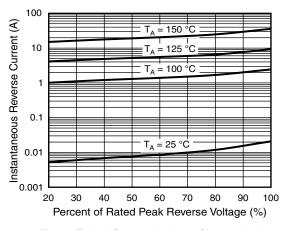


Fig. 4 - Typical Reverse Leakage Characteristics

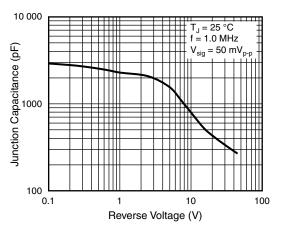


Fig. 5 - Typical Junction Capacitance

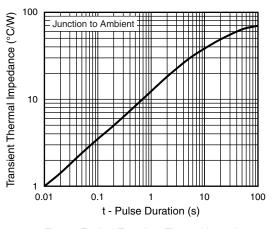
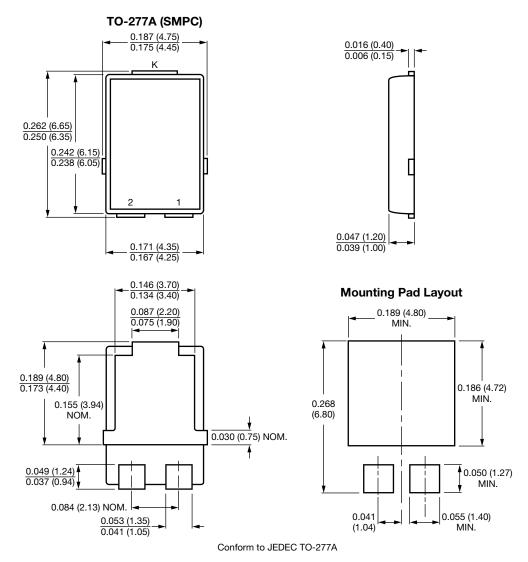


Fig. 6 - Typical Transient Thermal Impedance

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## PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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