CXT5401

SURFACE MOUNT PNP SILICON TRANSISTOR



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CXT5401 type is a PNP silicon transistor manufactured by the epitaxial planar process, epoxy molded in a surface mount package, designed for high voltage amplifier applications.

MARKING: FULL PART NUMBER



V_{CE}=10V, I_C=10mA, f=100MHz

 V_{CE} =10V, I_{C} =1.0mA, f=1.0kHz

 V_{CE} =5.0V, I_{C} =250 μ A, R_{S} =1.0 $k\Omega$,

 V_{CB} =10V, I_E =0, f=1.0MHz

f=10Hz to 15.7kHz

 f_{T}

 C_{ob} h_{fe}

 N_{F}

MAXIMUM RATINGS: (T _A =25°C)		SYMBOL		UNITS
Collector-Base Voltage		V_{CBO}	160	V
Collector-Emitter Voltage		V_{CEO}	150	V
Emitter-Base Voltage		V_{EBO}	5.0	V
Continuous Collector Current		l _C	600	mA
Power Dissipation		P_{D}	1.2	W
Operating and Storage Junction Temperature		T _{J,} T _{stg}	-65 to +150	°C
Thermal Resistance		Θ_{JA}	104	°C/W
ELECTRICAI SYMBOL	L CHARACTERISTICS: (T _A =25°C t TEST CONDITIONS	unless otherwise no	oted) MAX	UNITS
		IVIIIV		
ICBO	V _{CB} =120V		50	nA
ICBO	V _{CB} =120V, T _A =100°C		50	μA
I _{EBO}	V _{EB} =3.0V		50	nA
BV CBO	I _C =100μA	160		V
BV CEO	I _C =1.0mA	150		V
BVEBO	I _E =10μA	5.0		V
V _{CE(SAT)}	I _C =10mA, I _B =1.0mA		0.2	V
V _{CE(SAT)}	I _C =50mA, I _B =5.0mA		0.5	V
V _{BE} (SAT)	I _C =10mA, I _B =1.0mA		1.0	V
V _{BE} (SAT)	I _C =50mA, I _B =5.0mA		1.0	V
h _{FE}	V_{CE} =5.0V, I_{C} =1.0mA	50		
h _{FE}	V_{CE} =5.0V, I_{C} =10mA	60	240	
hFE	V_{CE} =5.0V, I_{C} =50mA	50		

100

40

300

6.0

200

8.0

R7 (23-February 2010)

MHz

рF

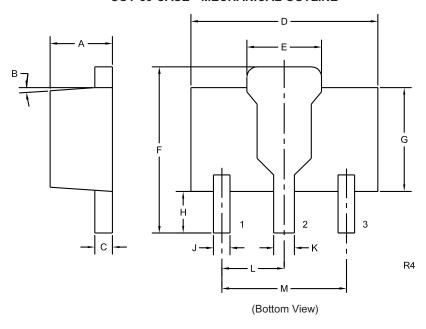
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CXT5401

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SOT-89 CASE - MECHANICAL OUTLINE



LEAD CODE:

- Emitter
 Collector
- 3) Base

MARKING:

FULL PART NUMBER

DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
Α	0.055	0.067	1.40	1.70			
В	4°		4°				
С	0.014	0.018	0.35	0.46			
D	0.173	0.185	4.40	4.70			
E	0.064	0.074	1.62	1.87			
F	0.146	0.177	3.70	4.50			
G	0.090	0.106	2.29	2.70			
Н	0.028	0.051	0.70	1.30			
J	0.014	0.019	0.36	0.48			
K	0.017	0.023	0.44	0.58			
L	0.059		1.50				
М	0.1	18	3.00				

SOT-89 (REV: R4)

R7 (23-February 2010)



OUTSTANDING SUPPORT AND SUPERIOR SERVICES

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Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
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- SPICE models
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- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- · PbSn plating options
- · Package details
- · Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

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