



40V N-CHANNEL ENHANCEMENT MODE MOSFET **POWERDI**

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _C = +25°C
40V	$2.5 \text{m}\Omega$ @ $V_{GS} = 10V$	90A
40 V	4mΩ @ V _{GS} = 4.5V	90A

Features

- 100% Unclamped Inductive Switching ensures more reliable and robust end application
- Low R_{DS(ON)} minimizes power losses
- Low Qg minimizes switching losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

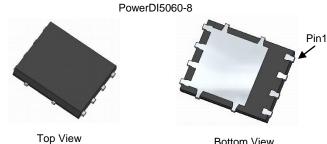
Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

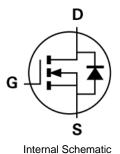
- **Engine Management Systems**
- **Body Control Electronics**
- **DC-DC Converters**

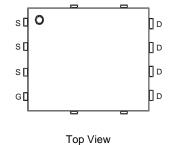
Mechanical Data

- Case: PowerDI5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)



Bottom View





Pin Configuration

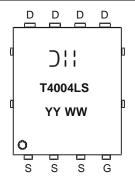
Ordering Information (Note 4)

Part Number	Case	Packaging
DMT4004LPS-13	PowerDI5060-8	2,500 / Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



) | = Manufacturer's Marking T4004LS = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 16 = 2016)WW = Week (01 to 53)



Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V _{DSS}	40	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current (Note 5)	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	26 21	А
Continuous Drain Current (Note 6)	$T_{C} = +25^{\circ}C$ $T_{C} = +70^{\circ}C$ (Note 8)	Ι _D	90 90	А
Maximum Continuous Body Diode Forward Current (Note 6)		I _S	70	Α
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)		I _{DM}	100	Α
Avalanche Current, L=0.2mH		I _{AS}	33.3	Α
Avalanche Energy, L=0.2mH		Eas	110	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5) $T_A = +25^{\circ}C$		P_{D}	2.6	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	47	°C/W	
Total Power Dissipation (Note 6) $T_C = +25^{\circ}C$		P_D	138	W
Thermal Resistance, Junction to Case (Note 6)	$R_{ heta JC}$	0.9	°C/W	
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

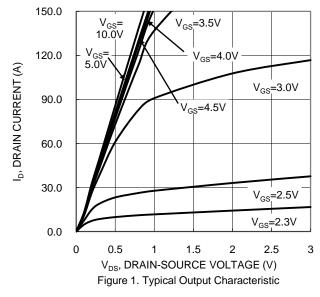
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)		1	1	1			
Drain-Source Breakdown Voltage	BV _{DSS}	40	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 32V$, $V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	1	_	3	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	D	_	_	2.5	mΩ	$V_{GS} = 10V, I_D = 50A$	
Static Diani-Source Off-Resistance	R _{DS(ON)}	_	_	4	11122	$V_{GS} = 4.5V, I_D = 50A$	
Diode Forward Voltage	V_{SD}	_	0.9	1.2	V	$V_{GS} = 0V, I_{S} = 50A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	4508	_		$V_{DS} = 20V, V_{GS} = 0V,$ f = 1MHz	
Output Capacitance	C _{oss}	_	1648	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	104	_			
Gate resistance	Rg	_	0.7	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	34.6	_	nC		
Total Gate Charge (V _{GS} = 10V)	Qg	_	82.2	_		$V_{DD} = 20V, I_{D} = 30A$	
Gate-Source Charge	Q_{gs}	_	9.9	_	nC	VDD = 20V, ID = 30A	
Gate-Drain Charge	Q_{gd}	_	11.2	_			
Turn-On Delay Time	t _{D(ON)}	_	5.9	_			
Turn-On Rise Time	t _R	_	13.3	_		$V_{DD} = 20V, V_{GS} = 10V,$ $I_{D} = 30A, R_{G} = 1.6\Omega$	
Turn-Off Delay Time	t _{D(OFF)}	_	25.9	_	ns		
Turn-Off Fall Time	t _F	_	7.9	_			
Body Diode Reverse Recovery Time	t _{RR}	_	48.4	_	ns	I FOA di/dt 1004/u-	
Body Diode Reverse Recovery Charge	Q _{RR}	_	72.4	_	nC	$I_F = 50A$, di/dt = 100A/ μ s	

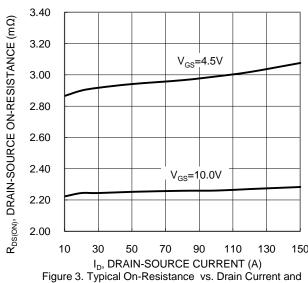
Notes:

- 5. Device mounted with exposed drain pad on 25mm by 25mm 2oz copper on a single- sided 1.6mm FR-4 PCB; device is measured under still air conditions whilst operating in a steady state.
- 6. Thermal resistance from junction to soldering point (on the exposed drain pad).
 7 .Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to production testing.

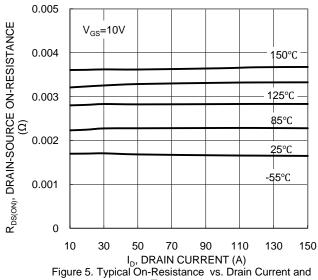




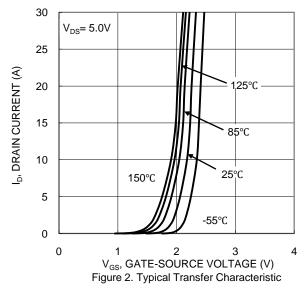


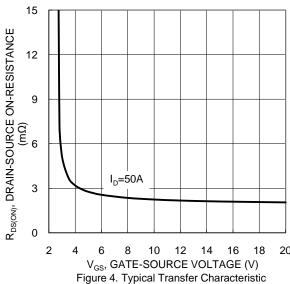


Gate Voltage



Temperature





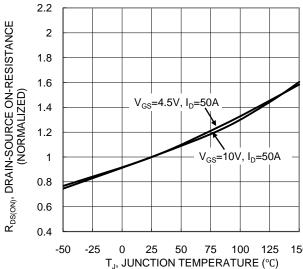
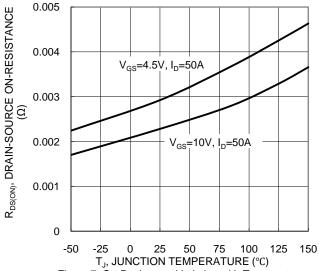
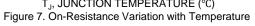


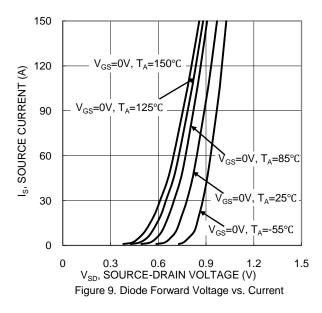
Figure 6. On-Resistance Variation with Temperature

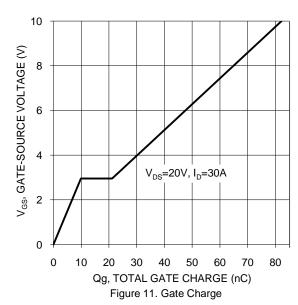


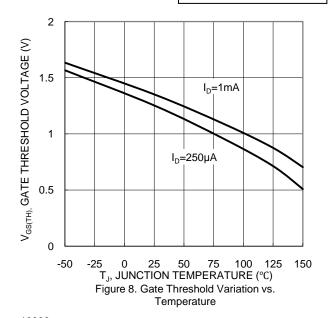








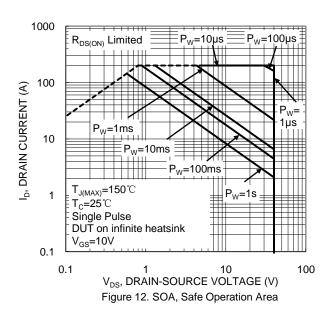




10000 | C_{iss} | f=1MHz | C_{oss} | C_{rss} | C_{rs} | C

V_{DS}, DRAIN-SOURCE VOLTAGE (V)

Figure 10. Typical Junction Capacitance





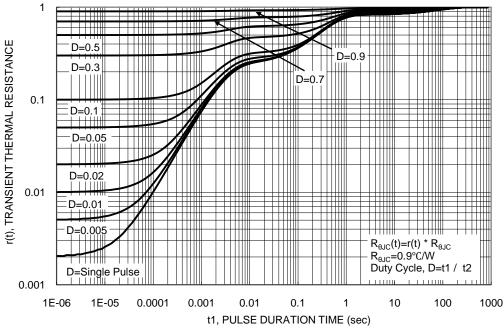
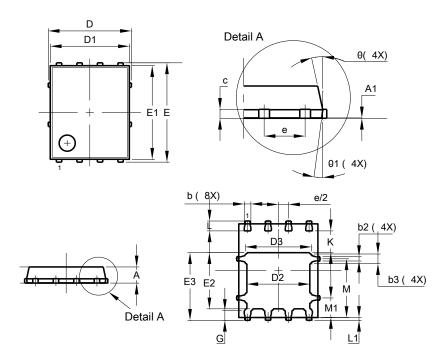


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

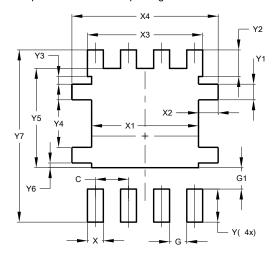


PowerDI5060-8					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0.00 0.05		-		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
С	0.230	0.330	0.277		
D	į.	5.15 BSC	;		
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
Е	6.15 BSC				
E1	5.60	6.00 5.8			
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	•	1.27 BSC	;		
G	0.51	0.71	0.61		
K	0.51	-	-		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
M	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
θ	10°	12º	11º		
θ1	6º	80	7º		
All Dimensions in mm					



Suggested Pad Lavout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	0.755
Х3	4.420
X4	5.610
Υ	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

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