

AAP Gen 7 (TO-240AA) Power Modules Schottky Rectifier, 200 A



PRIMARY CHARACTERISTICS			
I _{F(AV)}	200 A		
V _R	45 V		
Package	AAP Gen 7 (TO-240AA)		
Circuit configuration	Two diodes common cathode		

MECHANICAL DESCRIPTION

The AAP Gen 7, new generation of ADD-A-PAK module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation
- Low thermal resistance
- UL approved file E78996
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- High surge capability
- · Easy mounting on heatsink

ELECTRICAL DESCRIPTION / APPLICATIONS

The VS-VSKCS201.. Schottky rectifier common cathode has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature.

Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I _{F(AV)}	Rectangular waveform	200	A			
V_{RRM}		45	V			
I _{FSM}	t _p = 5 μs sine	8600	А			
V _F	100 A _{pk} , T _J = 125 °C	0.69	V			
T _J	Range	-55 to +175	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-VSKCS201/045	UNITS		
Maximum DC reverse voltage	V_{R}	45	V		
Maximum working peak reverse voltage	V_{RWM}	45	V		



ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average	per module		50 % duty cycle at T _C = 123 °C, rectangular waveform		200	
forward current	per leg	I _{F(AV)}			100	
Maximum peak one cycle			5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	8600	А
non-repetitive surge current		IFSM	1 3 10 1 1	rated V _{RRM} applied	1850	
Non-repetitive avalanche energ	Jy	E _{AS}	T _J = 25 °C, I _{AS} = 24 A, L = 1 mH		270	mJ
Repetitive avalanche current		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		А	

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V _{FM}	100 A	T _J = 25 °C	0.72	V
		200 A		1.04	
		100 A	T _J = 125 °C	0.69	
		200 A		0.98	
Marian and a last and a second	I _{RM}	T _J = 25 °C	V _R = Rated V _R	10	mA
Maximum reverse leakage current		T _J = 125 °C		90	MA
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		5200	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs
Maximum RMS insulation voltage	V _{INS}	50 Hz		3000 (1 min) 3600 (1 s)	V

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +175	°C
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation	0.52	· °C/W
Typical thermal resistance, case to heatsink per module		R _{thCS}		0.1	C/VV
Approximate weight				75	g
Approximate weight				2.7	oz.
Mounting torque ± 10 % to heatsink busbar		A mounting compound is recommended and the torque should be rechecked after a period of 3 hours to allow for	4	Nm	
	busbar		the spread of the compound.	3	INIII
Case style			JEDEC	TO-240AA co	mpatible

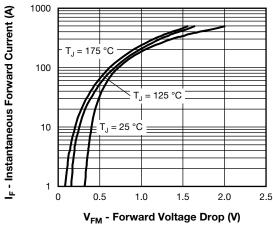


Fig. 1 - Maximum Forward Voltage Drop Characteristics

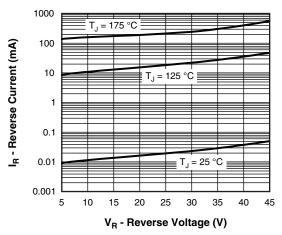


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

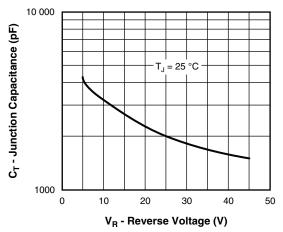


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

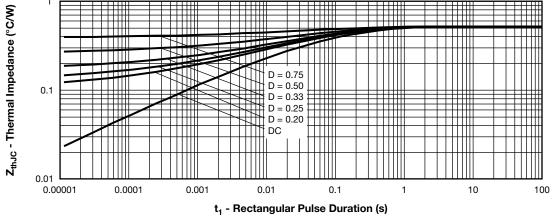


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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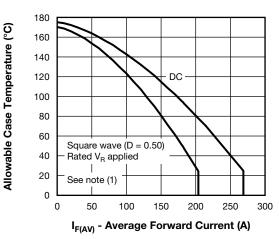


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

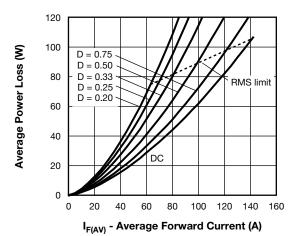


Fig. 6 - Forward Power Loss Characteristics

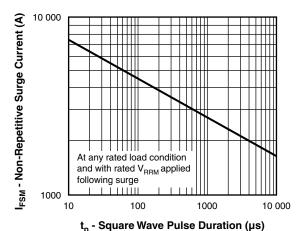


Fig. 7 - Maximum Non-Repetitive Surge Current

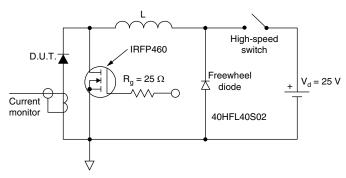


Fig. 8 - Unclamped Inductive Test Circuit

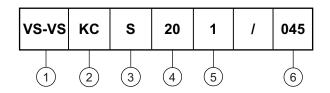
Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



- VS-VS = Vishay Semiconductors product

2 - Circuit configuration:

KC = ADD-A-PAK - 2 diodes common cathode

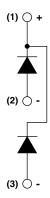
3 - S = Schottky diode

- Average current rating (20 = 200 A)

Product silicon identification

6 - Voltage rating (045 = 45 V)

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95369			



ADD-A-PAK Generation VII - Diode

DIMENSIONS in millimeters (inches)





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APT50M50JLL APT50M75JLLU3 APT50N60JCCU2 APT58M80J APT80F60J DZ540N26K B522F-2-YEC MSTC90-16

MT16HTF12864AZ-800G1 MT18HTF12872PZ-667G1 MT18HTF25672FDZ-667H1D6 MT18HTF25672PZ-80EH1 MT18RTF25672FDZ-667H1D6 MT36HTF51272FZ-667H1D4 MT36HTF51272FZ-667H1D6 MT8HTF12864HTZ-667H1 MT9HTF6472PZ-667G1

MT9HVF12872PZ-80EH1 MT9HVF6472PZ-667G1 ND104N16K 25.163.0653.1 25.163.2453.0 25.163.4253.0 25.190.2053.0 25.194.3453.0 25.320.4853.1 25.320.5253.1 25.325.3653.1 25.326.3253.1 25.326.3553.1 25.330.1653.1 25.330.4753.1 25.330.5253.1 25.334.3253.1