

Description

The AP2210 is a 300mA ULDO regulator which provides very low noise, ultra low dropout voltage (typically 250mV at 300mA), very low standby current (1µA maximum) and excellent power supply ripple rejection (PSRR 75dB at 100Hz) in battery powered applications, such as handsets, PDAs and in noise sensitive applications, such as RF electronics.

The AP2210 also features individual logic compatible enable/shutdown control inputs, a low power shutdown mode for extended battery life, over current protection, over temperature protection, as well as reversed-battery protection.

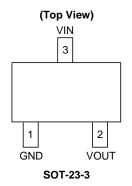
The AP2210 has 2.5V, 2.8V, 3.0V, 3.3V, 3.6V, 4.0V, 5.0V and ADJ versions.

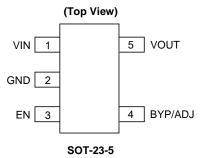
The AP2210 is available in space saving SOT-23-3 and SOT-23-5 packages.

Features

- Up to 300mA Output Current
- Excellent ESR Stability
- Low Standby Current
- Low Dropout Voltage: V_{DROP} = 250mV at 300mA
- High Output Accuracy: ±1%
- Good Ripple Rejection Ability: 75dB at 100Hz and I_{OUT} = 100μA
- Tight Load and Line Regulation
- Low Temperature Coefficient
- Over Current Protection
- Thermal Protection
- Reverse-battery Protection
- Logic-controlled Enable
- Lead-Free Packages: SOT-23-3, SOT-23-5
 - Totally Lead-Free; RoHS Compliant (Notes 1 & 2)
- Lead-Free Packages, Available in "Green" Molding Compound: SOT-23-3, SOT-23-5
 - Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
 - Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments





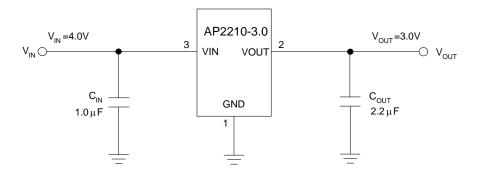
Applications

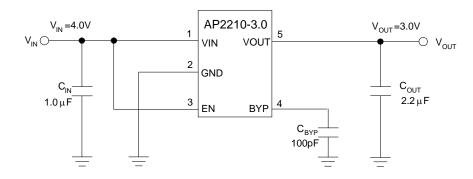
- Cellular Phones
- Cordless Phones
- Wireless Communicators
- PDAs/Palmtops
- PC Mother Board
- Consumer Electronics

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

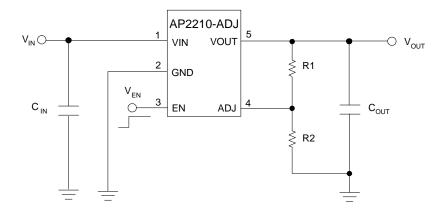


Typical Applications Circuit (Note 4)





For Fixed Version



 $V_{OUT} = 1.25V*(1+R2/R1)$

For Adjustable Version

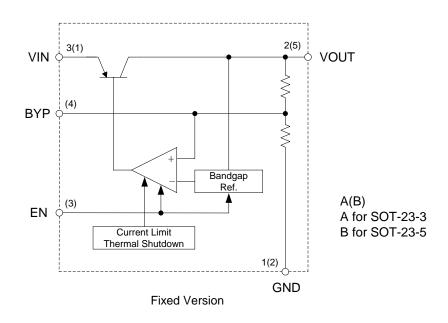
Note 4: Dropout voltage is 250mV when T_A = +25°C. In order to obtain a normal output voltage, V_{OUT}+0.25V is the minimum input voltage which will result a low PSRR, imposing a bad influence on system. Therefore, the recommended input voltage is V_{OUT}+1V to 13.2V. For AP2210-3.0 version, its input voltage can be set from 4V (V_{OUT}+1V) to 13.2V.

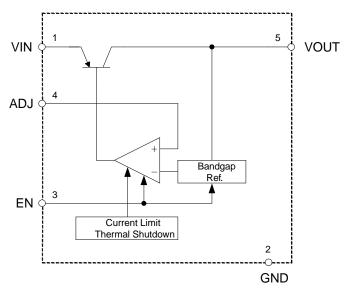


Pin Descriptions

| Pin N | lumber | 5: 1: | - |
|----------------|----------|----------|---|
| SOT-23-3 | SOT-23-5 | Pin Name | Function |
| 1 2 GND Ground | | GND | Ground |
| 2 | 5 | VOUT | Regulated output voltage |
| 3 | 1 | VIN | Input voltage |
| _ | 3 | EN | Enable input: CMOS or TTL compatible input. Logic high=enable, logic low=shutdown |
| _ | 4 | BYP/ADJ | Bypass capacitor for low noise operation/Adjustable Output |

Functional Block Diagram





ADJ Version (For SOT-23-5)



Absolute Maximum Ratings (Note 5)

| Symbol | Parameter | Rat | ting | Unit |
|-------------------|-------------------------------------|--------------|--------------------------|------|
| V _{IN} | Supply Input Voltage | 1 | 5 | V |
| V _{EN} | Enable Input Voltage | 1 | 5 | V |
| P _D | Power Dissipation | · · | y Limited Protection) | W |
| T _{LEAD} | Lead Temperature (Soldering, 10sec) | +260 | | °C |
| TJ | Junction Temperature | +1 | 50 | °C |
| T _{STG} | Storage Temperature | -65 to | +150 | °C |
| ESD | ESD (Machine Model) | 300 | | V |
| 0 | Thermal Decisions (No. 11 annie) | SOT-23-3 200 | | |
| θ _{JA} | Thermal Resistance (No Heatsink) | SOT-23-5 | 200 | °C/W |

Note 5: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Recommended Operating Conditions

| Symbol | Parameter | Min | Max | Unit |
|-----------------|--------------------------------|-----|------|------|
| V _{IN} | Supply Input Voltage | 2.5 | 13.2 | V |
| V _{EN} | Enable Input Voltage | 0 | 13.2 | V |
| TJ | Operating Junction Temperature | -40 | +125 | °C |



Electrical Characteristics

AP2210-2.5 Electrical Characteristics (V_{IN} = 3.5V, I_{OUT} = 100μA, C_{IN} = 1.0μF, C_{OUT} = 2.2μF, V_{EN} ≥ 2.0V, T_J = +25°C, **bold** typeface applies over -40°C ≤ T_J ≤ +125°C (Note 6), unless otherwise specified.)

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|-------------------------------------|------------------------------|--|-----|------|-----|--------|
| AV 07 | Outrot Valtage Assurant | Variation from an addited V | -1 | _ | 1 | 0/ |
| ΔVουτ/Vουτ | Output Voltage Accuracy | Variation from specified V _{OUT} | -2 | _ | 2 | % |
| $\Delta V_{OUT}/\Delta T$ | Output Voltage Temperature | - | _ | 120 | - | μV/°C |
| $(\Delta V_{OUT}/V_{OUT})/\Delta T$ | Coefficient (Note 7) | - | _ | 48 | - | ppm/°C |
| ., | l: 5 1 5 | V 0.5V (5.40.0V) | _ | 1.5 | 4.5 | ., |
| V_{RLINE} | Line Regulation | V _{IN} = 3.5V to 13.2V | _ | _ | 12 | mV |
| V | Local Develotion (Note 0) | 0.44.10004 | _ | 1 | 6 | |
| V _{RLOAD} | Load Regulation (Note 8) | I _{OUT} = 0.1mA to 300mA | _ | _ | 30 | mV |
| | | 400.4 | _ | 15 | 50 | |
| | | I _{OUT} = 100μA | _ | _ | 70 | |
| | Dropout Voltage (Note 9) | I _{OUT} = 50mA | _ | 110 | 150 | |
| V_{DROP} | | | _ | _ | 230 | mV |
| | | I _{OUT} = 100mA | _ | 140 | 250 | |
| | | | _ | _ | 300 | |
| | | I _{OUT} = 150mA | _ | 165 | 275 | |
| | | | _ | _ | 350 | |
| | | | _ | 250 | 400 | |
| | | | _ | _ | 500 | |
| | | V _{EN} ≤ 0.4V (shutdown) | _ | 0.01 | 1 | |
| I _{STD} | Standby Current | V _{EN} ≤ 0.18V (shutdown) | _ | _ | 5 | μA |
| | | | _ | 100 | 150 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 100μA | _ | _ | 180 | 1 |
| | | | _ | 350 | 600 | μA |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 50mA | _ | _ | 800 | |
| I_{GND} | Ground Pin Current (Note 10) | V - 0.0V I | _ | 1.3 | 1.9 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 150mA | _ | - | 2.5 | mA |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 300mA | _ | 4 | 10 | |
| | | | _ | - | 15 | |
| PSRR | Ripple Rejection | f = 100Hz, I _{OUT} = 100μA | _ | 75 | - | dB |
| I _{LIMIT} | Current Limit | V _{OUT} = 0V | _ | 450 | 900 | mA |



AP2210-2.5 Electrical Characteristics ($V_{IN} = 3.5V$, $I_{OUT} = 100\mu A$, $C_{IN} = 1.0\mu F$, $C_{OUT} = 2.2\mu F$, $V_{EN} \ge 2.0V$, $T_{J} = +25$ °C, bold typeface applies over -40°C ≤ $T_{J} \le +125$ °C (Note 6), unless otherwise specified.)

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|-----------------|--|---|-----|------|------|----------------|
| e _{no} | Output Noise | $I_{OUT} = 50$ mA, $C_{OUT} = 2.2\mu$ F, 100pF from BYP to GND | _ | 260 | _ | nV/\sqrt{Hz} |
| ., | | | _ | _ | 0.4 | |
| V _{IL} | Enable Input Logic-low Voltage | Regulator shutdown | - | - | 0.18 | V |
| V_{IH} | Enable Input Logic-high Voltage | Regulator enabled | 2.0 | _ | - | V |
| | | V _{IL} ≤ 0.4V | _ | 0.01 | 1 | |
| I _{IL} | I _{IL} Enable Input Logic-low Current | V _{IL} ≤ 0.18V | - | - | 2 | μA |
| | | V _{IL} ≥ 2.0V | - | 5 | 20 | |
| Іін | Enable Input Logic-high Current | V _{IL} ≥ 2.0V | - | - | 25 | μA |

- 6. Specifications in bold type are limited to -40°C ≤ T_J ≤ +125°C. Limits over temperature are guaranteed by design, but not tested in production.
- 7. Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.
- 9. Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 0.1mA to 300mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- 9. Dropout voltage is defined as the input to output differential at which the output voltage drops 1% ($T_J = +25^{\circ}C$) or 2% ($-40^{\circ}C \le T_J \le +125^{\circ}C$) below its nominal value measured at 1V differential.
- 10. Ground pin current is the regulator quiescent current plus pass transistor base current. The total current drawn from the supply is the sum of the load current plus the ground pin current.



AP2210-2.8 Electrical Characteristics ($V_{IN} = 3.8V$, $I_{OUT} = 100\mu$ A, $C_{IN} = 1.0\mu$ F, $C_{OUT} = 2.2\mu$ F, $V_{EN} \ge 2.0V$, $T_{J} = +25^{\circ}$ C, bold typeface applies over -40°C $\le T_{J} \le +125^{\circ}$ C (Note 6), unless otherwise specified.)

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|---|------------------------------|--|-----|------|-----|--------|
| | Outsid Waltana Assurance | Mariatian form on a "Cad M | -1 | _ | 1 | 0, |
| $\Delta V_{	ext{OUT}} / V_{	ext{OUT}}$ | Output Voltage Accuracy | Variation from specified V _{OUT} | -2 | _ | 2 | % |
| $\Delta V_{OUT}/\Delta T$ | Output Voltage Temperature | _ | _ | 120 | - | μV/°C |
| (ΔV _{OUT} /V _{OUT})/ΔΤ | Coefficient (Note 7) | _ | _ | 42.8 | - | ppm/°C |
| ., | l: B 16 | V 0.0V/1- 40.0V/ | _ | 1.5 | 4.5 | ., |
| V_{RLINE} | Line Regulation | V _{IN} = 3.8V to 13.2V | _ | _ | 12 | mV |
| ., | | | _ | 1 | 6 | ., |
| V _{RLOAD} | Load Regulation (Note 8) | I _{OUT} = 0.1mA to 300mA | _ | _ | 30 | mV |
| | | | _ | 15 | 50 | |
| | | I _{OUT} = 100μA | _ | _ | 70 | |
| | Dropout Voltage (Note 9) | I _{OUT} = 50mA | _ | 110 | 150 | |
| | | | _ | _ | 230 | mV |
| V_{DROP} | | I _{OUT} = 100mA | _ | 140 | 250 | |
| | | | _ | _ | 300 | |
| | | I _{OUT} = 150mA | _ | 165 | 275 | |
| | | | _ | _ | 350 | |
| | | I _{OUT} = 300mA | _ | 250 | 400 | |
| | | | _ | _ | 500 | |
| | | V _{EN} ≤ 0.4V (shutdown) | _ | 0.01 | 1 | |
| I _{STD} | Standby Current | V _{EN} ≤ 0.18V (shutdown) | _ | _ | 5 | μΑ |
| | | | _ | 100 | 150 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 100μA | _ | _ | 180 | 1 |
| | | | _ | 350 | 600 | μΑ |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 50mA | _ | _ | 800 | |
| I_{GND} | Ground Pin Current (Note 10) | V > 0.0V | - | 1.3 | 1.9 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 150mA | _ | - | 2.5 | mA |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 300mA | - | 4 | 10 | |
| | | | - | _ | 15 | |
| PSRR | Ripple Rejection | f = 100Hz, I _{OUT} = 100μA | - | 75 | - | dB |
| I _{LIMIT} | Current Limit | V _{OUT} = 0V | _ | 450 | 900 | mA |



AP2210-2.8 Electrical Characteristics ($V_{IN} = 3.8V$, $I_{OUT} = 100\mu A$, $C_{IN} = 1.0\mu F$, $C_{OUT} = 2.2\mu F$, $V_{EN} \ge 2.0V$, $T_J = +25^{\circ}C$, bold typeface applies over $-40^{\circ}C \le T_J \le +125^{\circ}C$ (Note 6), unless otherwise specified.)

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|-----------------|---------------------------------|---|-----|------|------|----------------|
| e _{no} | Output Noise | $I_{OUT} = 50$ mA, $C_{OUT} = 2.2\mu$ F, 100pF from BYP to GND | _ | 260 | _ | nV/\sqrt{Hz} |
| | | | _ | _ | 0.4 | ., |
| VIL | Enable Input Logic-low Voltage | Regulator shutdown | _ | - | 0.18 | V |
| V_{IH} | Enable Input Logic-high Voltage | Regulator enabled | 2.0 | _ | - | V |
| | | V _{IL} ≤ 0.4V | _ | 0.01 | 1 | |
| I _{IL} | Enable Input Logic-low Current | V _{IL} ≤ 0.18V | _ | _ | 2 | μA |
| | | V _{IL} ≥ 2.0V | _ | 5 | 20 | |
| l _{IH} | Enable Input Logic-high Current | V _{IL} ≥ 2.0V | _ | - | 25 | μA |

- 6. Specifications in bold type are limited to -40°C ≤ T_J ≤ +125°C. Limits over temperature are guaranteed by design, but not tested in production.
- 7. Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.
- 8. Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 0.1mA to 300mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- 9. Dropout voltage is defined as the input to output differential at which the output voltage drops 1% ($T_J = +25^{\circ}C$) or 2% ($-40^{\circ}C \le T_J \le +125^{\circ}C$) below its nominal value measured at 1V differential.
- 10. Ground pin current is the regulator quiescent current plus pass transistor base current. The total current drawn from the supply is the sum of the load current plus the ground pin current.



AP2210-3.0 Electrical Characteristics ($V_{IN} = 4V$, $I_{OUT} = 100\mu A$, $C_{IN} = 1.0\mu F$, $C_{OUT} = 2.2\mu F$, $V_{EN} \ge 2.0V$, $T_J = +25^{\circ}C$, **bold** typeface applies over $-40^{\circ}C \le T_J \le +125^{\circ}C$ (Note 6), unless otherwise specified.)

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|---|------------------------------|--|-----|------|-----|--------|
| | Outsid Mallage Assures | Mariatian franco an ariford M | -1 | _ | 1 | 0, |
| $\Delta V_{	ext{OUT}} / V_{	ext{OUT}}$ | Output Voltage Accuracy | Variation from specified V _{OUT} | -2 | _ | 2 | % |
| $\Delta V_{OUT}/\Delta T$ | Output Voltage Temperature | - | _ | 120 | - | μV/°C |
| (ΔV _{OUT} /V _{OUT})/ΔΤ | Coefficient (Note 7) | _ | _ | 40 | - | ppm/°C |
| ., | 1: 5 1: | V 4V/15 40 0V/ | _ | 1.5 | 4.5 | ., |
| V_{RLINE} | Line Regulation | V _{IN} = 4V to 13.2V | _ | _ | 12 | mV |
| ., | | | _ | 1 | 6 | ., |
| V _{RLOAD} | Load Regulation (Note 8) | I _{OUT} = 0.1mA to 300mA | _ | _ | 30 | mV |
| | | | _ | 15 | 50 | |
| | | I _{OUT} = 100μA | _ | _ | 70 | |
| | Dropout Voltage (Note 9) | I _{OUT} = 50mA | _ | 110 | 150 | |
| V_{DROP} | | | _ | _ | 230 | mV |
| | | I _{OUT} = 100mA | _ | 140 | 250 | |
| | | | _ | _ | 300 | |
| | | I _{OUT} = 150mA | _ | 165 | 275 | |
| | | | _ | _ | 350 | |
| | | I _{OUT} = 300mA | _ | 250 | 400 | |
| | | | _ | _ | 500 | |
| | | V _{EN} ≤ 0.4V (shutdown) | _ | 0.01 | 1 | |
| I _{STD} | Standby Current | V _{EN} ≤ 0.18V (shutdown) | _ | _ | 5 | μΑ |
| | | | _ | 100 | 150 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 100μA | _ | _ | 180 | 1 |
| | | | _ | 350 | 600 | μΑ |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 50mA | _ | _ | 800 | |
| I_{GND} | Ground Pin Current (Note 10) | | - | 1.3 | 1.9 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 150mA | _ | - | 2.5 | mA |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 300mA | - | 4 | 10 | |
| | | | _ | _ | 15 | |
| PSRR | Ripple Rejection | f = 100Hz, I _{OUT} = 100μA | - | 75 | - | dB |
| I _{LIMIT} | Current Limit | V _{OUT} = 0V | _ | 450 | 900 | mA |



AP2210-3.0 Electrical Characteristics ($V_{IN} = 4V$, $I_{OUT} = 100\mu A$, $C_{IN} = 1.0\mu F$, $C_{OUT} = 2.2\mu F$, $V_{EN} \ge 2.0V$, $T_J = +25^{\circ}C$, bold typeface applies over $-40^{\circ}C \le T_J \le +125^{\circ}C$ (Note 6), unless otherwise specified.)

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|-----------------|---------------------------------|---|-----|------|------|----------------|
| e _{no} | Output Noise | I _{OUT} = 50mA, C _{OUT} = 2.2μF, 100pF from BYP to GND | - | 260 | _ | nV/\sqrt{Hz} |
| ., | Enable Input Logic-low Voltage | | _ | _ | 0.4 | ., |
| V _{IL} | | Regulator shutdown | _ | - | 0.18 | V |
| V_{IH} | Enable Input Logic-high Voltage | Regulator enabled | 2.0 | - | - | V |
| | | V _{IL} ≤ 0.4V | _ | 0.01 | 1 | |
| I _{IL} | Enable Input Logic-low Current | V _{IL} ≤ 0.18V | _ | _ | 2 | μA |
| | | V _{IL} ≥ 2.0V | _ | 5 | 20 | μА |
| l _{IH} | Enable Input Logic-high Current | V _{IL} ≥ 2.0V | _ | - | 25 | |

- 6. Specifications in bold type are limited to -40°C ≤ T_J ≤ +125°C. Limits over temperature are guaranteed by design, but not tested in production.
- 7. Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.
- 8. Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 0.1mA to 300mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- 9. Dropout voltage is defined as the input to output differential at which the output voltage drops 1% ($T_J = +25^{\circ}C$) or 2% ($-40^{\circ}C \le T_J \le +125^{\circ}C$) below its nominal value measured at 1V differential.
- 10. Ground pin current is the regulator quiescent current plus pass transistor base current. The total current drawn from the supply is the sum of the load current plus the ground pin current.



AP2210-3.3 Electrical Characteristics ($V_{IN} = 4.3V$, $I_{OUT} = 100\mu$ A, $C_{IN} = 1.0\mu$ F, $C_{OUT} = 2.2\mu$ F, $V_{EN} \ge 2.0V$, $T_{J} = +25^{\circ}$ C, bold typeface applies over -40°C $\le T_{J} \le +125^{\circ}$ C (Note 6), unless otherwise specified.)

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|-----------------------|------------------------------|--|-----|------|-----|--------|
| | Q | V | -1 | _ | 1 | 0/ |
| ΔVουτ/Vουτ | Output Voltage Accuracy | Variation from specified V _{OUT} | -2 | - | 2 | % |
| ΔV _{OUT} /ΔΤ | Output Voltage Temperature | - | _ | 120 | - | μV/°C |
| (ΔVουτ/Vουτ)/ΔΤ | Coefficient (Note 7) | - | _ | 36.3 | - | ppm/°C |
| | 1. 5 1. | V 40V/5 40 0V/ | _ | 1.5 | 4.5 | |
| V_{RLINE} | Line Regulation | V _{IN} = 4.3V to 13.2V | _ | _ | 12 | mV |
| V | Load Damileties (Nate 0) | 0.4 | _ | 1 | 6 | |
| V _{RLOAD} | Load Regulation (Note 8) | I _{OUT} = 0.1mA to 300mA | _ | - | 30 | mV |
| | | 400-4 | _ | 15 | 50 | |
| | Dropout Voltage (Note 9) | I _{OUT} = 100μA | _ | _ | 70 | |
| | | 504 | _ | 110 | 150 | |
| | | I _{OUT} = 50mA | _ | - | 230 | m∨ |
| V | | I _{OUT} = 100mA | _ | 140 | 250 | |
| V_{DROP} | | | _ | - | 300 | |
| | | I _{OUT} = 150mA | _ | 165 | 275 | |
| | | | _ | - | 350 | |
| | | | _ | 250 | 400 | |
| | | I _{OUT} = 300mA | _ | - | 500 | |
| | | V _{EN} ≤ 0.4V (shutdown) | _ | 0.01 | 1 | |
| I _{STD} | Standby Current | V _{EN} ≤ 0.18V (shutdown) | _ | - | 5 | μA |
| | | | _ | 100 | 150 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 100μA | _ | - | 180 |] |
| | | | _ | 350 | 600 | μA |
| | 0 15 0 17 17 | V _{EN} ≥ 2.0V, I _{OUT} = 50mA | - | - | 800 | |
| I _{GND} | Ground Pin Current (Note 10) | V - 20V I - 175 1 | - | 1.3 | 1.9 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 150mA | _ | _ | 2.5 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 300mA | _ | 4 | 10 | mA |
| | | | _ | - | 15 | |
| PSRR | Ripple Rejection | f = 100Hz, I _{OUT} = 100μA | - | 75 | _ | dB |
| I _{LIMIT} | Current Limit | V _{OUT} = 0V | _ | 450 | 900 | mA |

June 2016 © Diodes Incorporated



Electrical Characteristics (Cont.)

AP2210-3.3 Electrical Characteristics ($V_{IN} = 4.3V$, $I_{OUT} = 100\mu A$, $C_{IN} = 1.0\mu F$, $C_{OUT} = 2.2\mu F$, $V_{EN} \ge 2.0V$, $T_{J} = +25$ °C, bold typeface applies over -40°C ≤ $T_{J} \le +125$ °C (Note 6), unless otherwise specified.)

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|-----------------|---------------------------------|---|-----|------|------|----------------|
| e _{no} | Output Noise | I _{OUT} = 50mA, C _{OUT} = 2.2μF, 100pF from BYP to GND | - | 260 | _ | nV/\sqrt{Hz} |
| ., | Enable Input Logic-low Voltage | | _ | _ | 0.4 | ., |
| V _{IL} | | Regulator shutdown | _ | - | 0.18 | V |
| V_{IH} | Enable Input Logic-high Voltage | Regulator enabled | 2.0 | - | - | V |
| | | V _{IL} ≤ 0.4V | _ | 0.01 | 1 | |
| I _{IL} | Enable Input Logic-low Current | V _{IL} ≤ 0.18V | _ | _ | 2 | μA |
| | | V _{IL} ≥ 2.0V | _ | 5 | 20 | μА |
| l _{IH} | Enable Input Logic-high Current | V _{IL} ≥ 2.0V | _ | - | 25 | |

- 6. Specifications in bold type are limited to -40°C ≤ T_J ≤ +125°C. Limits over temperature are guaranteed by design, but not tested in production.
- 7. Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.
- 8. Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 0.1mA to 300mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- 9. Dropout voltage is defined as the input to output differential at which the output voltage drops 1% ($T_J = +25^{\circ}C$) or 2% ($-40^{\circ}C \le T_J \le +125^{\circ}C$) below its nominal value measured at 1V differential.
- 10. Ground pin current is the regulator quiescent current plus pass transistor base current. The total current drawn from the supply is the sum of the load current plus the ground pin current.



AP2210-3.6 Electrical Characteristics ($V_{IN} = 4.6V$, $I_{OUT} = 100\mu$ A, $C_{IN} = 1.0\mu$ F, $C_{OUT} = 2.2\mu$ F, $V_{EN} \ge 2.0V$, $T_{J} = +25^{\circ}$ C, bold typeface applies over -40°C $\le T_{J} \le +125^{\circ}$ C (Note 6), unless otherwise specified.)

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|-----------------------|------------------------------|--|-----|------|-----|--------|
| | 0 | V | -1 | _ | 1 | 0/ |
| ΔVουτ/Vουτ | Output Voltage Accuracy | Variation from specified V _{OUT} | -2 | _ | 2 | % |
| ΔV _{OUT} /ΔΤ | Output Voltage Temperature | _ | _ | 120 | - | μV/°C |
| (ΔVουτ/Vουτ)/ΔΤ | Coefficient (Note 7) | _ | _ | 48 | - | ppm/°C |
| \ <u>'</u> | Line Berndeline | V 4 CV 45 42 OV | _ | 1.5 | 4.5 | \/ |
| V_{RLINE} | Line Regulation | V _{IN} = 4.6V to 13.2V | _ | _ | 12 | mV |
| V | Load Damietics (Nata 0) | 0.44.10004 | _ | 1 | 6 | \/ |
| V _{RLOAD} | Load Regulation (Note 8) | I _{OUT} = 0.1mA to 300mA | _ | _ | 30 | mV |
| | | 1004 | _ | 15 | 50 | |
| | Dropout Voltage (Note 9) | I _{OUT} = 100μA | _ | _ | 70 | |
| | | | _ | 110 | 150 | |
| | | I _{OUT} = 50mA | - | _ | 230 | |
| V | | I _{OUT} = 100mA | _ | 140 | 250 | mV |
| V_{DROP} | | | - | _ | 300 | |
| | | I _{OUT} = 150mA | _ | 165 | 275 | |
| | | | _ | _ | 350 | |
| | | | _ | 250 | 400 | |
| | | I _{OUT} = 300mA | _ | _ | 500 | |
| | | V _{EN} ≤ 0.4V (shutdown) | - | 0.01 | 1 | |
| I _{STD} | Standby Current | V _{EN} ≤ 0.18V (shutdown) | - | _ | 5 | μA |
| | | | _ | 100 | 150 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 100μA | _ | _ | 180 | |
| | | | _ | 350 | 600 | μA |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 50mA | _ | _ | 800 | |
| I_{GND} | Ground Pin Current (Note 10) | | - | 1.3 | 1.9 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 150mA | _ | _ | 2.5 | mA |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 300mA | - | 4 | 10 | |
| | | | - | _ | 15 | |
| PSRR | Ripple Rejection | f = 100Hz, I _{OUT} = 100μA | - | 75 | - | dB |
| I _{LIMIT} | Current Limit | V _{OUT} = 0V | _ | 450 | 900 | mA |



AP2210-3.6 Electrical Characteristics ($V_{IN} = 4.6V$, $I_{OUT} = 100\mu A$, $C_{IN} = 1.0\mu F$, $C_{OUT} = 2.2\mu F$, $V_{EN} \ge 2.0V$, $T_J = +25^{\circ}C$, bold typeface applies over $-40^{\circ}C \le T_J \le +125^{\circ}C$ (Note 6), unless otherwise specified.)

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|-----------------|---------------------------------|---|-----|------|------|----------------|
| e _{no} | Output Noise | Output Noise $ I_{OUT} = 50 \text{mA}, C_{OUT} = 2.2 \mu \text{F}, $ $100 \text{pF from BYP to GND} $ | | 260 | _ | nV/\sqrt{Hz} |
| ., | | | _ | _ | 0.4 | ., |
| V _{IL} | Enable Input Logic-low Voltage | Regulator shutdown | _ | - | 0.18 | V |
| V_{IH} | Enable Input Logic-high Voltage | Enable Input Logic-high Voltage Regulator enabled | | - | - | V |
| | | V _{IL} ≤ 0.4V | - | 0.01 | 1 | |
| I _{IL} | Enable Input Logic-low Current | V _{IL} ≤ 0.18V | - | _ | 2 | μA |
| | | V _{IL} ≥ 2.0V | _ | 5 | 20 | |
| Іін | Enable Input Logic-high Current | V _{IL} ≥ 2.0V | _ | _ | 25 | μA |

- 6. Specifications in bold type are limited to -40°C ≤ T_J ≤ +125°C. Limits over temperature are guaranteed by design, but not tested in production.
- 7. Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.
- 8. Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 0.1mA to 300mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- 9. Dropout voltage is defined as the input to output differential at which the output voltage drops 1% ($T_J = +25^{\circ}C$) or 2% ($-40^{\circ}C \le T_J \le +125^{\circ}C$) below its nominal value measured at 1V differential.
- 10. Ground pin current is the regulator quiescent current plus pass transistor base current. The total current drawn from the supply is the sum of the load current plus the ground pin current.



AP2210-4.0 Electrical Characteristics ($V_{IN} = 5.0V$, $I_{OUT} = 100\mu A$, $C_{IN} = 1.0\mu F$, $C_{OUT} = 2.2\mu F$, $V_{EN} \ge 2.0V$, $T_J = +25^{\circ}C$, bold typeface applies over $-40^{\circ}C \le T_J \le +125^{\circ}C$ (Note 6), unless otherwise specified.)

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit | |
|---|------------------------------|--|-------|------|-----|--------|--|
| | Outsid Mallage Assures | Mariatian franco an aiffed M | -1 | _ | 1 | 0, | |
| $\Delta V_{OUT}/V_{OUT}$ | Output Voltage Accuracy | Variation from specified V _{OUT} | -2 | _ | 2 | % | |
| ΔV _{OUT} /ΔΤ | Output Voltage Temperature | - | _ | 120 | - | μV/°C | |
| (ΔV _{OUT} /V _{OUT})/ΔΤ | Coefficient (Note 7) | _ | _ | 48 | - | ppm/°C | |
| V | 1: 5 1: | V 5.0V (5.40.0V (| _ | 1.5 | 4.5 | | |
| V_{RLINE} | Line Regulation | V _{IN} = 5.0V to 13.2V | _ | _ | 12 | mV | |
| ., | | | _ | 1 | 6 | ., | |
| V_{RLOAD} | Load Regulation (Note 8) | I _{OUT} = 0.1mA to 300mA | _ | _ | 30 | mV | |
| V_{DROP} | | | _ | 15 | 50 | | |
| | | I _{OUT} = 100μA | _ | _ | 70 | | |
| | | I _{OUT} = 50mA | _ | 110 | 150 | mV | |
| | | | _ | _ | 230 | | |
| | 2 | I _{OUT} = 100mA | _ | 140 | 250 | | |
| | Dropout Voltage (Note 9) | | _ | _ | 300 | | |
| | | I _{OUT} = 150mA | _ | 165 | 275 | | |
| | | | _ | _ | 350 | | |
| | | I _{OUT} = 300mA | _ | 250 | 400 | | |
| | | | _ | _ | 500 | | |
| | | V _{EN} ≤ 0.4V (shutdown) | _ | 0.01 | 1 | | |
| I _{STD} | Standby Current | V _{EN} ≤ 0.18V (shutdown) | _ | _ | 5 | μA | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 100μA | _ | 100 | 150 | | |
| | | | _ | _ | 180 | | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 50mA | - 350 | 600 | μΑ | | |
| I _{GND} | | | _ | 800 | | | |
| | Ground Pin Current (Note 10) | V - 0.0V I | - | 1.3 | 1.9 | | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 150mA | _ | - | 2.5 | mA | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 300mA | - | 4 | 10 | | |
| | | | - | _ | 15 | | |
| PSRR | Ripple Rejection | the Rejection $f = 100$ Hz, $I_{OUT} = 100\mu$ A | | 75 | - | dB | |
| I _{LIMIT} | Current Limit | V _{OUT} = 0V | _ | 450 | 900 | mA | |



AP2210-4.0 Electrical Characteristics ($V_{IN} = 5.0V$, $I_{OUT} = 100\mu A$, $C_{IN} = 1.0\mu F$, $C_{OUT} = 2.2\mu F$, $V_{EN} \ge 2.0V$, $T_{J} = +25$ °C, bold typeface applies over -40°C ≤ $T_{J} \le +125$ °C (Note 6), unless otherwise specified.)

| Symbol | Parameter Conditions | | Min | Тур | Max | Unit | |
|-----------------|--|---|-----|------|------|----------------|--|
| e _{no} | Output Noise $ I_{OUT} = 50 \text{mA}, C_{OUT} = 2.2 \mu \text{F}, $ $ 100 \text{pF from BYP to GND} $ | | - | 260 | _ | nV/\sqrt{Hz} | |
| ., | | | _ | _ | 0.4 | ., | |
| VIL | Enable Input Logic-low Voltage | Regulator shutdown | _ | - | 0.18 | V | |
| V_{IH} | Enable Input Logic-high Voltage | Enable Input Logic-high Voltage Regulator enabled | | - | - | V | |
| | Enable Input Logic-low Current | V _{IL} ≤ 0.4V | _ | 0.01 | 1 | | |
| I _{IL} | | V _{IL} ≤ 0.18V | _ | _ | 2 | μA | |
| | | V _{IL} ≥ 2.0V | _ | 5 | 20 | | |
| l _{IH} | Enable Input Logic-high Current | V _{IL} ≥ 2.0V | _ | _ | 25 | μΑ | |

- 6. Specifications in bold type are limited to -40°C ≤ T_J ≤ +125°C. Limits over temperature are guaranteed by design, but not tested in production.
- 7. Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.
- 8. Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 0.1mA to 300mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- 9. Dropout voltage is defined as the input to output differential at which the output voltage drops 1% ($T_J = +25^{\circ}C$) or 2% ($-40^{\circ}C \le T_J \le +125^{\circ}C$) below its nominal value measured at 1V differential.
- 10. Ground pin current is the regulator quiescent current plus pass transistor base current. The total current drawn from the supply is the sum of the load current plus the ground pin current.



AP2210-5.0 Electrical Characteristics ($V_{IN} = 6.0V$, $I_{OUT} = 100\mu A$, $C_{IN} = 1.0\mu F$, $C_{OUT} = 2.2\mu F$, $V_{EN} \ge 2.0V$, $T_J = +25^{\circ}C$, bold typeface applies over $-40^{\circ}C \le T_J \le +125^{\circ}C$ (Note 6), unless otherwise specified.)

| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|---|------------------------------|--|---|------|-----|--------|
| | | | -1 | _ | 1 | 0, |
| $\Delta V_{	ext{OUT}}/V_{	ext{OUT}}$ | Output Voltage Accuracy | Variation from specified V _{OUT} | -2 | _ | 2 | % |
| ΔV _{OUT} /ΔΤ | Output Voltage Temperature | - | _ | 120 | - | μV/°C |
| (ΔV _{OUT} /V _{OUT})/ΔΤ | Coefficient (Note 7) | _ | _ | 48 | - | ppm/°C |
| ., | l: B 16 | V 0.0V/1- 40.0V/ | _ | 1.5 | 4.5 | |
| V_{RLINE} | Line Regulation | $V_{IN} = 6.0V \text{ to } 13.2V$ | _ | _ | 12 | mV |
| ., | | | _ | 1 | 6 | ., |
| V_{RLOAD} | Load Regulation (Note 8) | I _{OUT} = 0.1mA to 300mA | _ | _ | 30 | mV |
| V_{DROP} | | | _ | 15 | 50 | |
| | | Ι _{ΟυΤ} = 100μΑ | _ | _ | 70 | |
| | | I _{OUT} = 50mA | _ | 110 | 150 | mV |
| | | | _ | _ | 230 | |
| | 2 | I _{OUT} = 100mA | _ | 140 | 250 | |
| | Dropout Voltage (Note 9) | | _ | _ | 300 | |
| | | I _{OUT} = 150mA | _ | 165 | 275 | |
| | | | _ | _ | 350 | |
| | | I _{OUT} = 300mA | _ | 250 | 400 | |
| | | | _ | _ | 500 | |
| | | V _{EN} ≤ 0.4V (shutdown) | _ | 0.01 | 1 | |
| I _{STD} | Standby Current | V _{EN} ≤ 0.18V (shutdown) | _ | _ | 5 | μA |
| | | | _ | 100 | 150 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 100μA | _ | _ | 180 | μΑ |
| IGND | | V _{EN} ≥ 2.0V, I _{OUT} = 50mA | _ | 350 | 600 | |
| | | | V _{EN} ≥ 2.0V, I _{OUT} = 50mA | _ | 800 | |
| | Ground Pin Current (Note 10) | | - | 1.3 | 1.9 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 150mA | _ | _ | 2.5 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 300mA | - | 4 | 10 | mA |
| | | | _ | _ | 15 | |
| PSRR | Ripple Rejection | ole Rejection $f = 100Hz$, $I_{OUT} = 100\mu A$ | | 75 | - | dB |
| I _{LIMIT} | Current Limit | V _{OUT} = 0V | _ | 450 | 900 | mA |



AP2210-5.0 Electrical Characteristics ($V_{IN} = 6.0V$, $I_{OUT} = 100\mu A$, $C_{IN} = 1.0\mu F$, $C_{OUT} = 2.2\mu F$, $V_{EN} \ge 2.0V$, $T_{J} = +25^{\circ}C$, bold typeface applies over -40°C ≤ $T_{J} \le +125^{\circ}C$ (Note 6), unless otherwise specified.)

| Symbol | Parameter Conditions | | Min | Тур | Max | Unit | |
|-----------------|--|---|-----|------|------|----------------|--|
| e _{no} | Output Noise $ I_{OUT} = 50 \text{mA}, C_{OUT} = 2.2 \mu \text{F}, $ $ 100 \text{pF from BYP to GND} $ | | - | 260 | _ | nV/\sqrt{Hz} | |
| ., | | | _ | _ | 0.4 | ., | |
| VIL | Enable Input Logic-low Voltage | Regulator shutdown | _ | - | 0.18 | V | |
| V_{IH} | Enable Input Logic-high Voltage | Enable Input Logic-high Voltage Regulator enabled | | - | - | V | |
| | Enable Input Logic-low Current | V _{IL} ≤ 0.4V | _ | 0.01 | 1 | | |
| I _{IL} | | V _{IL} ≤ 0.18V | _ | _ | 2 | μA | |
| | | V _{IL} ≥ 2.0V | _ | 5 | 20 | | |
| l _{IH} | Enable Input Logic-high Current | V _{IL} ≥ 2.0V | _ | _ | 25 | μΑ | |

- 6. Specifications in bold type are limited to -40°C ≤ T_J ≤ +125°C. Limits over temperature are guaranteed by design, but not tested in production.
- 7. Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.
- 8. Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 0.1mA to 300mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- 9. Dropout voltage is defined as the input to output differential at which the output voltage drops 1% ($T_J = +25^{\circ}C$) or 2% ($-40^{\circ}C \le T_J \le +125^{\circ}C$) below its nominal value measured at 1V differential.
- 10. Ground pin current is the regulator quiescent current plus pass transistor base current. The total current drawn from the supply is the sum of the load current plus the ground pin current.



AP2210-ADJ Electrical Characteristics ($V_{IN} = V_{OUT} + 1V$, $I_{OUT} = 100\mu$ A, $C_{IN} = 1.0\mu$ F, $C_{OUT} = 2.2\mu$ F, $V_{EN} \ge 2.0V$, $T_J = +25^{\circ}$ C, bold typeface applies over -40°C $\le T_J \le +125^{\circ}$ C (Note 6), unless otherwise specified.)

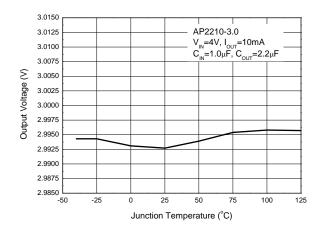
| Symbol | Parameter | Conditions | Min | Тур | Max | Unit |
|---|----------------------------------|---|-----|------|------|----------------|
| AN | Outset Value as Assume | Variation from an arifical V | -1 | _ | 1 | 0, |
| ΔV _{OUT} /V _{OUT} | Output Voltage Accuracy | Variation from specified V _{OUT} | -2 | _ | 2 | % |
| ΔV _{OUT} /ΔΤ | Output Voltage Temperature | _ | _ | 120 | _ | μV/°C |
| (ΔV _{ΟυΤ} /V _{ΟυΤ})/ΔΤ | Coefficient (Note 7) | _ | _ | 48 | _ | ppm/°C |
| \/ | Line Degulation | \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\ | _ | 1.5 | 4.5 | ., |
| VRLINE | Line Regulation | $V_{IN} = V_{OUT} + 1V$ to 13.2V | _ | _ | 12 | mV |
| V | Load Population (Note 9) | I _{OUT} = 0.1mA to 300mA | _ | 1 | 6 | mV |
| V _{RLOAD} Load Regulation (Note 8) | | 1007 = 0.1111A to 30011A | _ | _ | 30 | IIIV |
| 1 | Standby Current | V _{EN} ≤ 0.4V (shutdown) | _ | 0.01 | 1 | |
| I _{STD} | Standby Current | V _{EN} ≤ 0.18V (shutdown) | _ | _ | 5 | μA |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 100μA | _ | 100 | 150 | - - μΑ |
| | | | _ | _ | 180 | |
| | | \/ > 2 0\/ lov= = 50m/\ | _ | 350 | 600 | |
| | Crowned Bin Comment (Nata 40) | V _{EN} ≥ 2.0V, I _{OUT} = 50mA | _ | _ | 800 | |
| I_GND | Ground Pin Current (Note 10) | V _{EN} ≥ 2.0V, I _{OUT} = 150mA | _ | 1.3 | 1.9 | mA |
| | | | _ | _ | 2.5 | |
| | | \/> 2.0\/ 300m/ | _ | 4 | 10 | |
| | | V _{EN} ≥ 2.0V, I _{OUT} = 300mA | _ | _ | 15 | |
| PSRR | Ripple Rejection | f = 100Hz, I _{OUT} = 100μA | _ | 75 | | dB |
| I _{LIMIT} | Current Limit | V _{OUT} = 0V | _ | 450 | 900 | mA |
| e _{no} | Output Noise | $I_{OUT} = 50$ mA, $C_{OUT} = 2.2\mu$ F, 100pF from BYP to GND | _ | 260 | _ | nV/\sqrt{Hz} |
| .,, | Facility has still a significant | Da mulatan abad da ma | _ | _ | 0.4 | ., |
| VIL | Enable Input Logic-low Voltage | Regulator shutdown | _ | _ | 0.18 | V |
| V _{IH} | Enable Input Logic-high Voltage | Regulator enabled | 2.0 | _ | _ | V |
| I | Enable Input Logic Iou Current | V _{IL} ≤ 0.4V | _ | 0.01 | 1 | ^ |
| I _{IL} | Enable Input Logic-low Current | V _{IL} ≤ 0.18V | _ | _ | 2 | μA |
| | Enoble Input Logic high Course | V _{IL} ≥ 2.0V | _ | 5 | 20 | |
| Іін | Enable Input Logic-high Current | V _{IL} ≥ 2.0V | _ | _ | 25 | μΑ |

- 6. Specifications in bold type are limited to $-40^{\circ}\text{C} \le T_{\text{J}} \le +125^{\circ}\text{C}$. Limits over temperature are guaranteed by design, but not tested in production.
- 7. Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.
- 8. Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 0.1mA to 300mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- 10. Ground pin current is the regulator quiescent current plus pass transistor base current. The total current drawn from the supply is the sum of the load current plus the ground pin current.

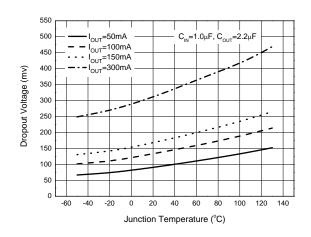


Performance Characteristics

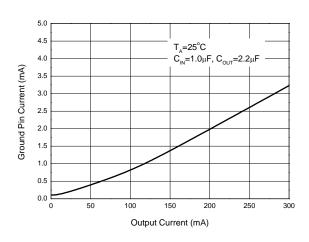
Output Voltage vs. Junction Temperature



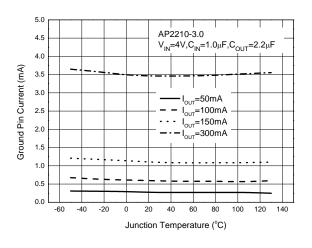
Dropout Voltage vs. Junction Temperature



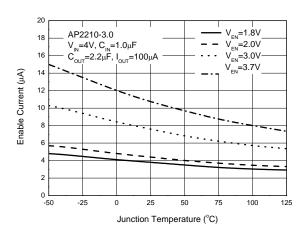
Ground Pin Current vs. Output Current



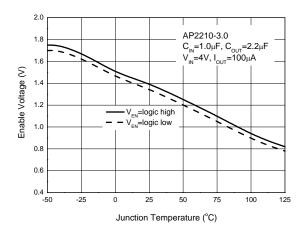
Ground Pin Current vs. Junction Temperature



Enable Current vs. Junction Temperature



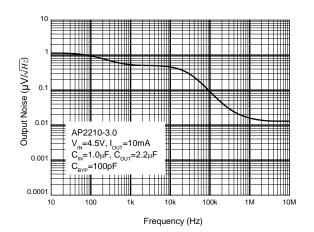
Enable Voltage vs. Junction Temperature



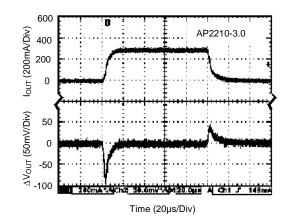


Performance Characteristics (Cont.)

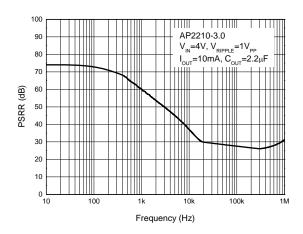
Output Noise vs. Frequency



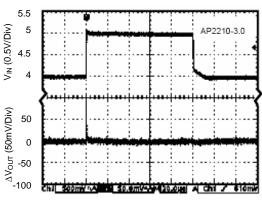
 $\label{eq:lower} Load\ Transient$ (Conditions: V_IN = 4V, V_EN = 2V, I_{OUT} = 10mA to 300mA, C_IN = 1.0 \mu F, C_{OUT} = 2.2 \mu F)



PSRR vs. Frequency

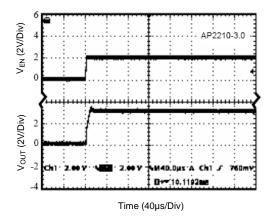


 $\label{eq:line_line_line} Line Transient $$ (Conditions: V_{IN} = 4 \text{ to } 5V, V_{EN} = 2V, \\ I_{OUT} = 1mA, C_{OUT} = 2.2 \mu F) $$$

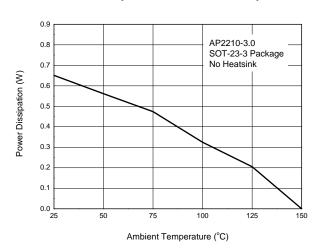


Time (20µs/Div)

 $V_{EN} \ vs. \ V_{OUT}$ (Conditions: $V_{EN}=0$ to 2V, $V_{IN}=4V$, $I_{OUT}=30mA$, $C_{IN}=1.0\mu F$, $C_{OUT}=2.2\mu F$)



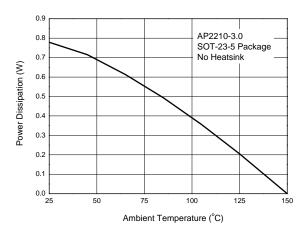
Power Dissipation vs. Ambient Temperature



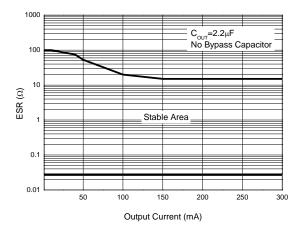


Performance Characteristics (Cont.)

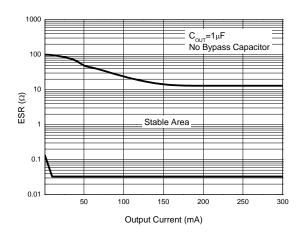
Power Dissipation vs. Ambient Temperature



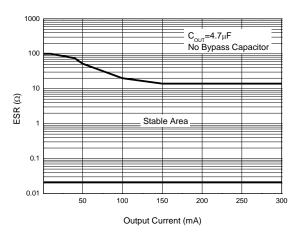
ESR vs. Output Current



ESR vs. Output Current



ESR vs. Output Current





Application Information

Input Capacitor

A $1\mu F$ minimum capacitor is recommended to be placed between V_{IN} and GND.

Output Capacitor

It is required to prevent oscillation. $1.0\mu\text{F}$ minimum is recommended when C_{BYP} is unused. $2.2\mu\text{F}$ minimum is recommended when C_{BYP} is 100pF. The output capacitor may be increased to improve transient response.

Noise Bypass Capacitor

Bypass capacitor is connected to the internal voltage reference. A small capacitor connected from BYP to GND make this reference quiet, resulting in a significant reduction in output noise, but the ESR stable area will be narrowed. In order to keep the output stability, it is recommended to use the bypass capacitor no more than 100pF.

The start-up speed of the AP2210 is inversely proportional to the value of reference bypass capacitor. In some cases, if output noise is not a major concern and rapid turn-on is necessary, omit C_{BYP} and leave BYP open.

Power Dissipation

Thermal shutdown may take place if exceeding the maximum power dissipation in application. Under all possible operating conditions, the junction temperature must be within the range specified under absolute maximum ratings to avoid thermal shutdown.

To determine if the power dissipated in the regulator reaches the maximum power dissipation (see Figure Power Dissipation vs. Ambient Temperature and Figure ESR vs. Output Current in Page 22), using:

 $T_J = P_D^*\theta_{JA} + T_A$

 $P_D = (V_{IN}-V_{OUT})*I_{OUT}+V_{IN}*I_{GND}$

Where: $T_J \le T_{J(max)}$, $T_{J(max)}$ is absolute maximum ratings for the junction temperature; $V_{IN}^*I_{GND}$ can be ignored due to its small value.

T_{J(max)} is +150°C, θ_{JA} is 200°C/W, no heatsink is required since the package alone will dissipate enough heat to satisfy these requirements unless the calculated value for power dissipation exceeds the limit.

Example (3.0V version):

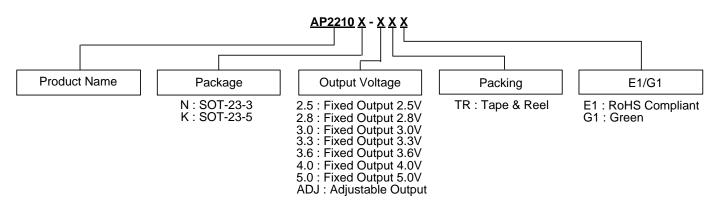
 $I_{OUT} = 300 \text{mA}$, $T_A = +50^{\circ}\text{C}$, $V_{IN(Max)}$ is:

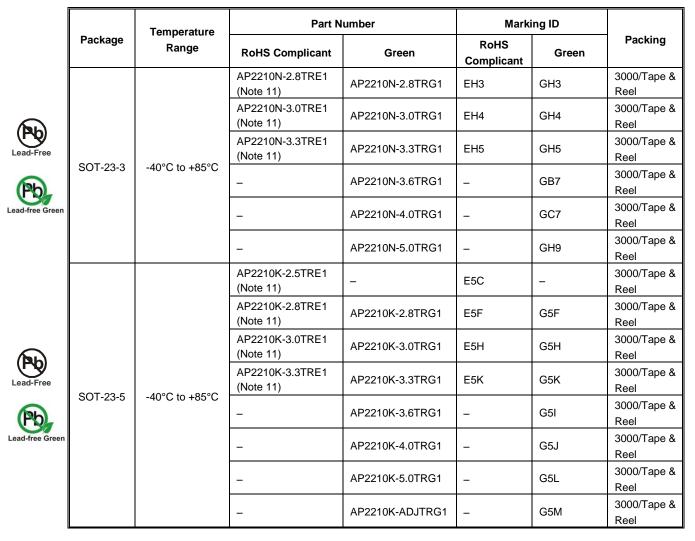
(150°C-50°C)/(0.3A*200°C/W)+3.0V=4.67V

Therefore, for good performance, please make sure that input voltage is less than 4.67V without heatsink when $T_A = +50^{\circ}$ C.



Ordering Information



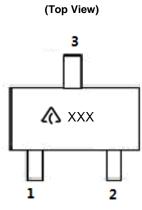


Note 11: Not recommended for new design.



Marking Information

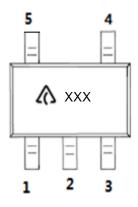
(1) SOT-23-3



♠ : Logo XXX: Marking ID (See Ordering Information)

(2) SOT-23-5



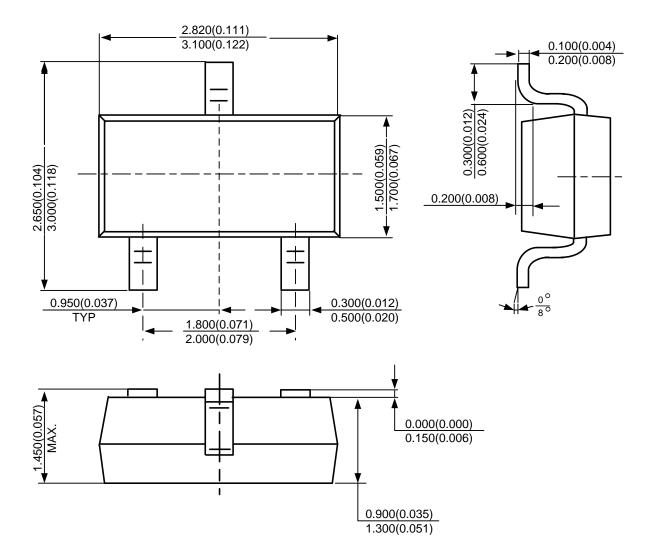


♠ : Logo
XXX: Marking ID
(See Ordering Information)



Package Outline Dimensions (All dimensions in mm(inch).)

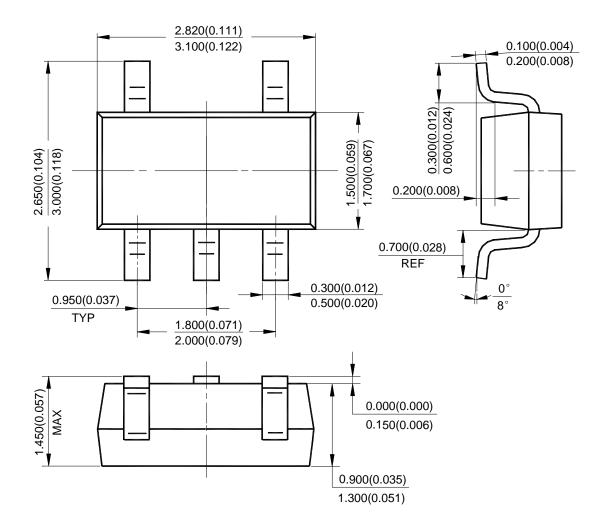
(1) Package Type: SOT-23-3





Package Outline Dimensions (Cont. All dimensions in mm(inch).)

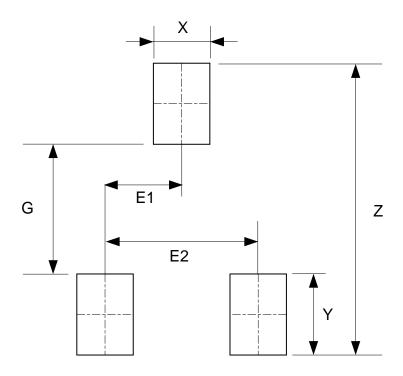
(2) Package Type: SOT-23-5





Suggested Pad Layout

(1) Package Type: SOT-23-3

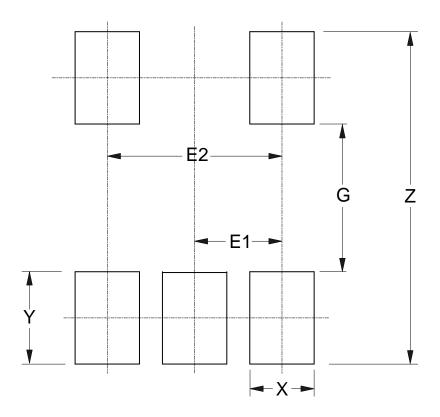


| Dimensions | Z | G | X | Y | E1 | E2 |
|------------|-------------|-------------|-------------|-------------|-------------|-------------|
| | (mm)/(inch) | (mm)/(inch) | (mm)/(inch) | (mm)/(inch) | (mm)/(inch) | (mm)/(inch) |
| Value | 3.600/0.142 | 1.600/0.063 | 0.700/0.028 | 1.000/0.039 | 0.950/0.037 | 1.900/0.075 |



Suggested Pad Layout (Cont.)

(2) Package Type: SOT-23-5



| Dimensions | Z | G | X | Y | E1 | E2 |
|------------|-------------|-------------|-------------|-------------|-------------|-------------|
| | (mm)/(inch) | (mm)/(inch) | (mm)/(inch) | (mm)/(inch) | (mm)/(inch) | (mm)/(inch) |
| Value | 3.600/0.142 | 1.600/0.063 | 0.700/0.028 | 1.000/0.039 | 0.950/0.037 | 1.900/0.075 |



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2016, Diodes Incorporated

www.diodes.com

AP2210 30 of 30 June 2016

Document number: DS37517 Rev. 2 - 2 **www.diodes.com** © Diodes Incorporated

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for LDO Voltage Regulators category:

Click to view products by Diodes Incorporated manufacturer:

Other Similar products are found below:

M38D29FFHP#U1 702103A 717726C 742457H MP20051DN-LF-Z R5F111PGGFB#30 AP7363-SP-13 NCP103AMX285TCG
NCV8664CST33T3G NCV8752AMX28TCG L9454 AP7362-HA-7 LX13043CLD TCR3DF185,LM(CT TCR3DF24,LM(CT
TCR3DF285,LM(CT TCR3DF31,LM(CT TCR3DF45,LM(CT TLF4949EJ MP2013GQ-33-Z L9708 L970813TR 030014BB 059985X
NCP121AMX173TCG NCP4687DH15T1G NCV8703MX30TCG 701326R 702087BB 755078E TCR2EN28,LF(S LM1117DT-1.8/NO
LT1086CM#TRPBF AZ1085S2-1.5TRE1 MAX15101EWL+T NCV8170AXV250T2G SCD337BTG TCR3DF27,LM(CT
TCR3DF19,LM(CT TCR3DF125,LM(CT TCR2EN18,LF(S MAX15103EWL+T TS2937CZ-5.0 C0 MAX8878EUK30-T MAX663CPA
NCV4269CPD50R2G NCV8716MT30TBG AZ1117IH-1.2TRG1 MP2013GQ-P AP2112R5A-3.3TRG1