

DMNH6008SPSQ

60V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET **POWERDI**

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D T _A = +25°C	
60V	$8.0 \text{m}\Omega$ @ $V_{GS} = 10V$	16.5A	

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

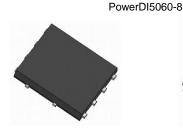
- Motor Control
- **DC-DC Converters**
- Power Management

Features and Benefits

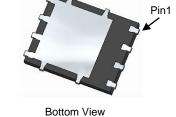
- Thermally Efficient Package-Cooler Running Applications
- High Conversion Efficiency
- Low R_{DS(ON)} Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- <1.1mm Package Profile Ideal for Thin Applications
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

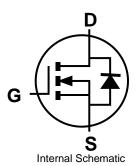
Mechanical Data

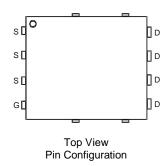
- Case: PowerDI5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.097 grams (Approximate)



Top View







Ordering Information (Note 5)

Part Number	Case	Packaging
DMNH6008SPSQ-13	PowerDI5060-8	2,500 / Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



☐ : Hanufacturer's Marking NH6008SS = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 16 = 2016) WW = Week Code (01 to 52)



Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	60	V		
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 7) \/ 40\/	Steady State	$T_A = +25$ °C $T_A = +100$ °C	I _D	16.5 11.7	А
Continuous Drain Current (Note 7) V _{GS} = 10V	Steady State	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	I _D	88 63	А
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)			I _{DM}	140	Α
Maximum Continuous Body Diode Forward Current (Note 7)			Is	90	Α
Avalanche Current (Note 8) L=0.1mH			I _{AS}	62	Α
Avalanche Energy (Note 8) L=0.1mH			E _{AS}	194	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)		P_D	1.6	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{ hetaJA}$	95	°C/W
Total Power Dissipation (Note 7)		P_{D}	3.3	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	$R_{ hetaJA}$	46	°C/W
Thermal Resistance, Junction to Case (Note 7)		$R_{ heta JC}$	1.6	C/VV
Operating and Storage Temperature Range		$T_{J_i} T_{STG}$	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

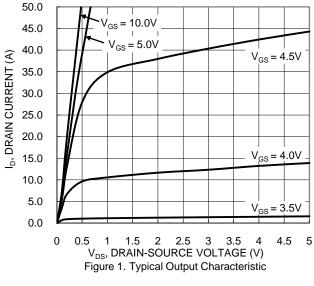
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	I	1	1	μA	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V _{GS(TH)}	2	1	4	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	l	6.0	8.0	mΩ	$V_{GS} = 10V, I_D = 20A$	
Diode Forward Voltage	V_{SD}			1.2	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	C _{iss}	_	2597	_		V _{DS} = 30V, V _{GS} = 0V f = 1.0MHz	
Output Capacitance	Coss	_	437	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	118	_			
Gate Resistance	R_g	_	2.0	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg	_	40.1	_			
Total Gate Charge (V _{GS} = 4.5V)	Q_g	_	21.2	_	nC	$V_{DD} = 30V, I_D = 20A$	
Gate-Source Charge	Qgs	_	8.3	_	nc nc		
Gate-Drain Charge	Q_{gd}	_	11.8	_			
Turn-On Delay Time	t _{D(ON)}	_	5.7	_		$V_{DD} = 30V, V_{GS} = 10V,$ $R_g = 1\Omega, I_D = 20A$	
Turn-On Rise Time	t _R	_	5.0	_			
Turn-Off Delay Time	t _{D(OFF)}		15.6	_	ns		
Turn-Off Fall Time	t _F	_	3.3	_			
Reverse Recovery Time	t _{RR}		33	_	ns I con livit 1000		
Reverse Recovery Charge	Q _{RR}	_	33	_	nC	I _F = 20A, di/dt = 100A/μs	

^{6.} Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
7. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
8. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
9. Short duration pulse test used to minimize self-heating effect.

^{10.} Guaranteed by design. Not subject to product testing.







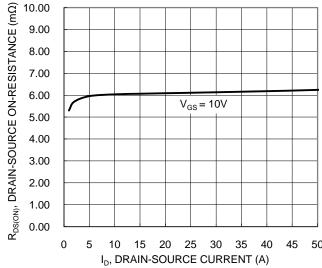


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

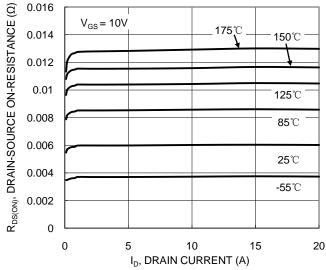
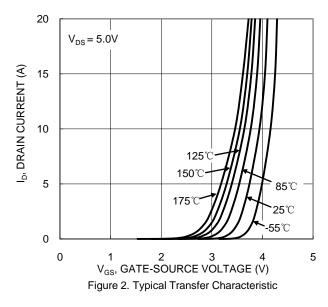
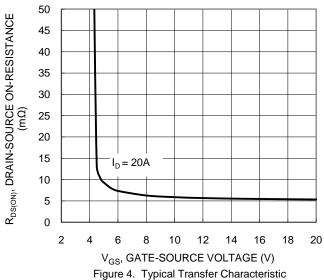


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





2.2 R_{DS(ON)}, DRAIN-SOURCE ON-RESISTANCE (NORMALIZED) 2 1.8 1.6 1.4 1.2 1 $V_{GS} = 10V, I_{D} = 20A$ 0.8 0.6 0.4 -50 50 75 100 125 150 175 25 T_J, JUNCTION TEMPERATURE (°C)

Figure 6. On-Resistance Variation with Temperature





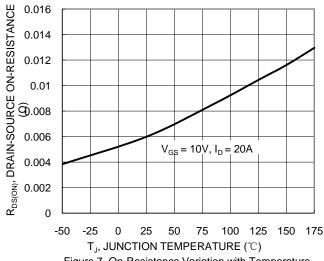


Figure 7. On-Resistance Variation with Temperature

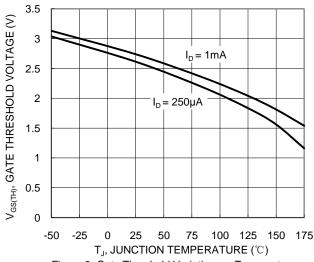


Figure 8. Gate Threshold Variation vs. Temperature

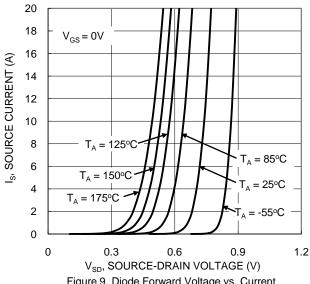
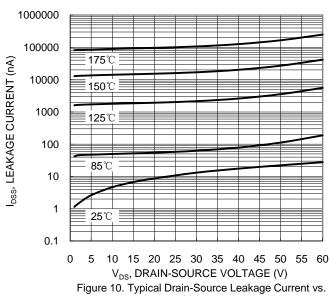
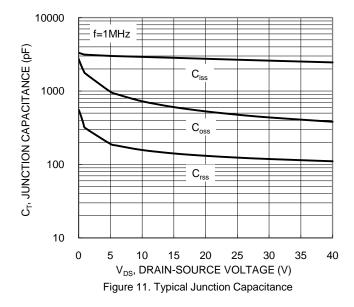


Figure 9. Diode Forward Voltage vs. Current

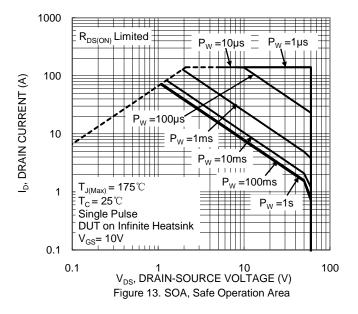


Voltage



10 8 6 $V_{GS}(V)$ 4 $V_{DS} = 30V, I_{D} = 20A$ 2 0 0 10 20 30 40 Q_g (nC) Figure 12. Gate Charge





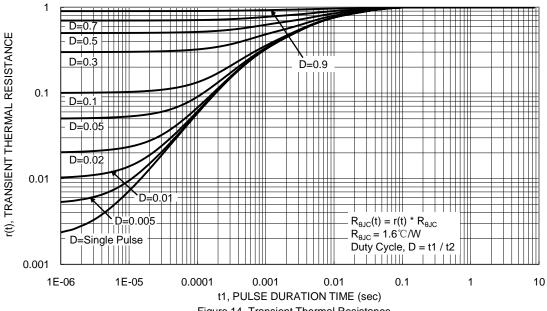


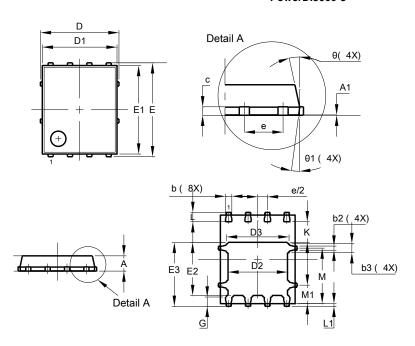
Figure 14. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8

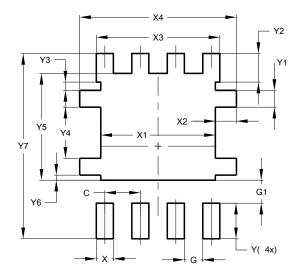


PowerDI5060-8						
Dim	Min Max Typ					
Α	0.90	1.10	1.00			
A1	0.00	0.05	_			
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
С	0.230	0.330	0.277			
D	ļ	5.15 BSC	;			
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90	4.30	4.10			
Е		6.15 BSC	;			
E1	5.60	6.00	5.80			
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
е	1.27 BSC					
G	0.51	0.71	0.61			
K	0.51	_	_			
L	0.51	0.71	0.61			
L1	0.100	0.200	0.175			
M	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
Θ	10°	12º	110			
Θ1	6º	8º	7º			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8



Dimensions	Value (in mm)			
С	1.270			
G	0.660			
G1	0.820			
Χ	0.610			
X1	4.100			
X2	0.755			
Х3	4.420			
X4	5.610			
Υ	1.270			
Y1	0.600			
Y2	1.020			
Y3	0.295			
Y4	1.825			
Y5	3.810			
Y6	0.180			
Y7	6.610			



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