

## **Film Capacitors**

EMI Suppression Capacitors (MKP)

 Series/Type:
 B32032 ... B32036

 Date:
 July 2018

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#### Y2 / 350 V AC

#### B32032 ... B32036

#### **Typical applications**

- Y2 class for interference suppression
- "Line to ground" applications
- Severe Ambient conditions
- Automotive

#### Climatic

- Max. operating temperature: 110 °C
- Climatic category (IEC 60068-1:2013): 40/110/56

#### Construction

- Dielectric: polypropylene (MKP)
- Plastic case (UL 94 V-0)
- Epoxy resin sealing (UL 94 V-0)

#### Features

- THB Grade IIIb<sup>1)</sup>
- Small dimensions
- Good self-healing properties
- High voltage capability
- RoHS-compatible
- Halogen-free capacitors available on request
- AEC-Q200D compliant

#### Terminals

- Parallel wire leads, lead-free tinned
- Special lead lengths available on request

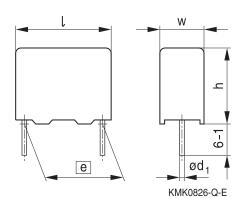
#### Marking

Manufacturer's logo, lot number, date code, rated capacitance (coded), capacitance tolerance (code letter), rated AC voltage (IEC), series number, sub-class (Y2), dielectric code (MKP), climatic category, passive flammability category, approvals.

#### **Delivery mode**

Bulk (untaped) Taped (Ammo pack or reel) For taping details, refer to chapter "Taping and packing"

#### **Dimensional drawing**



Dimensions in mm

Lead spacing <i>e</i> ±0.4	Lead diameter d <sub>1</sub> ±0.05	Туре
15 27.5	0.8	B32032 B32034
37.5	1.0	B32036

## Marking example (position of marks may vary):

🚖 Z 123123123 A
XX - μ39 M 350V~
B32034 Y2 MKP/SH
40/110/56/B
<b>₩15</b> c <b>A</b> Uus
KMK2005

KMK2005-I

1) Certification ongoing and pending acc. to IEC60384-14AMD1:2016



Y2 / 350 V AC

## Y2

#### Approvals

Appr. marks	Standards	Certificate
<b>E</b> 15	EN 60384-14:2014 / IEC 60384-14:2013	ENEC-01642 (approved by UL Demko)
c <b>Al</b> us	UL 60384-14:2014 / CSA E60384-14:2013	E97863

### Overview of available types

Lead spacing	15 mm	22.5 mm	27.5 mm	37.5 mm
Туре	B32032	B32033	B32034	B32036
C <sub>R</sub> (μF)				
0.0047				
0.0056				
0.0068				
0.0082				
0.010				
0.015				
0.018				
0.022				
0.027				
0.033				
0.039				
0.047				
0.056				
0.068				
0.082				
0.10				
0.12				
0.15				
0.18				
0.22				
0.27				
0.33				
0.39				
0.47				
0.56				
0.68				
0.82				
1.0				
1.2				



Y2

#### B32032 ... B32036

Y2 / 350 V AC

#### Ordering codes and packing units

Lead	C <sub>R</sub>	Max. dimensions	Ordering code	Ammo pack	Reel	Untaped
spacing		$w \times h \times I$	(composition see			
mm	μF	mm	below)	pcs./MOQ	pcs./MOQ	pcs./MOQ
15	0.0047	$5.0\times10.5\times18.0$	B32032A4472+***	4680	5200	4000
	0.0056	$5.0\times10.5\times18.0$	B32032A4562+***	4680	5200	4000
	0.0068	$5.0\times10.5\times18.0$	B32032A4682+***	4680	5200	4000
	0.0082	$5.0\times10.5\times18.0$	B32032A4822+***	4680	5200	4000
	0.010	$5.0\times10.5\times18.0$	B32032A4103+***	4680	5200	4000
	0.015	$6.0\times11.0\times18.0$	B32032A4153+***	3840	4400	4000
	0.018	$7.0\times12.5\times18.0$	B32032A4183+***	3320	3600	4000
	0.022	$7.0\times12.5\times18.0$	B32032A4223+***	3320	3600	4000
	0.027	$8.0\times14.0\times18.0$	B32032A4273+***	2920	3000	2000
	0.033	$8.0\times14.0\times18.0$	B32032A4333+***	2920	3000	2000
	0.039	$8.5\times14.5\times18.0$	B32032A4393M***	2720	2800	2000
	0.039	$9.0\times17.5\times18.0$	B32032B4393K***	2560	2800	2000
	0.047	$9.0\times17.5\times18.0$	B32032A4473+***	2560	2800	2000
	0.056	$9.0\times17.5\times18.0$	B32032A4563M***	2560	2800	2000
	0.056	$11.0\times18.5\times18.0$	B32032B4563K***	—	2200	1200
	0.068	$11.0\times18.5\times18.0$	B32032A4683+***	_	2200	1200

MOQ = Minimum Order Quantity, consisting of 4 packing units. Further intermediate capacitance values on request.

#### Composition of ordering code

- + = Capacitance tolerance code:
  - $M = \pm 20\%$
  - K = ±10%

- \*\*\* = Packaging code:
  - 289 = Straight terminals, Ammo pack
  - 189 = Straight terminals, Reel
  - 003 = Straight terminals, untaped (lead length 3.2 ±0.3 mm)
  - 000 = Straight terminals, untaped (lead length 6 -1 mm)



B32032 ... B32036 Y2 / 350 V AC Y2

#### Ordering codes and packing units

Lead	C <sub>R</sub>	Max. dimensions	Ordering code	Ammo pack	Reel	Untaped
spacing		$w \times h \times l$	(composition see			
mm	μF	mm	below)	pcs./MOQ	pcs./MOQ	pcs./MOQ
22.5	0.033	$6.0\times15.0\times26.5$	B32033A4333+***	2720	2800	2880
	0.047	$7.0\times16.0\times26.5$	B32033A4473+***	2320	2400	2520
	0.056	$7.0\times16.0\times26.5$	B32033A4563+***	2320	2400	2520
	0.068	$8.5\times16.5\times26.5$	B32033A4683+***	1920	2000	2040
	0.082	$8.5\times16.5\times26.5$	B32033A4823M***	1920	2000	2040
	0.082	$10.5\times16.5\times26.5$	B32033B4823K***	1560	1600	2160
	0.10	$10.5\times16.5\times26.5$	B32033A4104M***	1560	1600	2160
	0.10	$10.5\times18.5\times26.5$	B32033B4104K***	1560	1600	2160
	0.12	$10.5\times18.5\times26.5$	B32033A4124M***	1560	1600	2160
	0.12	$10.5\times20.5\times26.5$	B32033B4124+***	_	—	2160
	0.15	$11.0\times20.5\times26.5$	B32033A4154M***	_	—	2160
	0.15	$12.0\times22.0\times26.5$	B32033B4154K***	_	—	1800
	0.18	$12.0\times22.0\times26.5$	B32033A4184M***	_	—	1800
	0.18	$14.5\times29.5\times26.5$	B32033B4184K***	_	—	2160
	0.22	$14.5\times29.5\times26.5$	B32033A4224+***	_	—	2160
	0.27	$14.5\times29.5\times26.5$	B32033A4274+***	_	_	2160
	0.33	$15.5\times33.5\times26.5$	B32033B4334+***	-	_	1120
	0.39	$15.5\times33.5\times26.5$	B32033A4394+***	—	_	1120

MOQ = Minimum Order Quantity, consisting of 4 packing units. Further intermediate capacitance values on request.

#### Composition of ordering code

+ = Capacitance tolerance code:

 $M = \pm 20\%$ 

 $K = \pm 10\%$ 

- \*\*\* = Packaging code:
  - 289 = Straight terminals, Ammo pack
  - 189 = Straight terminals, Reel
  - $\begin{array}{rl} \text{003} = & \text{Straight terminals, untaped} \\ & (\text{lead length 3.2 } \pm 0.3 \text{ mm}) \end{array}$
  - 000 = Straight terminals, untaped (lead length 6 -1 mm)



Y2

#### B32032 ... B32036

Y2 / 350 V AC

#### Ordering codes and packing units

Lead	C <sub>R</sub>	Max. dimensions	Ordering code	Ammo pack	Reel	Untaped
spacing		$w \times h \times I$	(composition see			
mm	μF	mm	below)	pcs./MOQ	pcs./MOQ	pcs./MOQ
27.5	0.10	$11.0 \times 19.0 \times 31.5$	B32034A4104+***	_	1400	1280
	0.15	$11.0 \times 19.0 \times 31.5$	B32034A4154M***	—	1400	1280
	0.15	$11.0\times21.0\times31.5$	B32034B4154K***	—	1400	1280
	0.18	$11.0\times21.0\times31.5$	B32034A4184+***	—	1400	1280
	0.22	$13.5\times23.0\times31.5$	B32034B4224+***	—	1000	1040
	0.27	$15.0\times24.5\times31.5$	B32034A4274+***	—	—	960
	0.33	$16.0\times32.0\times31.5$	B32034B4334+***	—	—	880
	0.33	$18.0\times27.5\times31.5$	B32034C4334+***	—	—	800
	0.39	$16.0\times32.0\times31.5$	B32034A4394M***	—	—	880
	0.39	$18.0\times27.5\times31.5$	B32034B4394+***	—	—	800
	0.47	$16.0\times32.0\times31.5$	B32034A4474M***	—	—	880
	0.47	$18.0\times33.0\times31.5$	B32034C4474K***	—	—	800
	0.56	$21.0\times31.0\times31.5$	B32034C4564+***	—	—	720
	0.68	$22.0\times36.5\times31.5$	B32034A4684+***	—	—	640
	0.82	$22.0\times48.0\times31.5$	B32034B4824K***	—	—	640
	1.0	$22.0\times48.0\times31.5$	B32034A4105+***	—	—	640
37.5	0.27	$12.0\times22.0\times42.0$	B32036A4274+***	_	—	1620
	0.33	$14.0\times25.0\times42.0$	B32036B4334K***	_	—	1380
	0.39	$14.0\times25.0\times42.0$	B32036A4394+***	_	—	1380
	0.47	$16.0\times28.5\times42.0$	B32036A4474+***	_	—	800
	0.56	$16.0\times28.5\times42.0$	B32036A4564+***	_	—	800
	0.68	$17.0\times32.0\times42.0$	B32036A4684+***	_	—	720
	0.82	$18.0\times32.5\times42.0$	B32036A4824M***	_	-	720
	0.82	$20.0\times39.5\times42.0$	B32036B4824K***	_	-	640
	1.0	$20.0\times39.5\times42.0$	B32036A4105+***	_	_	640
	1.2	$28.0 \times 42.5 \times 42.0$	B32036A4125+***	_	_	440

MOQ = Minimum Order Quantity, consisting of 4 packing units. Further intermediate capacitance values on request.

#### Composition of ordering code

+ = Capacitance tolerance code:

 $M = \pm 20\%$ 

K = ±10%

\*\*\* = Packaging code:

289 = Straight terminals, Ammo pack

- 189 = Straight terminals, Reel
- 003 = Straight terminals, untaped (lead length 3.2 ±0.3 mm)
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#### Ordering code example

<b>B</b> Compo- nents class	<b>3203</b> Series	<b>3</b> Lead spacing (mm)	A Dimension code	<b>4</b> Rated voltage	<b>154</b> Rated capacitance	M Capacitance tolerance
Passive compo- nents	Y2 MKP	2 = 15.0 3 = 22.5 4 = 27.5 6 = 37.5	See table "Ordering codes and packing units"	4 = 350 V AC	15 · 10⁴ pF	K = ±10% M = ±20% + = K or M

#### Technical data

Reference standard:UL / IEC 60384-14:2013 / AMD1:2016. All data given at T = 20  $^{\circ}$ C, unless otherwise specified.

Rated AC voltage V <sub>AC</sub>	350 V AC (50/60 Hz)
(IEC 60384-14:2013)	
Rated DC voltage V <sub>DC</sub>	1000 V DC
Max. operating temperature T <sub>op</sub>	+110 °C
$(T_{op} = T_A + T_{self-heating})$	
DC test voltage for 2 seconds	4200 V (C $\le$ 0.33 $\mu$ F)
	4000 V (C > 0.33 μF), 2 s
AC test voltage (type test)	2000 V <sub>RMS</sub> , 60 s

The repetition of this DC voltage test may damage the capacitor. Special care must be taken in case of use several capacitors in a parallel configuration (refer to section "Parallel configuration).

Dissipation factor tan $\delta$ at 1 kHz (upper limit)	1 · 10 <sup>-3</sup>		
Insulation resistance R <sub>ins</sub> or	$C_{\text{R}} \leq 0.33 \ \mu\text{F}$	C <sub>R</sub> > 0.33 μF	
time constant $\tau = C_R \cdot R_{ins}$ at 100 V DC,	100 000 MΩ	30 000 s	
rel. humidity $\leq$ 65% and for 60 s			
(minimum as-delivered values)			
Passive flammability category	В		
Capacitance tolerances	±10% (K), ±20% (M)		
(measured at 1 kHz)			
Grade III high robustness	T = +85 °C ± 2 °C		
under high humidity	Relative humidity: $85\% \pm 2\%$	%	
	V <sub>R</sub> = 350 V AC, 50/60 Hz		
Test B:	Test duration: 1000 hours		
Criteria for passing after	Capacitance change ( $\Delta$ C/C	3) ≤10%	
biased damp heat test	Dissipation factor change ( $\Delta \tan \delta$ ) < 0.005		
	Insulation resistance R <sub>ins</sub>	> 50% of initial limit	
	Mechanical criteria: not app	olicable	





#### Pulse handling capability

"dV/dt" represents the maximum permissible voltage change per unit of time for non-sinusoidal voltages, expressed in  $V/\mu s$ .

" $k_0$ " represents the maximum permissible pulse characteristic of the waveform applied to the capacitor, expressed in V<sup>2</sup>/µs.

#### Note:

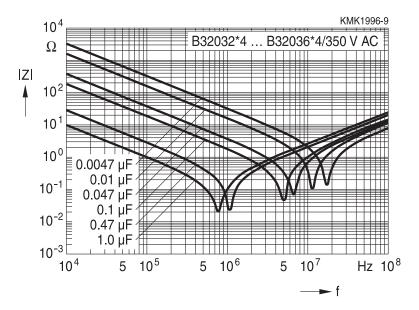
The values of dV/dt and  $k_0$  provided below must not be exceeded in order to avoid damaging the capacitor. These parameters are given for isolated pulses in such a way that the heat generated by one pulse will be completely dissipated before applying the next pulse. For a train of pulses, please refer to the curves of permissible AC voltage-current versus frequency

#### dV/dt and k<sub>0</sub> values

Lead spacing	15 mm	22.5 mm	27.5 mm	37.5 mm
dV/dt in V/µs	600	500	400	300
k₀in V²/µs	508 000	423 000	338 000	254 000

#### Impedance Z versus frequency f

(typical values)



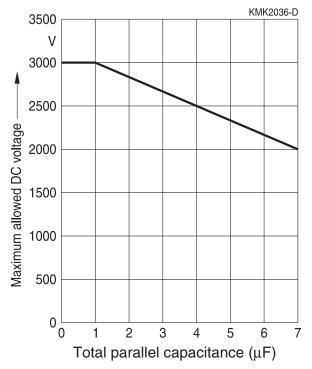


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#### **Parallel configuration**

Only capacitors of the same capacitance may be connected in parallel and the maximum DC test voltage must be reduced as a function of the total parallel capacitance value. The diagram below shows a typical trend of maximum allowed voltage versus total parallel capacitance. (Cut off current 10 mA, rise time  $\leq$  1000 V/s, dwell time  $\leq$  2s, maximum allowed parallel connected capacitor 8 pcs).

B3203\* DC test voltage as function of total parallel capacitance ( $\mu$ F):

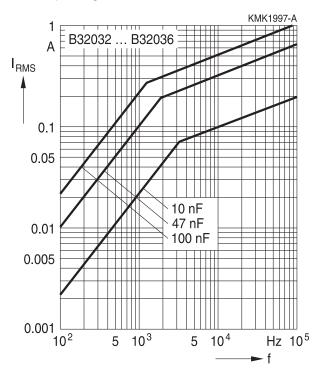




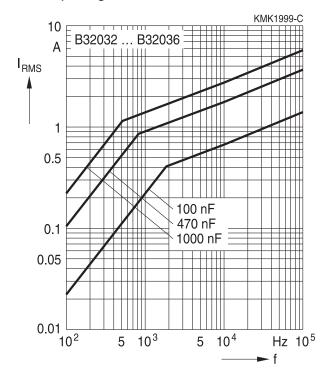


## Permissible current $I_{\text{RMS}}$ versus frequency (for sinusoidal waveform, $T_A$ ≤90 °C and $\Delta ESR$ <100% from receipt condition)

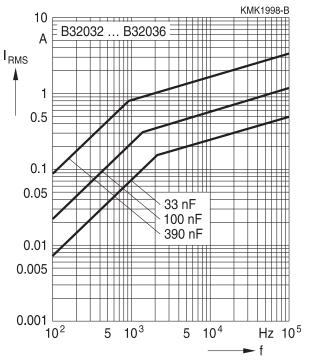
Lead spacing 15 mm

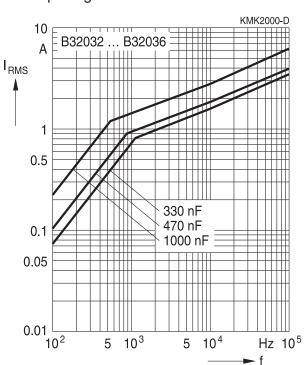


Lead spacing 27.5 mm



Lead spacing 22.5 mm





Lead spacing 37.5 mm



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# Y2

#### **Testing and Standards**

Test	Reference	<b>Conditions of test</b>		Performance requirements
Electrical parameters	IEC 60384-14:2013/ AMD1:2016	Voltage proof – Between terminals: 1500 V AC, 1 min – Terminals and enclosure: $2 V_R + 1500 V AC$ Special voltage proof – Between terminals: 2000 V AC ,1min Insulation resistance, R <sub>ins</sub> Capacitance, C Dissipation factor, tan $\delta$		Within specified limits
Robustness of termina- tions	IEC 60068-2-21:2006	Tensile strength (test U <sub>a</sub> 1) Wire diameter Tensile force		Capacitance and tan $\delta$ within specified limits
		$\begin{array}{l} 0.5 < d_{1} \leq 0.8 \mbox{ mm} \\ 0.8 < d_{1} \leq 1.25 \mbox{ mm} \end{array}$		
Resistance to soldering heat	IEC 60068-2-20:2008, test Tb, method 1A	Solder bath temperature at , 260 ±5 °C, immersion for 10 seconds		$\Delta C/C_0 \le 5\%$ tan $\delta$ within specified limits
Vibration	IEC 60384-14:2013/ AMD1:2016	Test FC: vibration sinusoidal Displacement: 0.75 mm Accleration: 98 m/s <sup>2</sup> Frequency: 10 Hz 500 Hz Test duration: 3 orthogonal axes, 2 hours each axe		No visible damage
Bump	IEC 60384-14:2013/ AMD1:2016	Test Eb: Total 4000 bumps with 400 m/s <sup>2</sup> mounted on PCB 6 ms duration		No visible damage $ \Delta C/C_0  \le 5\%$ tan $\delta$ within specified limits
Damp heat, steady state	IEC 60384-14:2013/ AMD1:2016	40 °C / 93% RH / 5	6 days	No visible damage $ \Delta C/C_0  \le 5\%$ $ \Delta \tan \delta  \le 0.008$ for $C \le 1 \mu F$ $ \Delta \tan \delta  \le 0.005$ for $C > 1 \mu F$ Voltage proof $R_{ins} \ge 50\%$ of initial limit



# Y2

### B32032 ... B32036

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Test	Reference	Conditions of test	Performance requirements
Grade III High robustness under high humidity Test B:	IEC 60384-14:2013/ AMD1:2016	85 °C / 85% RH / 1000 hours / 350 V AC, 50 Hz	$\begin{split}  \Delta C/C_0  &\leq 10\% \\  \Delta \tan \delta  &\leq 0.005 \\ R_{\text{ins}} &\geq 50\% \text{ of initial limit} \end{split}$
Biased humidity test	AEC-Q200D	40 °C / 93% RH / 1000 hours / 350 V AC, 50 Hz	$ \Delta C/C_0  \le 10\%$ $ \Delta \tan \delta  \le 0.005$ $R_{ins} \ge 50\%$ of initial limit
Temperature cycling	AEC-Q200D	$T_A$ = lower category temperature $T_B$ = upper category temperature 1000 cycles, duration t = 30 min	No visible damage $ \Delta C/C_0  \le 5\%$ tan $\delta$ within specified limits
Climatic sequence	IEC 60384-14:2013/ AMD1:2016	Dry heat Tb / 16 h Damp heat cyclic, 1 <sup>st</sup> cycle +55 °C / 24 h / 95% 100% RH Cold Ta / 2 h Damp heat cyclic, 5 cycles +55 °C / 24 h / 95% 100% RH	No visible damage $\begin{split}  \Delta C/C_0  \leq 5\% \\  \Delta \tan \delta  \leq 0.008 \text{ for } C \leq 1\mu\text{F} \\  \Delta \tan \delta  \leq 0.005 \text{ for } C > 1\mu\text{F} \\ Voltage \text{ proof} \\ R_{\text{ins}} \geq 50\% \text{ of initial limit} \end{split}$
Impulse test endurance	IEC 60384-14:2013/ AMD1:2016	3 impulses 110 °C / 1.7 V <sub>R</sub> / 1000 hours, 1000 V <sub>RMS</sub> for 0.1 s every hour	No visible damage $\begin{split}  \Delta C/C_0  &\leq 10\% \\  \Delta \tan \delta  &\leq 0.008 \text{ for } C \leq 1\mu\text{F} \\  \Delta \tan \delta  &\leq 0.005 \text{ for } C > 1\mu\text{F} \\ Voltage \text{ proof} \\ R_{\text{ins}} &\geq 50\% \text{ of initial limit} \end{split}$
Passive flammability	IEC 60384-14:2013/ AMD1:2016	Flame applied for a period of time depending on capacitor volume	В
Active flammability	IEC 60384-14:2013/ AMD1:2016	20 discharges at 5 kV + AC	The cheesecloth shall not burn with a flame



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# Y2

#### Mounting guidelines

#### 1 Soldering

#### 1.1 Solderability of leads

The solderability of terminal leads is tested to IEC 60068-2-20:2008, test Ta, method 1.

Before a solderability test is carried out, terminals are subjected to accelerated ageing (to IEC 60068-2-2:2007, test Ba: 4 h exposure to dry heat at 155 °C). Since the ageing temperature is far higher than the upper category temperature of the capacitors, the terminal wires should be cut off from the capacitor before the ageing procedure to prevent the solderability being impaired by the products of any capacitor decomposition that might occur.

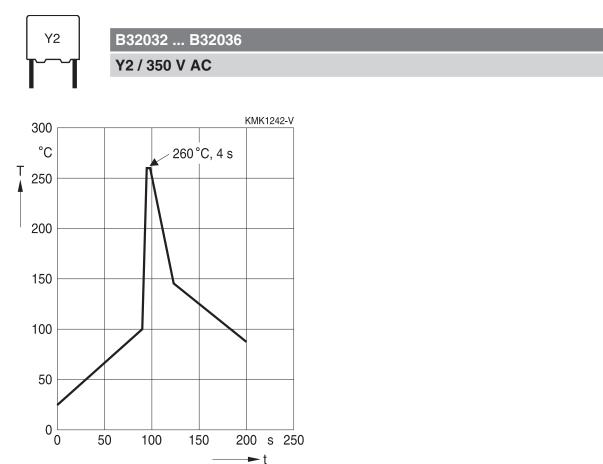
Solder bath temperature	235 ±5 °C	
Soldering time	2.0 ±0.5 s	
Immersion depth	2.0 + 0/-0.5 mm from capacitor body or seating plane	
Evaluation criteria:		
Visual inspection	Wetting of wire surface by new solder ≥90%, free-flowing solder	

#### 1.2 Resistance to soldering heat

Resistance to soldering heat is tested to IEC 60068-2-20:2008, test Tb, method 1. Conditions:

Series		Solder bath temperature	Soldering time
MKT	boxed (except $2.5 \times 6.5 \times 7.2$ mm) coated uncoated (lead spacing >10 mm)	260 ±5 °C	10 ±1 s
MFP			
MKP	(lead spacing >7.5 mm)		
MKT	boxed (case 2.5 $\times$ 6.5 $\times$ 7.2 mm)		5 ±1 s
MKP MKT	(lead spacing ≤7.5 mm) uncoated (lead spacing ≤10 mm) insulated (B32559)		<4 s recommended soldering profile for MKT uncoated (lead spacing $\leq$ 10 mm) and insulated (B32559)





Immersion depth	2.0 + 0/-0.5 mm from capacitor body or seating plane
Shield	Heat-absorbing board, (1.5 $\pm$ 0.5) mm thick, between capacitor body and liquid solder
Evaluation criteria:	
Visual inspection	No visible damage
$\Delta C/C_0$	2% for MKT/MKP/MFP 5% for EMI suppression capacitors
tan δ	As specified in sectional specification

#### 1.3 General notes on soldering

Permissible heat exposure loads on film capacitors are primarily characterized by the upper category temperature  $T_{max}$ . Long exposure to temperatures above this type-related temperature limit can lead to changes in the plastic dielectric and thus change irreversibly a capacitor's electrical characteristics. For short exposures (as in practical soldering processes) the heat load (and thus the possible effects on a capacitor) will also depend on other factors like:

- Pre-heating temperature and time
- Forced cooling immediately after soldering
- Terminal characteristics: diameter, length, thermal resistance, special configurations (e.g. crimping)
- Height of capacitor above solder bath
- Shadowing by neighboring components
- Additional heating due to heat dissipation by neighboring components
- Use of solder-resist coatings

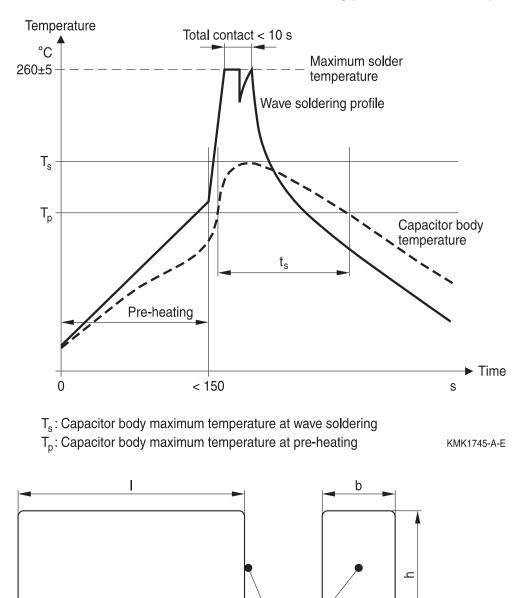




The overheating associated with some of these factors can usually be reduced by suitable countermeasures. For example, if a pre-heating step cannot be avoided, an additional or reinforced cooling process may possibly have to be included.

#### **EPCOS** recommendations

As a reference, the recommended wave soldering profile for our film capacitors is as follows:



Ρ

KMK1744-9-E

Body temperature sensor position





Body temperature should follow the description below:

- MKP capacitor During pre-heating: T<sub>p</sub> ≤110 °C During soldering: T<sub>s</sub> ≤120 °C, t<sub>s</sub> ≤45 s
- MKT capacitor During pre-heating: T<sub>p</sub> ≤125 °C During soldering: T<sub>s</sub> ≤160 °C, t<sub>s</sub> ≤45 s

When SMD components are used together with leaded ones, the film capacitors should not pass into the SMD adhesive curing oven. The leaded components should be assembled after the SMD curing step.

Leaded film capacitors are not suitable for reflow soldering.

In order to ensure proper conditions for manual or selective soldering, the body temperature of the capacitor (T<sub>s</sub>) must be  $\leq$ 120 °C.

One recommended condition for manual soldering is that the tip of the soldering iron should be <360 °C and the soldering contact time should be no longer than 3 seconds.

For uncoated MKT capacitors with lead spacings  $\leq$ 10 mm (B32560/B32561) the following measures are recommended:

- pre-heating to not more than 110 °C in the preheater phase
- rapid cooling after soldering

Please refer to EPCOS Film Capacitor Data Book in case more details are needed.

#### Application note

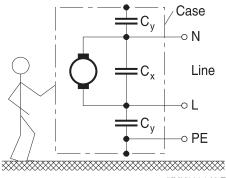
A Y capacitor to be connected between AC lines and the Frame Ground (FG) has an effect of emitting common mode current to the FG.Leak current corresponding in frequency and voltage to the AC power supply flows in the Y capacitor.

As the capacity of a Y capacitor grows, leak current also increases. This can cause electric shock. Therefore, UL and other safety standards restrict the capacity so that the amount of leak current does not exceed a certain level. Usually, two Y capacitors are used, as shown in Figure below. As AC lines are connected with capacitors, they are effective in reducing differential mode noises. In particular, they are effective for high frequency ranges of 8 to 10 MHz.

Example of EMI suppression, typical application:







KMK0804-U-E





Y2 / 350 V AC

#### **Cautions and warnings**

- Do not exceed the upper category temperature (UCT).
- Do not apply any mechanical stress to the capacitor terminals.
- Avoid any compressive, tensile or flexural stress.
- Do not move the capacitor after it has been soldered to the PC board.
- Do not pick up the PC board by the soldered capacitor.
- Do not place the capacitor on a PC board whose PTH hole spacing differs from the specified lead spacing.
- Do not exceed the specified time or temperature limits during soldering.
- Avoid external energy inputs, such as fire or electricity.
- Avoid overload of the capacitors.
- Consult us if application is with severe temperature and humidity condition.
- There are no serviceable or repairable parts inside the capacitor. Opening the capacitor or any attempts to open or repair the capacitor will void the warranty and liability of EPCOS.
- Please note that the standards referred to in this publication may have been revised in the meantime.

The table below summarizes the safety instructions that must always be observed. A detailed description can be found in the relevant sections of the chapters "General technical information" and "Mounting guidelines".

Торіс	Safety information	Reference chapter
		"General technical
		information"
Storage	Make sure that capacitors are stored within the specified	4.5
conditions	range of time, temperature and humidity conditions.	"Storage conditions"
Flammability	Avoid external energy, such as fire or electricity (passive	5.3
	flammability), avoid overload of the capacitors (active	"Flammability"
	flammability) and consider the flammability of materials.	
Resistance to	Do not exceed the tested ability to withstand vibration.	5.2
vibration	The capacitors are tested to IEC 60068-2-6:2007.	"Resistance to
	EPCOS offers film capacitors specially designed for	vibration"
	operation under more severe vibration regimes such as	
	those found in automotive applications. Consult our	
	catalog "Film Capacitors for Automotive Electronics".	

Торіс	Safety information	Reference chapter "Mounting guidelines"
Soldering	Do not exceed the specified time or temperature limits	1 "Soldering"
	during soldering.	
Cleaning	Use only suitable solvents for cleaning capacitors.	2 "Cleaning"



Y2/350 V AC

Y2

Торіс	Safety information	Reference chapter
		"Mounting guidelines"
Embedding of	When embedding finished circuit assemblies in plastic	3 "Embedding of
capacitors in	resins, chemical and thermal influences must be taken	capacitors in finished
finished	into account.	assemblies"
assemblies	Caution: Consult us first, if you also wish to embed other	
	uncoated component types!	

#### Design of EMI Capacitors

EPCOS EMI capacitors use polypropylene (PP) film metalized with a thin layer of Zinc (Zn). The following key points have made this design suitable to IEC/UL testing, holding a minimum size.

- Overvoltage AC capability with very high temperature Endurance test of IEC 60384-14:2013 (4th edition) / UL 60384-14:2014 (2<sup>nd</sup> edition) must be performed at 1.25 × V<sub>R</sub> at maximum temperature, during 1000 hours, with a capacitance drift less than 10%.
- Higher breakdown voltage withstanding if compared to other film metallizations, like Aluminum. IEC 60384-14:2013 (4<sup>th</sup> edition) / UL 60384-14:2014 (2<sup>nd</sup> edition) establishes high voltage tests performed at 4.3 × V<sub>R</sub> −1 minute, impulse testing at 2500 V for C = 1 µF and active flammability tests.
- Damp heat steady state: 40 °C/ 93% RH / 56 days. (without voltage or current load)

#### Effect of humidity on capacitance stability

Long contact of a film capacitor with humidity can produce irreversible effects. Direct contact with liquid water or excess exposure to high ambient humidity or dew will eventually remove the film metallization and thus destroy the capacitor. Plastic boxed capacitors must be properly tested in the final application at the worst expected conditions of temperature and humidity in order to check if any parameter drift may provoke a circuit malfunction.

In case of penetration of humidity through the film, the layer of Zinc can be degraded, specially under AC operation (change of polarity), accelerated by the temperature, provoking an increment of the serial resistance of the electrode and eventually a reduction of the capacitance value. For DC operation, the parameter drift is much less.

Plastic boxes and resins can not protect 100% against humidity. Metal enclosures, resin potting or coatings or similar measures by customers in their applications will offer additional protection against humidity penetration.

#### Display of ordering codes for EPCOS products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications and the website of EPCOS, or in order-related documents such as shipping notes, order confirmations and product labels. **The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products**. Detailed information can be found on the Internet under <u>www.epcos.com/orderingcodes</u>.



Y2

## B32032 ... B32036

Y2 / 350 V AC

#### Symbols and terms

Symbol	English	German
α	Heat transfer coefficient	Wärmeübergangszahl
$\alpha_{c}$	Temperature coefficient of capacitance	Temperaturkoeffizient der Kapazität
A	Capacitor surface area	Kondensatoroberfläche
β <sub>c</sub>	Humidity coefficient of capacitance	Feuchtekoeffizient der Kapazität
С	Capacitance	Kapazität
C <sub>R</sub>	Rated capacitance	Nennkapazität
ΔC	Absolute capacitance change	Absolute Kapazitätsänderung
$\Delta C/C$	Relative capacitance change (relative deviation of actual value)	Relative Kapazitätsänderung (relative Abweichung vom Ist-Wert)
$\Delta C/C_R$	Capacitance tolerance (relative deviation from rated capacitance)	
dt	Time differential	Differentielle Zeit
Δt	Time interval	Zeitintervall
ΔT	Absolute temperature change (self-heating)	Absolute Temperaturänderung (Selbsterwärmung)
∆tan δ	Absolute change of dissipation factor	Absolute Änderung des Verlustfaktors
ΔV	Absolute voltage change	Absolute Spannungsänderung
dV/dt	Time differential of voltage function (rate of voltage rise)	Differentielle Spannungsänderung (Spannungsflankensteilheit)
$\Delta V / \Delta t$	Voltage change per time interval	Spannungsänderung pro Zeitintervall
E	Activation energy for diffusion	Aktivierungsenergie zur Diffusion
ESL	Self-inductance	Eigeninduktivität
ESR	Equivalent series resistance	Ersatz-Serienwiderstand
f	Frequency	Frequenz
f <sub>1</sub>	Frequency limit for reducing permissible AC voltage due to thermal limits	Grenzfrequenz für thermisch bedingte Reduzierung der zulässigen Wechselspannung
f <sub>2</sub>	Frequency limit for reducing permissible AC voltage due to current limit	Grenzfrequenz für strombedingte Reduzierung der zulässigen Wechselspannung
f <sub>r</sub>	Resonant frequency	Resonanzfrequenz
F <sub>D</sub>	Thermal acceleration factor for diffusion	Therm. Beschleunigungsfaktor zur Diffusion
F <sub>τ</sub>	Derating factor	Deratingfaktor
i	Current (peak)	Stromspitze
I <sub>C</sub>	Category current (max. continuous current)	Kategoriestrom (max. Dauerstrom)



B32032 ... B32036 Y2 / 350 V AC Y2

Symbol	English	German
I <sub>RMS</sub>	(Sinusoidal) alternating current,	(Sinusförmiger) Wechselstrom
	root-mean-square value	
İ <sub>z</sub>	Capacitance drift	Inkonstanz der Kapazität
k <sub>0</sub>	Pulse characteristic	Impulskennwert
L <sub>S</sub>	Series inductance	Serieninduktivität
λ	Failure rate	Ausfallrate
λο	Constant failure rate during useful	Konstante Ausfallrate in der
	service life	Nutzungsphase
$\lambda_{test}$	Failure rate, determined by tests	Experimentell ermittelte Ausfallrate
P <sub>diss</sub>	Dissipated power	Abgegebene Verlustleistung
$P_{gen}$	Generated power	Erzeugte Verlustleistung
Q	Heat energy	Wärmeenergie
ρ	Density of water vapor in air	Dichte von Wasserdampf in Luft
R	Universal molar constant for gases	Allg. Molarkonstante für Gas
R	Ohmic resistance of discharge circuit	Ohmscher Widerstand des
		Entladekreises
R <sub>i</sub>	Internal resistance	Innenwiderstand
<b>R</b> <sub>ins</sub>	Insulation resistance	Isolationswiderstand
R <sub>P</sub>	Parallel resistance	Parallelwiderstand
Rs	Series resistance	Serienwiderstand
S	severity (humidity test)	Schärfegrad (Feuchtetest)
t	Time	Zeit
Т	Temperature	Temperatur
τ	Time constant	Zeitkonstante
tan δ	Dissipation factor	Verlustfaktor
tan $\delta_D$	Dielectric component of dissipation factor	Dielektrischer Anteil des Verlustfaktors
tan δ <sub>P</sub>	Parallel component of dissipation factor	Parallelanteil des Verlfustfaktors
tan $\delta_s$	Series component of dissipation factor	Serienanteil des Verlustfaktors
T <sub>A</sub>	Temperature of the air surrounding the component	Temperatur der Luft, die das Bauteil umgibt
T <sub>max</sub>	Upper category temperature	Obere Kategorietemperatur
T <sub>min</sub>	Lower category temperature	Untere Kategorietemperatur
t <sub>OL</sub>	Operating life at operating temperature	Betriebszeit bei Betriebstemperatur und
	and voltage	-spannung
T <sub>op</sub>	Operating temperature, $T_A + \Delta T$	Beriebstemperatur, $T_A + \Delta T$
T <sub>R</sub>	Rated temperature	Nenntemperatur
T <sub>ref</sub>	Reference temperature	Referenztemperatur
t <sub>SL</sub>	Reference service life	Referenz-Lebensdauer





Y2/350 V AC

Symbol	English	German
V <sub>AC</sub>	AC voltage	Wechselspannung
V <sub>c</sub>	Category voltage	Kategoriespannung
$V_{C,RMS}$	Category AC voltage	(Sinusförmige)
		Kategorie-Wechselspannung
$V_{CD}$	Corona-discharge onset voltage	Teilentlade-Einsatzspannung
$V_{ch}$	Charging voltage	Ladespannung
$V_{\text{DC}}$	DC voltage	Gleichspannung
$V_{FB}$	Fly-back capacitor voltage	Spannung (Flyback)
Vi	Input voltage	Eingangsspannung
Vo	Output voltage	Ausgangssspannung
$V_{op}$	Operating voltage	Betriebsspannung
$V_p$	Peak pulse voltage	Impuls-Spitzenspannung
$V_{pp}$	Peak-to-peak voltage Impedance	Spannungshub
V <sub>R</sub>	Rated voltage	Nennspannung
Ŷ <sub>R</sub>	Amplitude of rated AC voltage	Amplitude der Nenn-Wechselspannung
$V_{\text{RMS}}$	(Sinusoidal) alternating voltage,	(Sinusförmige) Wechselspannung
	root-mean-square value	
$V_{\text{SC}}$	S-correction voltage	Spannung bei Anwendung "S-correction"
$V_{sn}$	Snubber capacitor voltage	Spannung bei Anwendung
		"Beschaltung"
Z	Impedance	Scheinwiderstand
е	Lead spacing	Rastermaß



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