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4805 (1/15)

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					RE	VISIONS				
			Ρ	LTR	DE	SCRIPTION	DATE	DWN	APVD	
				E	ECO-16-010227		11JUL2016	LL	SH	
]	<u>_</u>	OVERMOLDE	) T	ERM	INAL ARRAY -					
]		PLATED WIT	Η	1.27		MINIMUM HARD				
GOLD IN LOCALIZED AREA AND 3.81 um[.000150] MINIMUM MATTE TIN IN SOLDER AREA OVER 1.27 um							n			
		[.000050]	MIN	IIMU	M NICKEL UND	ERPLATE.				
			LDS – 0.25 [.010] THICK COPPER ALLOY							
		AND 2.03 L	WITH 1.27 um [.000050] MINMUM NICKEL 03 um [.000080] MINIMUM HOT TIN DIP 3 GROUND TABS.							
	$\triangle$				DRMS TO FCC F T 68 SUBPART					
]	$\Delta$	OBSOLETE P	AR <sup>-</sup>		DBSOLETE CIS S PER D.RENAUD/					
1	$\bigtriangleup$	ENLARGED T	ΗE		,	THE CONTACTS A	ND SHIE	LD		
J	A	MATERIAL: H	10L	JSIN	G – NYLON, B	LACK, UL 94V-0.				
		overmolde	) T	ERM	INAL ARRAY –	PBT, BROWN.				
					[.013] THICK					
						MINIMUM HARD				
						3.81 um[.000150] AREA OVER 1.27				
					M NICKEL UND					
					.010] THICK C					
		SHILLDS	$\bigcirc$ .	20						

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SHIELDS	<ul> <li>0.25 [.010] THICK COPPER ALLOY</li> </ul>
PLATED	WITH 1.27 um [.000050] MINMUM NICKEL
AND 2.0	3 um [.000080] MINIMUM HOT TIN DIP
ON PCB	GROUND TABS.

	NA	NA		NONE			540	6206	-4		
	40.64 [1.600]	-0.64 [1.600] 54.61[2.150]		3			-5406206-3				
	40.64 [1.600]	NA		1			5406206-2				
	NA	NA	NONE				5406206-1				
	DIM B	DIM C	NUMB Shiei			PCB BS	P	ART	NO		
C	DNTROLLED DOCUMENT	- DWN - T. SPRINKLE, снк - J.WESTMAN	25MAY2005 /L.A.MAYER 17DEC96		e te	TE	Conn	ectivity	r		
1	TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± - 1 PLC ± - 2 PLC ± 0.25[.01] 3 PLC ± 0.13[.005]	apvd S.FLICKINGER product spec 108—11 application spec		NAME MODULAR JACK ASSEMBLY, 6 PORT, 8 POSITION, SHIELDED, REVERSE PANEL GROUND TABS							
	4 PLC ±	114-20	48	SIZE CAGE CODE	DRAWING NO				RESTR	RICTED TO	
FINISH SEE NOTE 1		WEIGHT 0.00	WEIGHT 0.000000		A 1 00779 <b>C=</b> 5406206						
	CUSTOMER DRAWING					scale 2:1	SHEET	0F	1 R	ev E	

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