



74LVC2G07

DUAL BUFFERS WITH OPEN DRAIN OUTPUTS

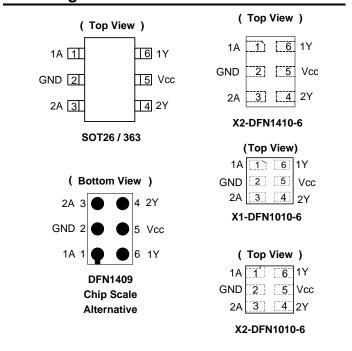
Description

The 74LVC2G07 is a dual buffer gate with open drain outputs. The device is designed for operation with a power supply range of 1.65V to 5.5V. The input is tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using I_{OFF}. The I_{OFF} circuitry disables the output preventing damaging current backflow when the device is powered down. The open-drain output can be connected to other open drain outputs to implement active-low wired-OR or active-high wired-AND functions. The maximum sink current is 32mA.

Features

- Wide Supply Voltage Range from 1.65V to 5.5V
- -24mA Output Drive at 3.0V
- CMOS Low Power Consumption
- I_{OFF} Supports Partial-Power-Down Mode Operation
- Inputs Accept up to 5.5V
- ESD Protection Tested per JESD 22
 - Exceeds 200-V Machine Model (A115)
 - Exceeds 2000-V Human Body Model (A114)
 - Exceeds 1000-V Charged Device Model (C101)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- DFN1409 Package Designed as a Direct Replacement for Chip Scale Packaging
- Range of Package Options SOT26, SOT363, X1-DFN1010-6, X2-DFN1010-6, X2-DFN1409-6, and X2-DFN1410-6
- Leadless Packages Named per JESD30E
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Pin Assignments



Applications

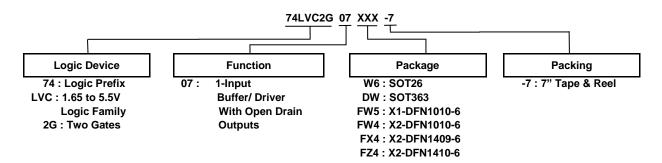
- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide Array of Products Such as:
 - PCs, Networking, Notebooks, Netbooks, Tablets
 - Computer Peripherals, Hard Drives, SSD, CD/DVD ROM
 - TV, DVD, DVR, Set Top Box
 - Cell Phones, Personal Navigation / GPS
 - MP3 Players, Cameras, Video Recorders

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Ordering Information



Part Number	Package	Package	Package	7" Tape and R	eel (Note 5)
Fait Number	Code	(Note 4)	Size	Quantity	Part Number Suffix
74LVC2G07W6-7	W6	SOT26	2.8mm X 2.2mm X 1.1mm 0.95mm lead pitch	3000/Tape & Reel	-7
74LVC2G07DW-7	DW	SOT363	2.0mm X 2.0mm X 1.1mm 0.65mm lead pitch	3000/Tane & Reel	
74LVC2G07FW5-7	FW5	X1-DFN1010-6	1.0mm X 1.0mm X 0.5mm 0.35mm pad pitch	5000/Tape & Reel	-7
74LVC2G07FW4-7	FW4	X2-DFN1010-6	1.0mm X 1.0mm X 0.4mm 0.35mm pad pitch	5000/Tape & Reel	-7
74LVC2G07FX4-7	FX4	X2-DFN1409-6 Chip Scale Alternative	1.4mm X 0.9mm X 0.4mm 0.5mm pad pitch	5000/Tape & Reel	-7
74LVC2G07FZ4-7	FZ4	X2-DFN1410-6	1.4mm X 1.0mm X 0.4mm 0.5mm pad pitch	5000/Tape & Reel	-7

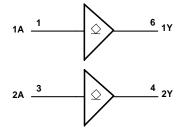
Notes: 4. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

5. The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf

Pin Descriptions

Pin Number	Pin Name	Function		
1	1A	Data Input		
2	GND	Ground		
3	2A	Data Input		
4	2Y	Data Output Open Drain		
5	V _{CC}	Supply Voltage		
6	1Y	Data Output Open Drain		

Logic Diagram



Function Table

Inputs	Output
Α	Y
Н	Z
L	L



Absolute Maximum Ratings (Notes 6, 7) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
V _{CC}	Supply Voltage Range	-0.5 to +6.5	V
VI	Input Voltage Range	-0.5 to +6.5	V
Vo	Voltage Applied to Output in High Impedance or I _{OFF} State	-0.5 to +6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.3 to V _{CC} +0.5	V
I _{IK}	Input Clamp Current V _I < 0	-50	mA
lok	Output Clamp Current V _O < 0	-50	mA
lo	Continuous Output Current	-50	mA
_	Continuous Current through Vdd or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C

Notes:

Recommended Operating Conditions (Note 8) (@T_A = +25°C, unless otherwise specified.)

Symbol		Parameter	Min	Max	Unit
\/	Operating Voltage	Operating	1.65	5.5	V
V _{CC}	Operating Voltage	Data Retention Only	1.5	_	V
		$V_{CC} = 1.65V \text{ to } 1.95V$	0.65 X V _{CC}	_	
\ /	Lligh Loyal Innut Valtage	$V_{CC} = 2.3V \text{ to } 2.7V$	1.7	_	V
V_{IH}	High-Level Input Voltage	V _{CC} = 3V to 3.6V	2	_	V
		V _{CC} = 4.5V to 5.5V	0.7 X V _{CC}	_	
		V _{CC} = 1.65V to 1.95V	_	0.35 X V _{CC}	
.,	Lave Lavel Inner Maltana	V _{CC} = 2.3V to 2.7V	_	0.7	.,,
V_{IL}	Low-Level Input Voltage	V _{CC} = 3V to 3.6V	_	0.8	V
		V _{CC} = 4.5V to 5.5V	_	0.3 X V _{CC}	
VI	Input Voltage	·	0	5.5	V
Vo	Output Voltage		0	V _{CC}	V
		V _{CC} = 1.65V	_	4	
		$V_{CC} = 2.3V$	_	8	
I_{OL}	Low-Level Output Current		_	16	mA
		$V_{CC} = 3V$	_	24	
		$V_{CC} = 4.5V$	_	32	
		$V_{CC} = 1.8V \pm 0.15V, 2.5V \pm 0.2V$	_	20	
$\Delta t/\Delta V$	Input Transition Rise or Fall Rate	$V_{CC} = 3.3V \pm 0.3V$	_	10	ns/V
	Tato	$V_{CC} = 5V \pm 0.5V$	_	10	
T _A	Operating Free-air Temperature	-	-40	+125	°C

Note:

8. Unused inputs should be held at V_{CC} or Ground.

^{6.} Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

^{7.} Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could Cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.



Electrical Characteristics

Symbol	Parameter	Test Conditions	V	-40°C to	+85°C	-40°C to	+125°C	Unit
Symbol	Faranietei	rest Conditions	V _{CC}	Min	Max	Min	Max	Offic
		$I_{OL} = 100 \mu A$	1.65V to 5.5V	_	0.1	_	0.1	
		$I_{OL} = 4mA$	1.65V	_	0.45	_	0.70	
.,,	Low Level Output Voltage	$I_{OL} = 8mA$	2.3V	-	0.3	_	0.45	V
V _{OL}	Low Level Output Voltage	$I_{OL} = 16mA$	3V	-	0.4	_	0.60	V
		$I_{OL} = 24mA$	3 V	_	0.55	_	0.80	
		$I_{OL} = 32mA$	4.5V	_	0.55	_	0.80	
l _l	Input Current	$V_I = 5.5V$ or GND	0 to 5.5V	-	±5	-	±20	μA
l _{OZ}	Z State Leakage Current	$V_{O} = 0 \text{ to } 5.5V$	3.6V	-	±10	-	±10	μA
loff	Power Down Leakage Current	V_{I} or $V_{O} = 5.5V$	0V	-	±10	-	±20	μA
Icc	Supply Current	$V_I = 5.5V$ or GND, $I_O = 0$	1.65V to 5.5V	_	10	_	40	μA
Δlcc	Additional Supply Current	Input at V _{CC} –0.6V	3V to 5.5V	_	500	_	5000	μA

Package Characteristics (@T_A = +25°C, V_{CC} = 3.3V, unless otherwise specified.)

Symbol	Parameter	Package	Conditions	Min	Тур	Max	Unit
Cı	Input Capacitance	Typical of All Packages	Vcc = 3.3V $V_I = V_{CC}$ or GND	_	3.5	_	рF
		SOT26		-	204	_	
		SOT363		_	371	_	
0	Thermal Resistance Junction-to-	X2-DFN1410-6	(Note O)	_	430	_	°C/W
θ_{JA}	Ambient	X2-DFN1409-6	(Note 9)	_	450	_	C/VV
		X1-DFN1010-6		_	495	_	
		X2-DFN1010-6		_	510	_	
		SOT26		-	52	_	
		SOT363		_	143	_	
0	Thermal Resistance Junction-to-	X2-DFN1410-6	(Note O)	_	190	_	°C/W
θ_{JC}	Case	X2-DFN1409-6	(Note 9)	_	225	_	- C/VV
		X1-DFN1010-6		_	245	_	
		X2-DFN1010-6		_	250	_	

Note: 9. Test condition for all packages: Device mounted on FR-4 substrate PC board, 2oz copper with minimum recommended pad layout.

Switching Characteristics

 $T_A = -40$ °C to +85°C, $C_L = 30$ or 50pF (see Figure 1)

Parameter	From (Input)	To (Output)		= 1.8V 15V		= 2.5V .2V		: 3.3V .3V	V _{CC} :		Unit
	(iliput)	(Output)	Min	Max	Min	Max	Min	Max	Min	Max	
t _{PD}	Α	Υ	0.5	6.7	0.5	4.3	0.5	3.7	0.5	2.9	ns

 $T_A = -40$ °C to +125°C, $C_L = 30$ or 50pF (see Figure 1)

Parameter	From (Input)	To (Output)		= 1.8V 15V		: 2.5V .2V		= 3.3V .3V	V _{CC} :		Unit
	(ilipat)	(Output)	Min	Max	Min	Max	Min	Max	Min	Max	
t _{PD}	А	Y	0.5	8.4	0.5	5.5	0.5	4.7	0.5	3.7	ns

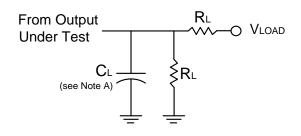


Operating Characteristics

т	_	±25°(

	Parameter Test Conditions		V _{CC} = 1.8V Typ	V _{CC} = 2.5V Typ	V _{CC} = 3.3V Typ	V _{CC} = 5V Typ	Unit
C _{PD}	Power Dissipation Capacitance	f = 10MHz	3	3	4	6	pF

Parameter Measurement Information



TEST	Condition
t _{PLZ} (see Notes D and E)	V _{LOAD}
t _{PZL} (see Notes D and F)	V_{LOAD}

V	Inputs		V	V	6		VA
V _{CC}	VI	t _r /t _f	V _M	V _{LOAD}	CL	R_L	$\mathbf{V}\Delta$
1.8V±0.15V	Vcc	≤2ns	V _{CC} /2	2 X V _{CC}	30pF	1kΩ	0.15V
2.5V±0.2V	V _{CC}	≤2ns	V _{CC} /2	2 X V _{CC}	30pF	500Ω	0.15V
3.3V±0.3V	3V	≤2.5ns	1.5V	6V	50pF	500Ω	0.3V
5V±0.5V	V _{CC}	≤2.5ns	V _{CC} /2	2 X V _{CC}	50pF	500Ω	0.3V

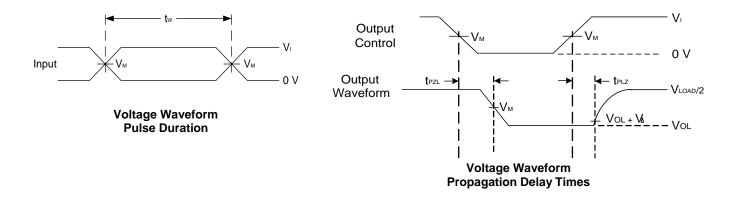


Figure 1. Load Circuit and Voltage Waveforms

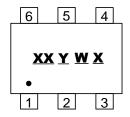
Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 10 MHz
- C. The inputs are measured one at a time with one transition per measurement.
- D. For the open drain device t_{PLZ} and t_{PZL} are the same as t_{PD} .
- E. t_{PZL} is measured at V_{M} .
- F. t_{PLZ} is measured at V_{OL} + V_{Δ} .



Marking Information

(1) SOT26, SOT363



 $\frac{XX}{Y}: Identification Code \\ \underline{Y}: Year 0~9$

<u>W</u>: Week : A~Z : 1~26 Week; a~z : 27~52 Week; z Represents

52 and 53 Week \underline{X} : A~Z: Internal Code

Part Number	Package	Identification Code
74LVC2G07W6-7	SOT26	Z4
74LVC2G07DW-7	SOT363	Z4

(2) X1-DFN1010-6, X2-DFN1010-6, X2-DFN1409-6, X2-DFN1410-6

(Top View)

 $\frac{XX}{Y}: Identification Code \\ \underline{Y}: Year 0~9$

<u>W</u>: Week: A~Z: 1~26 Week; a~z: 27~52 Week; z Represents

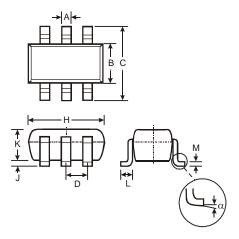
52 and 53 Week X: A~Z: Internal Code

Part Number	Package	Identification Code
74LVC2G07FW4-7	X2-DFN1010-6	Z 4
74LVC2G07FW5-7	X1-DFN1010-6	W4
74LVC2G07FX4-7	X2-DFN1409-6	X4
74LVC2G07FZ4-7	X2-DFN1410-6	Z4

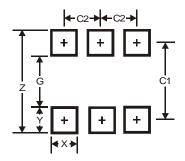


SOT26 Package Outline Dimensions and Suggested Pad Layout

 $Please see AP02002 \ at \ http://www.diodes.com/datasheets/ap02002.pdf \ for \ the \ latest \ version.$



	SOT26				
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
ပ	2.70	3.00	2.80		
D	_	_	0.95		
Н	2.90	3.10	3.00		
J	0.013	0.10	0.05		
K	1.00	1.30	1.10		
L	0.35	0.55	0.40		
М	0.10	0.20	0.15		
α	0°	8°	_		
All D	All Dimensions in mm				

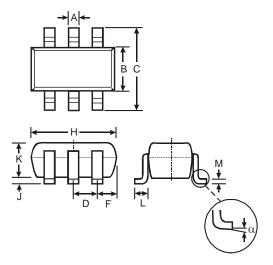


Dimensions	Value (in mm)	
Z	3.20	
G	1.60	
Х	0.55	
Υ	0.80	
C1	2.40	
C2	0.95	

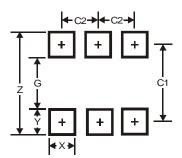


SOT363 Package Outline Dimensions and Suggested Pad Layout

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



	SOT363			
Dim	Min	Max	Тур	
Α	0.10	0.30	0.25	
В	1.15	1.35	1.30	
С	2.00	2.20	2.10	
D	0.65 Typ			
F	0.40	0.45	0.425	
Н	1.80	2.20	2.15	
J	0	0.10	0.05	
K	0.90	1.00	1.00	
L	0.25	0.40	0.30	
М	0.10	0.22	0.11	
α	0°	8°	-	
All	All Dimensions in mm			

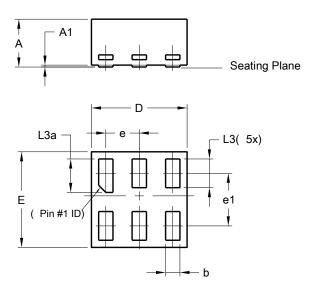


Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Υ	0.6
C1	1.9
C2	0.65

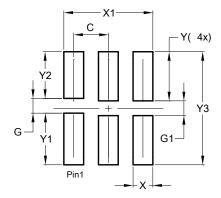


X1-DFN1010-6 (Type B) Package Outline Dimensions and Suggested Pad Layout

 $Please see AP02002 \ at \ http://www.diodes.com/datasheets/ap02002.pdf \ for \ the \ latest \ version.$



	X1-DFN1010-6 (Type B)				
Dim	Min	Max	Тур		
Α	-	0.50	0.39		
A1	-	0.04	-		
b	0.12	0.20	0.15		
D	0.95	1.050	1.00		
Е	0.95	1.050	1.00		
е		0.35 B	SC		
e1		0.55 B	SC		
L3	0.27	0.30	0.30		
L3a	0.32	0.40	0.35		
All	Dimen	sions	in mm		

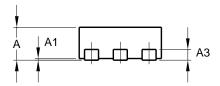


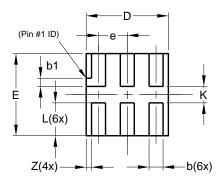
Dimensions	Value (in mm)	
С	0.350	
G	0.150	
G1	0.150	
Х	0.200	
X1	0.900	
Υ	0.500	
Y1	0.525	
Y2	0.475	
Y3	1.150	



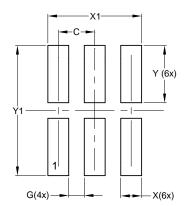
X2-DFN1010-6 Package Outline Dimensions and Suggested Pad Layout

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.





	X2-DFN1010-6				
Dim	Min	Max	Тур		
Α	_	0.40	0.39		
A1	0.00	0.05	0.02		
A3			0.13		
b	0.14	0.20	0.17		
b1	0.05	0.15	0.10		
D	0.95	1.05	1.00		
E	0.95	1.05	1.00		
е			0.35		
L	0.35	0.45	0.40		
K	0.15				
Z			0.065		
All	All Dimensions in mm				

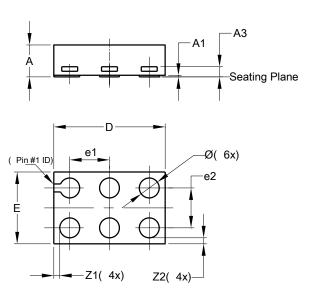


Dimensions	Value (in mm)	
С	0.350	
G	0.150	
Х	0.200	
X1	0.900	
Υ	0.550	
Y1	1.250	

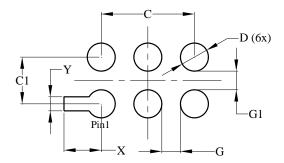


X2-DFN1409-6 Package Outline Dimensions and Suggested Pad Layout

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



X2-DFN1409-6			
Dim	Min	Max	Тур
Α	_	0.40	0.39
A1	0	0.05	0.02
А3	_	-	0.13
Ø	0.20	0.30	0.25
D	1.35	1.45	1.40
Е	0.85	0.95	0.90
e1	_	1	0.50
e2	_	_	0.50
Z1	_		0.075
Z2	_		0.075
All Dimensions in mm			

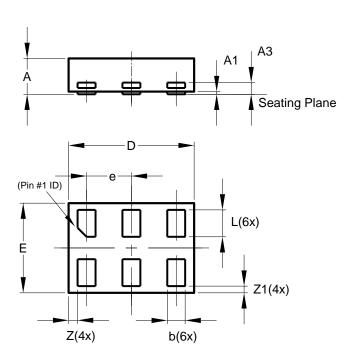


Dimensions	Value (in mm)
С	1.000
C1	0.500
D	0.300
G	0.200
G1	0.200
Х	0.400
Y	0.150

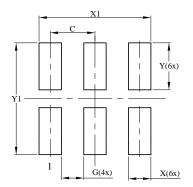


X2-DFN1410-6 Package Outline Dimensions and Suggested Pad Layout

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



X2-DFN1410-6			
Dim	Min	Max	Тур
Α	_	0.40	0.39
A1	0.00	0.05	0.02
A3			0.13
b	0.15	0.25	0.20
D	1.35	1.45	1.40
Е	0.95	1.05	1.00
е	_	_	0.50
L	0.25	0.35	0.30
Z			0.10
Z 1	0.045	0.105	0.075
All Dimensions in mm			



Dimensions	Value (in mm)
С	0.500
G	0.250
Х	0.250
X1	1.250
Y	0.525
Y1	1.250

March 2015



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 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

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