

FJ4B01120L

Single P-channel MOS FET

■ Features

- Drain-source On-state Resistance : $R_{DS(on)}$ typ. = 40 m Ω ($V_{GS} = -2.5$ V)
- CSP(Chip Size Package)
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL : Level 1)

■ Marking Symbol : 1F

■ Packaging

Embossed type (Thermo-compression sealing) : 20 000 pcs / reel (standard)

■ Absolute Maximum Ratings $T_a = 25$ °C

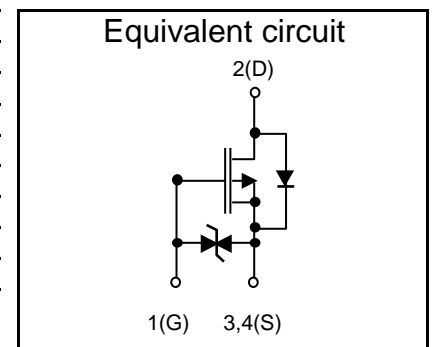
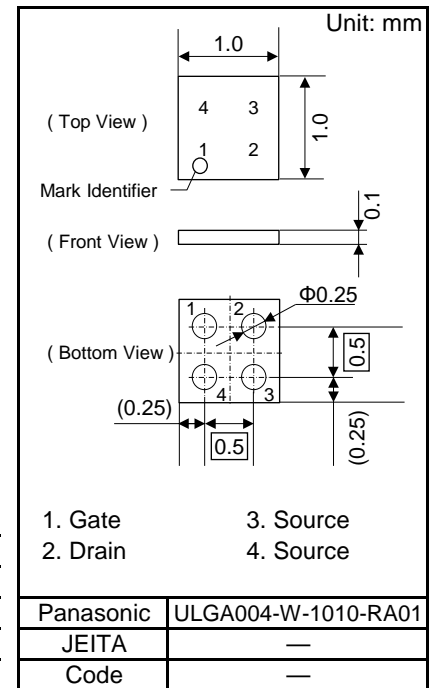
Parameter		Symbol	Rating	Unit
Drain-source Voltage		VDS	-12	V
Gate-source Voltage		VGS	± 8	V
Drain Current	DC	ID1 ^{*1}	-2.6	A
		ID2 ^{*2}	-4.2	A
		ID3 ^{*3}	-5.4	A
	Pulsed ^{*4}	IDp1	-20	A
		IDp2	-33	A
		IDp3	-43	A
Total Power Dissipation		PD1 ^{*1}	0.37	W
		PD2 ^{*2}	0.94	W
		PD3 ^{*3}	1.5	W
Channel Temperature		Tch	150	°C
Operating Ambient Temperature		Topr	-40 to +85	°C
Storage Temperature Range		Tstg	-55 to +150	°C

Note *1 FR4 board (25.4mm×25.4mm×1.0mm), Min Cu 36mm² Copper.

*2 FR4 board (25.4mm×25.4mm×1.0mm), Full Cu.

*3 Ceramic substrate (70mm×70mm×1.0mm).

*4 $t = 10$ μ s, Duty Cycle ≤ 1 %





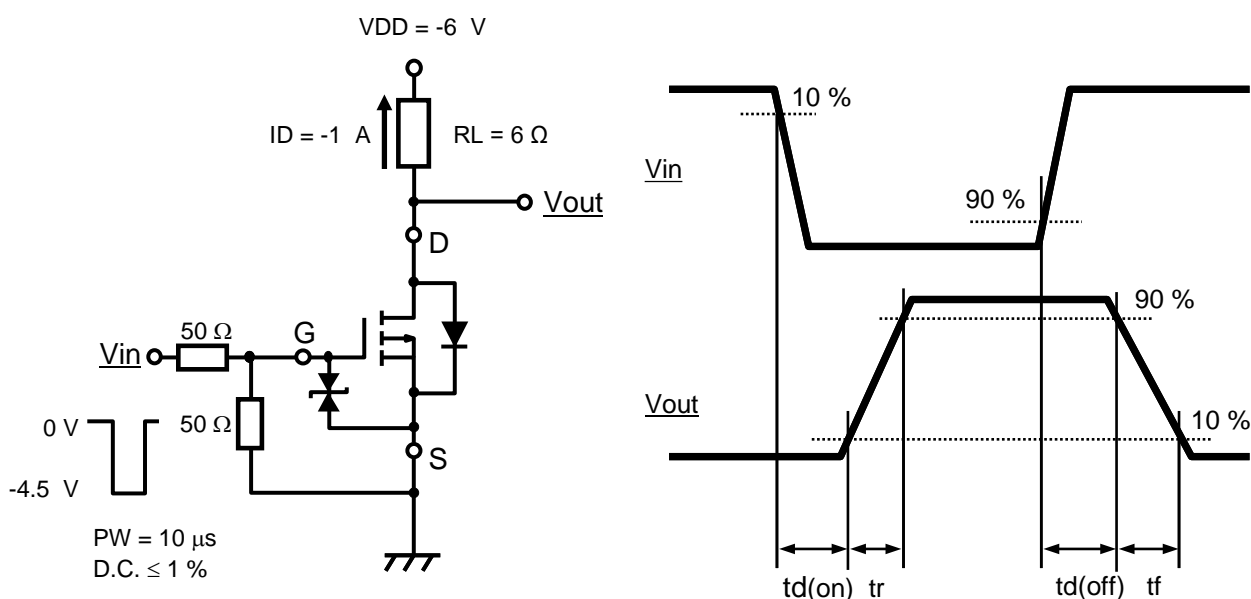
■ Electrical Characteristics Ta = 25 °C ± 3 °C

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-source Breakdown Voltage	VDSS	ID = -1 mA, VGS = 0	-12			V
Zero Gate Voltage Drain Current	IDSS	VDS = -12 V, VGS = 0			-1	μA
Gate-source Leakage Current	IGSS	VGS = ±8 V, VDS = 0 V			±10	μA
Gate-source Threshold Voltage	Vth	ID = -2 mA, VDS = -10 V	-0.3		-1.0	V
Drain-source On-state Resistance	RDS(on)1	ID = -2 A, VGS = -4.5 V		34	51	mΩ
	RDS(on)2	ID = -2 A, VGS = -2.5 V		40	61	
	RDS(on)3	ID = -0.2 A, VGS = -1.8 V		48	85	
	RDS(on)4	ID = -0.1 A, VGS = -1.5 V		57	170	
Body Diode Forward Voltage	VF(s-d)	IF = -0.2 A, VGS = 0 V		-0.7	-1.2	V
Input Capacitance ^{*1}	Ciss	VDS = -10 V, VGS = 0 V f = 1 MHz		814		pF
Output Capacitance ^{*1}	Coss			201		
Reverse Transfer Capacitance ^{*1}	Crss			187		
Turn-on Delay Time ^{*1,*2}	td(on)	VDD = -6 V, VGS = 0 to -4.5 V		6		ns
Rise Time ^{*1,*2}	tr	ID = -1 A		4		
Turn-off Delay Time ^{*1,*2}	td(off)	VDD = -6 V, VGS = -4.5 to 0 V		63		
Fall Time ^{*1,*2}	tf	ID = -1 A		46		
Total Gate Charge ^{*1}	Qg	VDD = -6 V, VGS = -4.5 V ID = -1 A		10.7		nC
Gate-source Charge ^{*1}	Qgs			1.4		
Gate-drain Charge ^{*1}	Qgd			2.1		

Note Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

*1 Guaranteed by design, not subject to production testing.

*2 Measurement circuit for Turn-on Delay Time / Rise Time / Turn-off Delay Time / Fall Time.

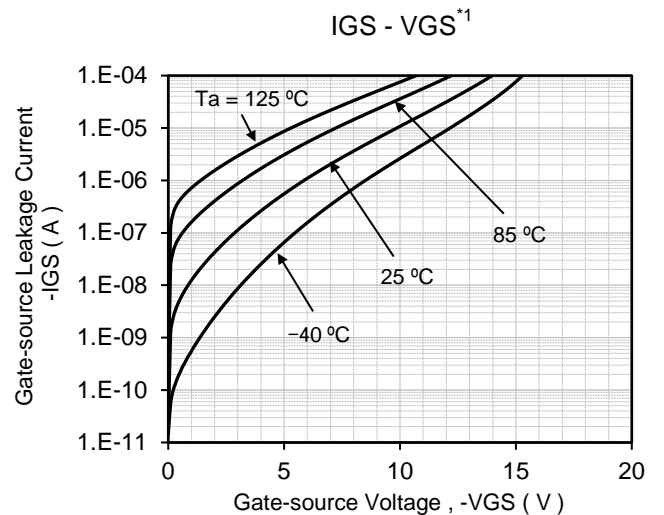
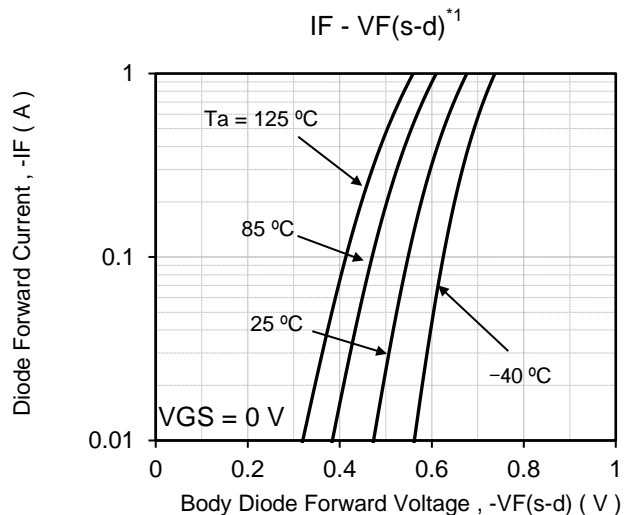
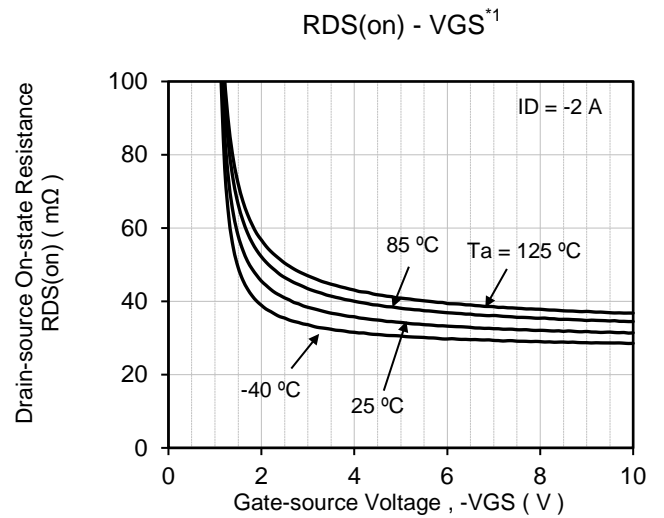
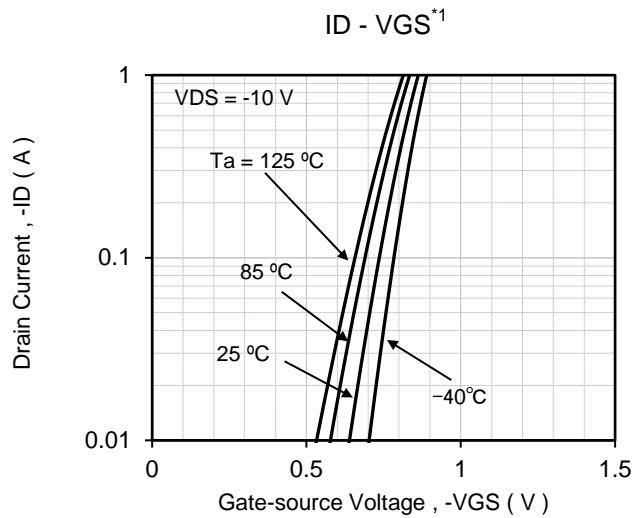
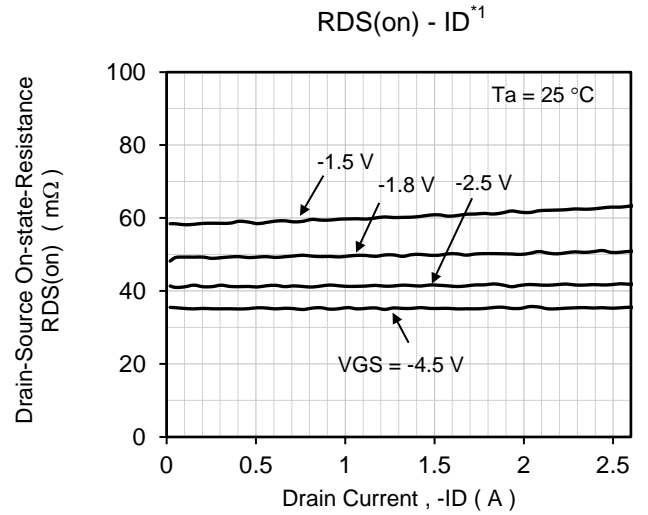
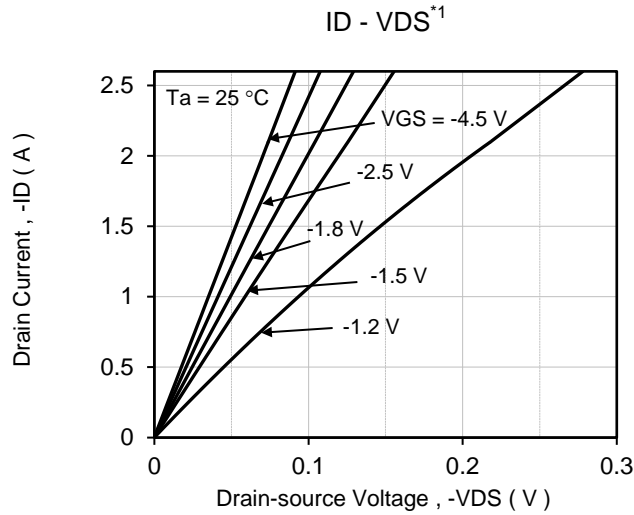


■ Electrical State Discharge Characteristics

Standard	Test Type	Symbol	Conditions	Class	Value	Unit
AEC-Q101	Human Body Model	HBM	C = 100 pF, R = 1.5 kΩ	H1C	> 1k to ≤ 2k	V
	Machine Model	MM	C = 200 pF, R = 0 Ω	M2	> 100 to ≤ 200	V

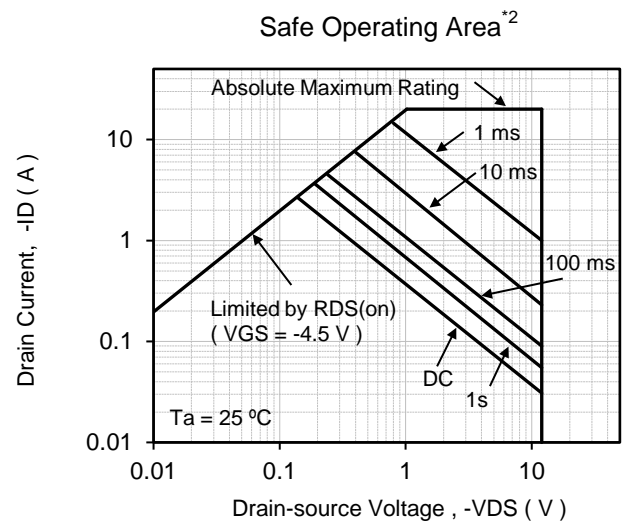
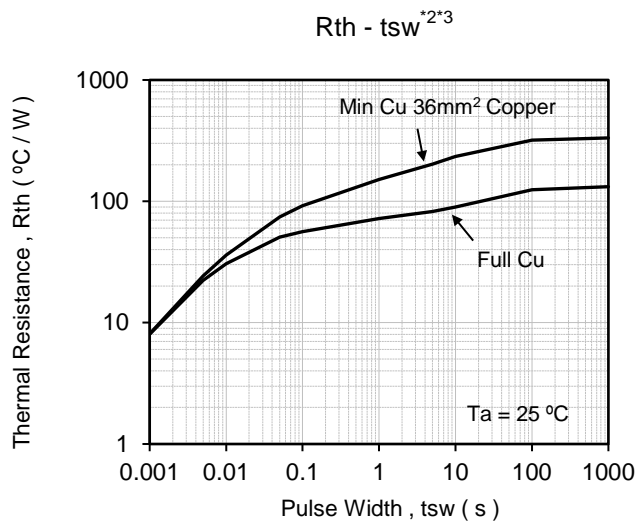
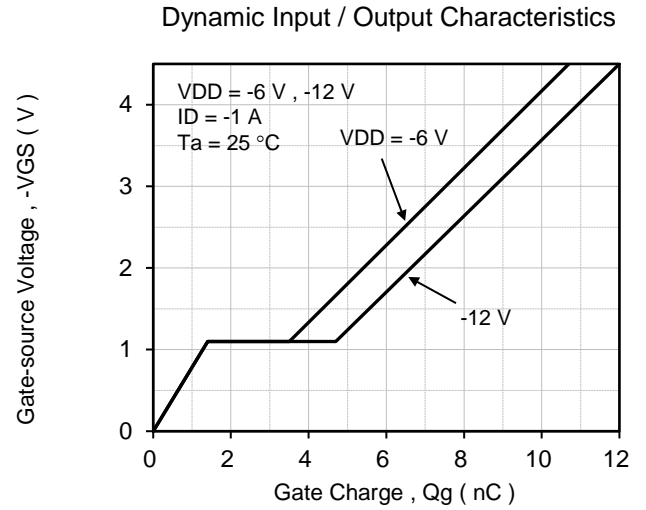
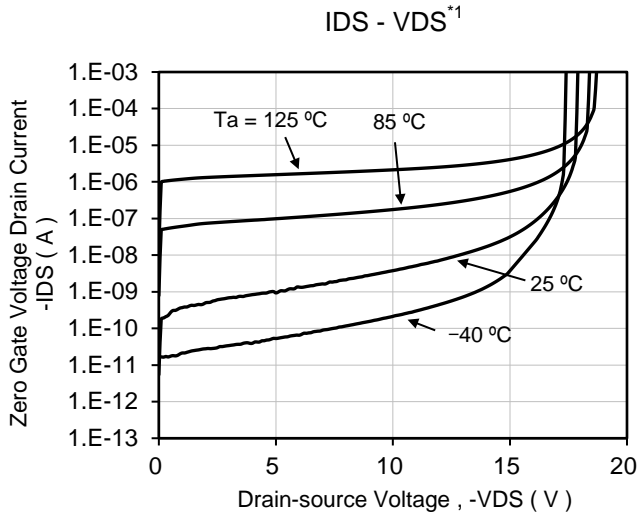


Technical Data (reference)





Technical Data (reference)

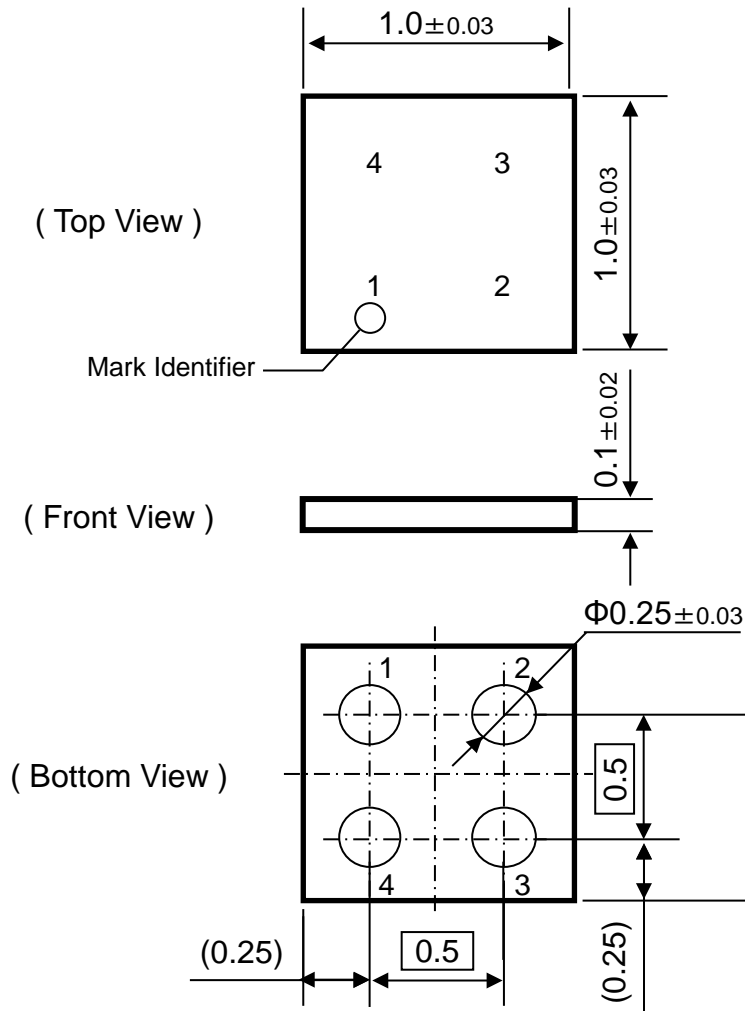


Note

- *1 Pulse measurement
- *2 FR4 board (25.4mm×25.4mm×1.0mm), Min Cu 36mm² Copper.
- *3 FR4 board (25.4mm×25.4mm×1.0mm), Full Cu.

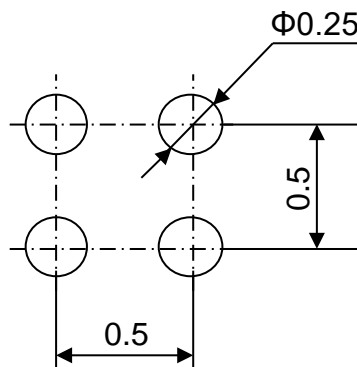
■ Outline

Unit: mm



■ Land & Stencil Pattern (Reference)

Unit: mm



Request for your special attention and precautions in using the technical information and semiconductors described in this book

- (1) If any of the products or technical information described in this book is to be exported or provided to non-residents, the laws and regulations of the exporting country, especially, those with regard to security export control, must be observed.
- (2) The technical information described in this book is intended only to show the main characteristics and application circuit examples of the products. No license is granted in and to any intellectual property right or other right owned by Panasonic Corporation or any other company. Therefore, no responsibility is assumed by our company as to the infringement upon any such right owned by any other company which may arise as a result of the use of technical information de-scribed in this book.
- (3) The products described in this book are intended to be used for general applications (such as office equipment, communications equipment, measuring instruments and household appliances), or for specific applications as expressly stated in this book.
Please consult with our sales staff in advance for information on the following applications, moreover please exchange documents separately on terms of use etc.: Special applications (such as for in-vehicle equipment, airplanes, aerospace, automotive equipment, traffic signaling equipment, combustion equipment, medical equipment and safety devices) in which exceptional quality and reliability are required, or if the failure or malfunction of the products may directly jeopardize life or harm the human body.
Unless exchanging documents on terms of use etc. in advance, it is to be understood that our company shall not be held responsible for any damage incurred as a result of or in connection with your using the products described in this book for any special application.
- (4) The products and product specifications described in this book are subject to change without notice for modification and/or improvement. At the final stage of your design, purchasing, or use of the products, therefore, ask for the most up-to-date Product Standards in advance to make sure that the latest specifications satisfy your requirements.
- (5) When designing your equipment, comply with the range of absolute maximum rating and the guaranteed operating conditions (operating power supply voltage and operating environment etc.). Especially, please be careful not to exceed the range of absolute maximum rating on the transient state, such as power-on, power-off and mode-switching. Otherwise, we will not be liable for any defect which may arise later in your equipment.
Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.
- (6) Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. We do not guarantee quality for disassembled products or the product re-mounted after removing from the mounting board.
When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.
- (7) When reselling products described in this book to other companies without our permission and receiving any claim of request from the resale destination, please understand that customers will bear the burden.
- (8) This book may be not reprinted or reproduced whether wholly or partially, without the prior written permission of our company.

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for [MOSFET](#) category:

Click to view products by [Panasonic](#) manufacturer:

Other Similar products are found below :

[614233C](#) [648584F](#) [MCH3443-TL-E](#) [MCH6422-TL-E](#) [FDPF9N50NZ](#) [FW231A-TL-E](#) [APT5010JVR](#) [NTNS3A92PZT5G](#) [IRF100S201](#)
[JANTX2N5237](#) [2SK2464-TL-E](#) [2SK3818-DL-E](#) [FCA20N60_F109](#) [FDZ595PZ](#) [STD6600NT4G](#) [FSS804-TL-E](#) [2SJ277-DL-E](#) [2SK1691-DL-](#)
[E](#) [2SK2545\(Q,T\)](#) [D2294UK](#) [405094E](#) [423220D](#) [MCH6646-TL-E](#) [TPCC8103,L1Q\(CM](#) [367-8430-0972-503](#) [VN1206L](#) [424134F](#) [026935X](#)
[051075F](#) [SBVS138LT1G](#) [614234A](#) [715780A](#) [NTNS3166NZT5G](#) [751625C](#) [873612G](#) [IRF7380TRHR](#) [IPS70R2K0CEAKMA1](#)
[RJK60S3DPP-E0#T2](#) [RJK60S5DPK-M0#T0](#) [APT5010JVFR](#) [APT12031JFLL](#) [APT12040JVR](#) [DMN3404LQ-7](#) [NTE6400](#) [JANTX2N6796U](#)
[JANTX2N6784U](#) [JANTXV2N5416U4](#) [SQM110N05-06L-GE3](#) [SIHF35N60E-GE3](#) [2SK2614\(TE16L1,Q\)](#)