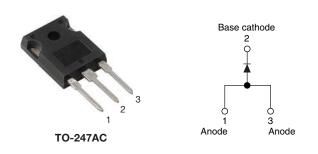
Vishay Semiconductors

High Performance Schottky Rectifier, 65 A



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PRODUCT SUMMARY	
Package	TO-247AC
I _{F(AV)}	65 A
V _R	15 V
V _F at I _F	0.46 V
I _{RM} max.	870 mA at 100 °C
T _J max.	125 °C
Diode variation	Single die
E _{AS}	9 mJ

FEATURES

- 125 °C T_J operation ($V_R < 5 V$)
- Single diode configuration
- Optimized for OR-ing applications
- Ultralow forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability



- RoHS COMPLIANT HALOGEN
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Designed and gualified according to JEDEC-JESD47
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-65PQ015... Schottky rectifier module has been optimized for ultralow forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	65	А						
V _{RRM}		15	V						
I _{FSM}	t _p = 5 μs sine	1500	А						
V _F	65 A _{pk} , T _J = 125 °C	0.46	V						
TJ	Range	- 55 to 125	°C						

VOLTAGE RATINGS										
PARAMETER	SYMBOL	TEST CONDITIONS	VS-65PQ015PbF	VS-65PQ015-N3	UNITS					
Maximum DC reverse voltage	V _R	T _J = 100 °C	15	15	V					
		T _J = 125 °C	5	5	v					

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS					
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_{C} = 83 °C, r	65						
Maximum peak one cycle	leave.	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	1500	A				
non-repetitive surge current	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	400					
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 4.5 m⊦	9	mJ					
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero Frequency limited by ${\sf T}_{\sf J}$ maximum	2	А					

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PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS	
		65 A	T _{.1} = 25 °C	0.50		
Forward voltage drop	V _{FM} ⁽¹⁾	130 A	$1_{\rm J} = 25$ C	0.71		
Forward voltage drop	VFM ("	65 A	T, = 125 °C	0.46	V	
		130 A	1j = 125 C	0.76		
Reverse leakage current		T _J = 125 °C	V _R = 5 V	1.2	А	
	I _{RM} ⁽¹⁾	T _J = 25 °C	V Detect V	18		
		T _J = 100 °C	$V_R = Rated V_R$	870	mA	
Threshold voltage	V _{F(TO)}	T T maximum		0.137	mV	
Forward slope resistance	r _t	$T_J = T_J maximum$		4.9	mΩ	
Maximum junction capacitance	CT	$V_R = 5 V_{DC}$ (test signal ran	ge 100 kHz to 1 MHz) 25 °C	4300	pF	
Typical series inductance	L _S	Measured lead to lead 5 m	nm from package body	8	nH	
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs		

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction temperature range	TJ		- 55 to 125	°C				
Maximum storage temperature range	T _{Stg}		- 55 to 150					
Maximum thermal resistance, junction to case	R _{thJC}	DC operation	0.8	°C/W				
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth and greased	0.3					
Approvimeto weight			6	g				
Approximate weight			0.21	oz.				
Mounting torque		Non lubricated threads	6 (5)	kgf · cm (lbf · in)				
Mounting torque maximum	1	Non-lubricated threads	12 (10)					
Marking device		Case style TO-247AC (JEDEC)	65P0	Q015				



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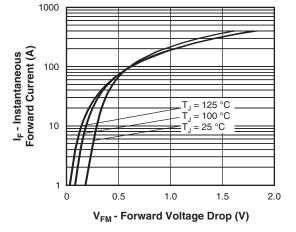
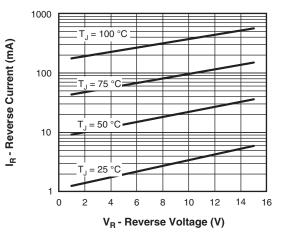
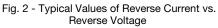


Fig. 1 - Maximum Forward Voltage Drop Characteristics





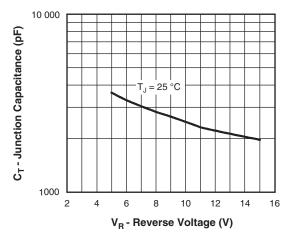
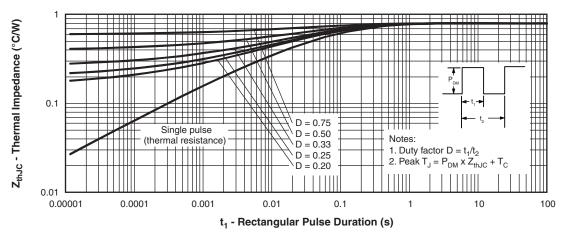
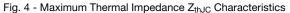


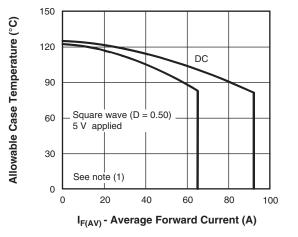
Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



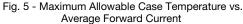


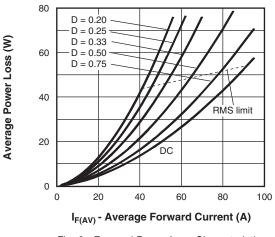
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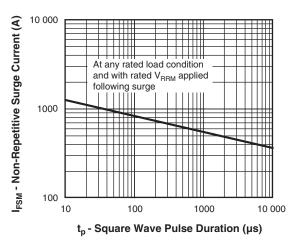
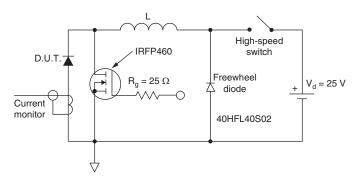
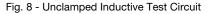


Fig. 7 - Maximum Non-Repetitive Surge Current





Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 5 V

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ORDERING INFORMATION TABLE

Device code	VS-	65	Р	Q	015	PbF
	1	2	3	4	5	6
	1 - 2 - 3 -	Curr Pac	•	iconduc ng (65 =		duct
	4 -	Sch	ottky "Q	" series		
	5 -	Volt	age cod	e (015 =	= 15 V)	
	6 -	Env	ironmen	ital digit		
		• P	bF = Le	ad (Pb)-	free and	d RoHS

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-65PQ015PbF	25	500	Antistatic plastic tube				
VS-65PQ015-N3	25	500	Antistatic plastic tube				

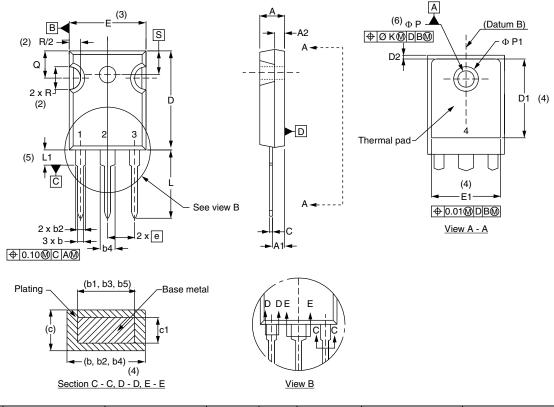
LINKS TO RELATED DOCUMENTS							
Dimensions		www.vishay.com/doc?95542					
Part marking information	TO-247AC modified PbF	www.vishay.com/doc?95226					
	TO-247AC modified -N3	www.vishay.com/doc?95007					
SPICE model		www.vishay.com/doc?95306					





TO-247AC - 50 mils L/F

DIMENSIONS in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES	
STNIBOL	MIN.	MAX.	MIN.	MAX.	NOTES		STWBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			e	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ØК	0.2	254	0.0)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			ØР	3.56	3.66	0.14	0.144	
b5	2.59	3.38	0.102	0.133			Ø P1	-	7.39	-	0.291	
С	0.38	0.89	0.015	0.035			Q	5.31	5.69	0.209	0.224	
c1	0.38	0.84	0.015	0.033			R	4.52	5.49	0.178	0.216	
D	19.71	20.70	0.776	0.815	3		S	5.51	BSC	0.217	BSC	
D1	13.08	-	0.515	-	4							

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁴⁾ Thermal pad contour optional with dimensions D1 and E1

⁽⁵⁾ Lead finish uncontrolled in L1

⁽⁶⁾ Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-247 with exception of dimension c and Q

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