InvenSense ICS-40181

RF-Hardened, Low-Noise Microphone with Top Port and Analog Output

GENERAL DESCRIPTION

The ICS-40181 is an analog MEMS microphone with high SNR and enhanced RF immunity. The ICS-40181 includes a MEMS microphone element, an impedance converter, and an output amplifier.

Other high-performance specification include a linear response up to 124 dB SPL, tight ±1 dB sensitivity tolerance and enhanced immunity to both radiated and conducted RF interference.

This microphone's electro-acoustic performance matches the bottom port ICS-40180, making this pair of microphones suitable to use together in applications requiring both top and bottom port devices.

The ICS-40181 is available in a small 3.5 mm × 2.65 mm × 0.98 mm surface-mount package.

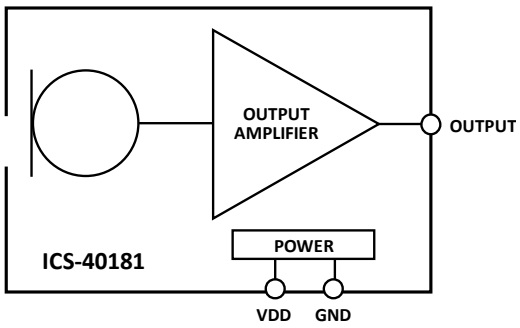
APPLICATIONS

- Smartphones
- Tablet Computers
- Wearable Devices
- Still and Video Cameras
- Bluetooth Headsets
- Notebook PCs
- Security and Surveillance

FEATURES

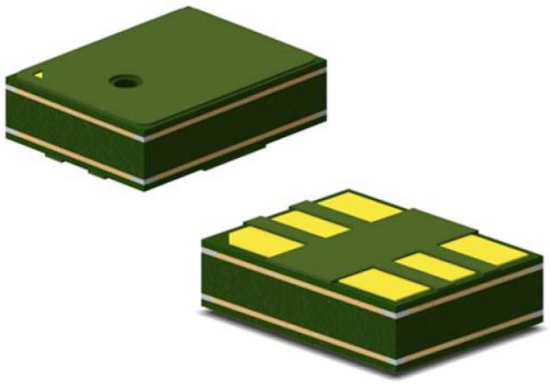
- High 65 dBA SNR
- −38 dBV Sensitivity
- ±1 dB Sensitivity Tolerance
- Noninverted Signal Output
- Omnidirectional Response
- Extended Frequency Response from 60 Hz to 20 kHz
- Enhanced RF Immunity
- 124 dB SPL Acoustic Overload Point
- Low Current Consumption of 180 µA
- Single-Ended Analog Output
- High −78 dBV PSR
- 3.5 × 2.65 × 0.98 mm Surface-Mount Package
- Compatible with Sn/Pb and Pb-Free Solder Processes
- RoHS/WEEE Compliant

FUNCTIONAL BLOCK DIAGRAM



ORDERING INFORMATION

PART	TEMP RANGE	PACKAGING
ICS-40181	−40°C to +85°C	13" Tape and Reel
EV_ICS-40181-FX	—	



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SPECIFICATIONS

TABLE 1. ELECTRICAL CHARACTERISTICS

(T_A = −40 to 85°C, V_{DD} = 1.5 to 3.63 V, unless otherwise noted. All minimum and maximum specifications are guaranteed across temperature and voltage specified in Table 1, unless otherwise noted. Typical specifications are not guaranteed.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS	NOTES
PERFORMANCE						
Directionality			Omni			
Output Polarity			Non-Inverted			
Sensitivity	1 kHz, 94 dB SPL	−39	−38	−37	dBV	1
NORMAL MODE PERFORMANCE						
Signal-to-Noise Ratio (SNR)	20 Hz to 20 kHz, A-weighted		65		dBA	
Equivalent Input Noise (EIN)	20 Hz to 20 kHz, A-weighted		29		dBA SPL	
Dynamic Range	Derived from EIN and maximum acoustic input		95		dB	
Frequency Response	Low frequency −3 dB point		60		Hz	2
	High frequency −3 dB point		>20		kHz	
Total Harmonic Distortion (THD)	105 dB SPL		0.2	1	%	
Power-Supply Rejection (PSR)	217 Hz, 100 mVp-p square wave superimposed on V_{DD} = 1.8 V		−78		dBV	
Power Supply Rejection Ratio (PSRR)	1 kHz, 100 mV p-p sine wave superimposed on V_{DD} = 1.8 V		−46		dB	
Acoustic Overload Point	10% THD		124		dB SPL	
POWER SUPPLY						
Supply Voltage (V_{DD})	Normal Mode	1.5		3.63	V	
Supply Current (I_S)	V_{DD} = 1.8 V		180	220	µA	
	V_{DD} = 3.3 V		210	250	µA	
OUTPUT CHARACTERISTICS						
Output Impedance			350		Ω	
Output DC Offset			0.7		V	
Maximum Output Voltage	124 dB SPL input		0.398		V rms	
Noise Floor	20 Hz to 20 kHz, A-weighted, rms		−103		dBV	

Note 1: The sensitivity shall not deviate more than 1.5 dB from its initial value after reliability tests.
Note 2: See Figure 3 and Figure 4 .

ABSOLUTE MAXIMUM RATINGS

Stress above those listed as Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these conditions is not implied. Exposure to the absolute maximum ratings conditions for extended periods may affect device reliability.

TABLE 2. ABSOLUTE MAXIMUM RATINGS

PARAMETER	RATING
Supply Voltage (V_{DD})	−0.3 V to +3.63 V
Sound Pressure Level	160 dB
Mechanical Shock	10,000 *g*
Vibration	Per MIL-STD-883 Method 2007, Test Condition B
Temperature Range	
Biased	−40°C to +85°C
Storage	−55°C to +150°C

ESD CAUTION

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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SOLDERING PROFILE

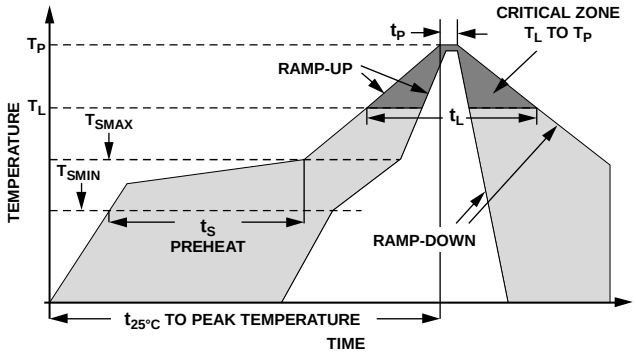

Figure 1. Recommended Soldering Profile Limits

TABLE 3. RECOMMENDED SOLDERING PROFILE*

PROFILE FEATURE		Sn63/Pb37	Pb-Free
Average Ramp Rate (T_L to T_P)		1.25°C/sec max	1.25°C/sec max
Preheat	Minimum Temperature (T_{SMIN})	100°C	100°C
	Minimum Temperature (T_{SMIN})	150°C	200°C
	Time (T_{SMIN} to T_{SMAX}), t_S	60 sec to 75 sec	60 sec to 75 sec
Ramp-Up Rate (T_{SMAX} to T_L)		1.25°C/sec	1.25°C/sec
Time Maintained Above Liquidous (t_L)		45 sec to 75 sec	~50 sec
Liquidous Temperature (T_L)		183°C	217°C
Peak Temperature (T_P)		215°C +3°C/−3°C	260°C +0°C/−5°C
Time Within +5°C of Actual Peak Temperature (t_P)		20 sec to 30 sec	20 sec to 30 sec
Ramp-Down Rate		3°C/sec max	3°C/sec max
Time +25°C ($t_{25°C}$) to Peak Temperature		5 min max	5 min max

**The reflow profile in* Table 3 *is recommended for board manufacturing with InvenSense MEMS microphones. All microphones are also compatible with the J-STD-020 profile*

PIN CONFIGURATIONS AND FUNCTION DESCRIPTIONS

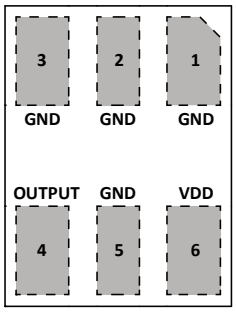

Figure 2. Pin Configuration (Top View, Terminal Side Down)

TABLE 4. PIN FUNCTION DESCRIPTIONS

PIN	NAME	FUNCTION
1	GND	Ground
2	GND	Ground
3	GND	Ground
4	OUTPUT	Analog Output Signal
5	GND	Ground
6	VDD	Power Supply. Decouple to GND pin with 0.1 µF capacitor

TYPICAL PERFORMANCE CHARACTERISTICS

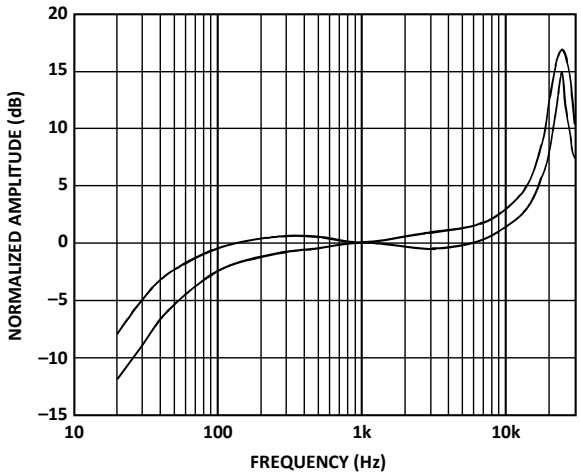

Figure 3. Frequency Response Mask

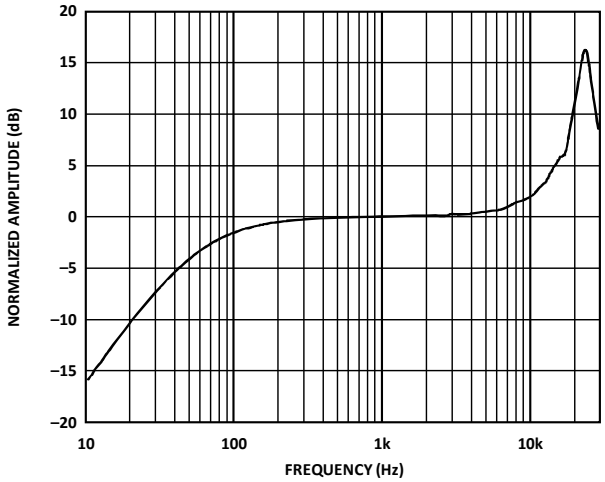

Figure 4. Typical Frequency Response (Measured)

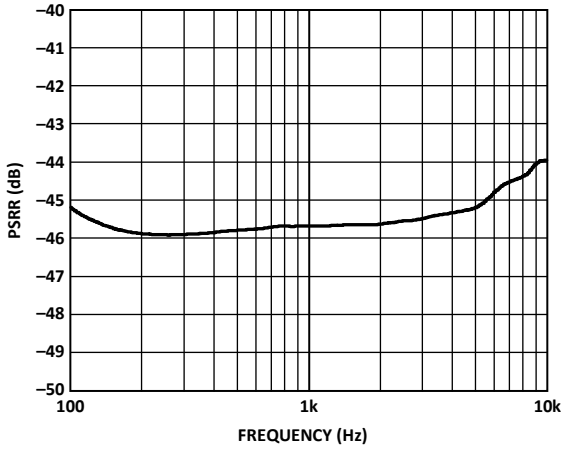

Figure 5. PSR vs. Frequency, 100 mV p-p Swept Sine Wave

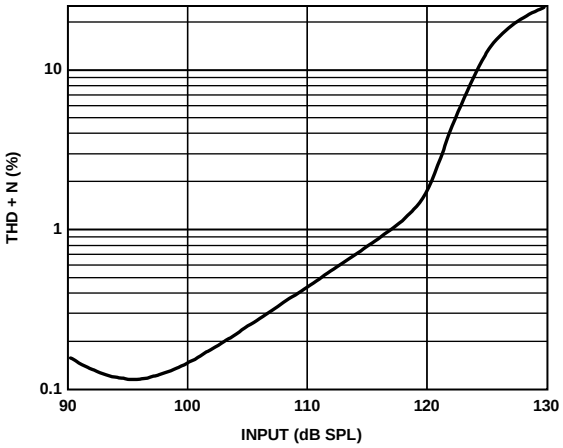

Figure 6. Total Harmonic Distortion + Noise (THD+N) vs. Input SPL

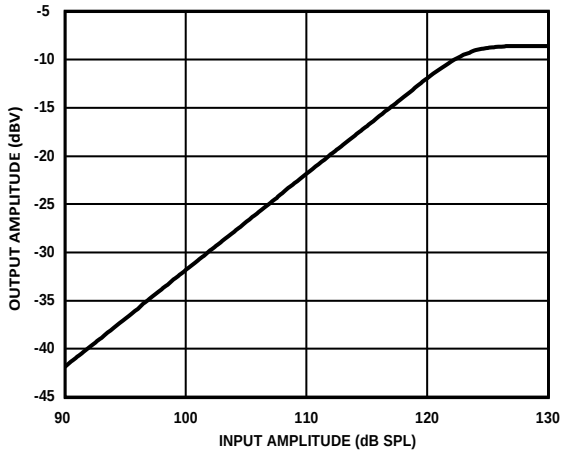

Figure 7. Linearity

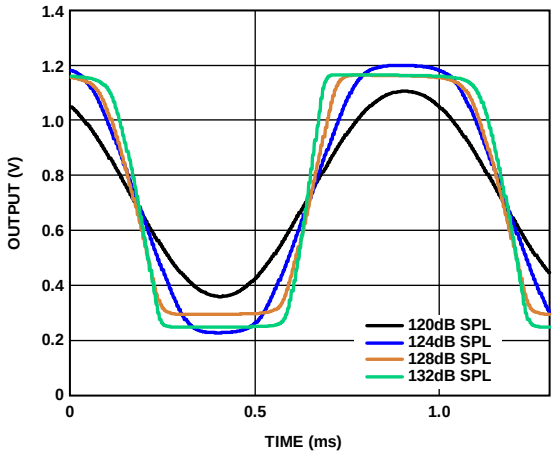

Figure 8. Clipping Characteristics

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APPLICATIONS INFORMATION

CODEC CONNECTION

The ICS-40181 output can be connected to a dedicated codec microphone input (see Figure 9) or to a high input impedance gain stage. A 0.1 µF ceramic capacitor placed close to the ICS-40181 supply pin is used for testing and is recommended to adequately decouple the microphone from noise on the power supply. A DC blocking capacitor is required at the output of the microphone. This capacitor creates a high-pass filter with a corner frequency at

$$f_C = 1/(2\pi \times C \times R)$$

where, *R* is the input impedance of the codec.

A minimum value of 2.2 µF is recommended in Figure 9 because the input impedance of some codecs can be as low as 2 kΩ at their highest PGA gain setting, which results in a high-pass filter corner frequency at 37 Hz. Figure 10 shows the ICS-40181 connected to an op amp configured as a noninverting preamplifier.

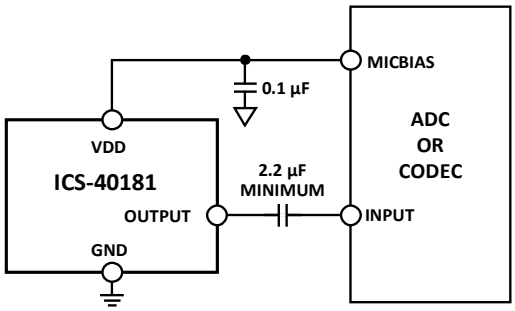

Figure 9. ICS-40181 Connected to a Codec

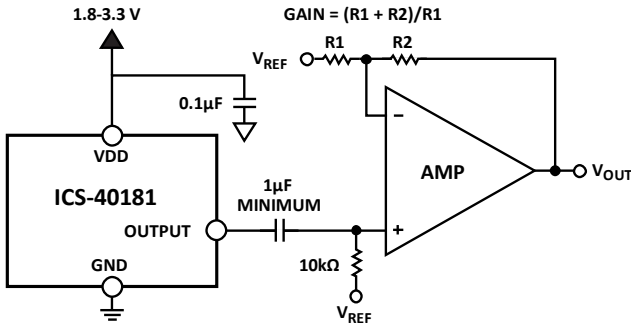

Figure 10. ICS-40181 Connected to an Op Amp

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SUPPORTING DOCUMENTS

For additional information, see the following documents.

EVALUATION BOARD USER GUIDE

UG-325, *Analog Output MEMS Microphone Flex Evaluation Board*

APPLICATION NOTES

AN-100, *MEMS Microphone Handling and Assembly Guide*
AN-1003, *Recommendations for Mounting and Connecting the InvenSense Bottom-Ported MEMS Microphones*
AN-1112, *Microphone Specifications Explained*
AN-1124, *Recommendations for Sealing InvenSense Bottom-Port MEMS Microphones from Dust and Liquid Ingress*
AN-1140, *Microphone Array Beamforming*
AN-1165, *Op Amps for Microphone Preamp Circuits*
AN-1181, *Using a MEMS Microphone in a 2-Wire Microphone Circuit*

PCB DESIGN AND LAND PATTERN LAYOUT

Lay out the PCB land pattern for the ICS-40181 at a 1:1 ratio to the solder pads on the microphone package (see Figure 11.) Figure 12 shows a suggested solder paste stencil pattern layout.

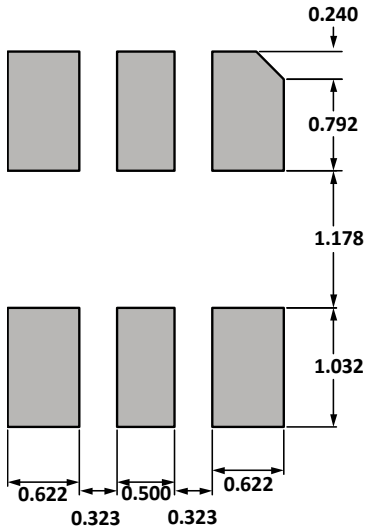


Figure 11. Recommended PCB Land Pattern Layout

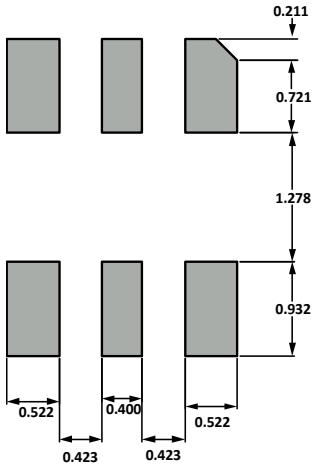


Figure 12. Recommended Solder Paste Stencil Pattern Layout

PCB MATERIAL AND THICKNESS

The ICS-40181 can be mounted on either a rigid or flexible PCB. A microphone's lid can be attached directly to the device housing with an adhesive layer. This mounting method offers a reliable seal around the sound port while providing the shortest acoustic path for good sound quality. The sound port can also be routed to the device housing through a port in a rubber boot. This boot should be designed to seal the connection between the microphone's lid and the rubber completely.

HANDLING INSTRUCTIONS

PICK AND PLACE EQUIPMENT

The MEMS microphone can be handled using standard pick-and-place and chip shooting equipment. Take care to avoid damage to the MEMS microphone structure as follows:

- Use a standard pickup tool to handle the microphone. Because the microphone hole is on the top of the package, the pickup tool should not be placed over the microphone port.
- Do not pull air out of or blow air into the microphone port.
- Do not use excessive force to place the microphone on the PCB.

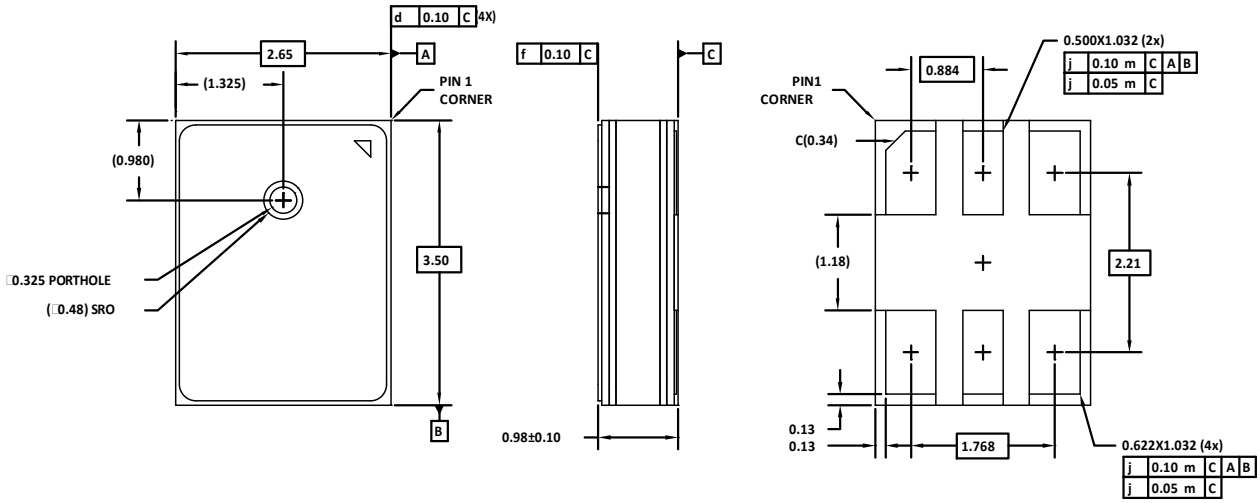
REFLOW SOLDER

For best results, the soldering profile must be in accordance with the recommendations of the manufacturer of the solder paste used to attach the MEMS microphone to the PCB. It is recommended that the solder reflow profile not exceed the limit conditions specified in Figure 1 and Table 3.

BOARD WASH

When washing the PCB, ensure that water does not make contact with the microphone port. Do not use blow-off procedures or ultrasonic cleaning.

OUTLINE DIMENSIONS



TOP VIEW **SIDE VIEW** **BOTTOM VIEW**

Figure 13. 6-Terminal Chip Array Small Outline No-Lead Cavity
3.50 mm × 2.65 mm × 0.98 mm Body

Dimensions shown in millimeters

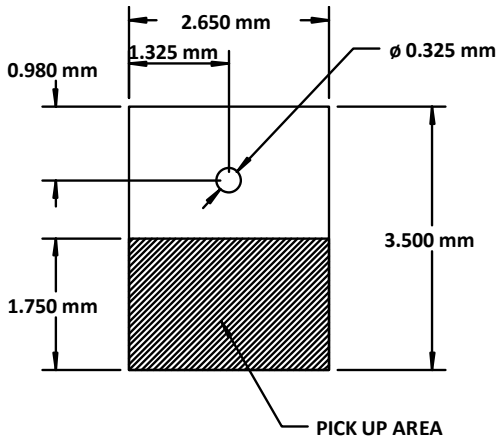


Figure 14. Recommended Vacuum Pick-up Area

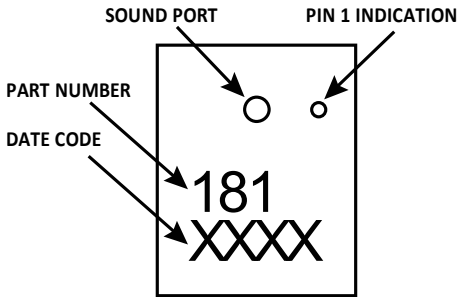


Figure 15. Package Marking Specification (Top View)

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ORDERING GUIDE

PART	TEMP RANGE	PACKAGE	QUANTITY	PACKAGING
ICS-40181	−40°C to +85°C	6-Terminal LGA_CAV	10,000	13” Tape and Reel
EV_ICS-40181-FX	—	Flexible Evaluation Board	—	

REVISION HISTORY

REVISION DATE	REVISION	DESCRIPTION
3/23/2015	1.0	Initial Version
4/27/2015	1.1	Updated Figures 3 and 4
07/15/2015	1.2	Added Note 1 to Table 1
04/06/2016	1.3	Updated Sensitivity condition in Table 1; updated Figure 7.

COMPLIANCE DECLARATION DISCLAIMER

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