

Surge arrester

2-electrode arrester

Series/Type: Ordering code:	ES90XSMD B88069X6241T902	
Date:	2015-07-30	
Version:	02	

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ES90XSMD

B88069X6241T902

Surge arrester

2-electrode arrester

Features

- Extremely small size
- Extremely fast response time
- Stable performance over life
- Extremely low capacitance
- High insulation resistance
- RoHS-compatible

Applications

- Modem
- XDSL-splitter
- Tuner
- Data lines
- Antenna

Electrical specifications			
DC spark-over voltage ^{1) 2)}	90	V	
Tolerance		± 20	%
Min.		72	V
Max.		108	V
Impulse spark-over voltage			
at 100 V/µs - for 99%	of measured values	< 450	V
- typical v	alues of distribution	< 300	V
at 1 kV/µs - for 99%	of measured values	< 600	V
- typical v	alues of distribution	< 550	V
Service life			
10 operations	8/20 µs	2.5	kA
1 operation	8/20 µs	5	kA
Insulation resistance at 50 V_{DC}		> 1	GΩ
Capacitance at 1 MHz		< 1	pF
Arc voltage at 1 A		~ 12	V
Glow to arc transition current	~ 0.5	A	
Glow voltage		~ 70	V
Weight		~ 0.3	g
Operation and storage temperatu	Ire	-40 +90	°C
Climatic category (IEC 60068-1)		40/090/21	
Marking, red positive EPCOSES 90 YY O ES - Series 90 - Nominal voltage YY - Year of producti O - Non radioactive		age luction	
Certification		UL 497B (E16307)	

1) At delivery AQL 0.65 level II, DIN ISO 2859

²⁾ In ionized mode

Terms in accordance with ITU-T Rec. K.12, IEC 61663-2 and IEC 61643-311

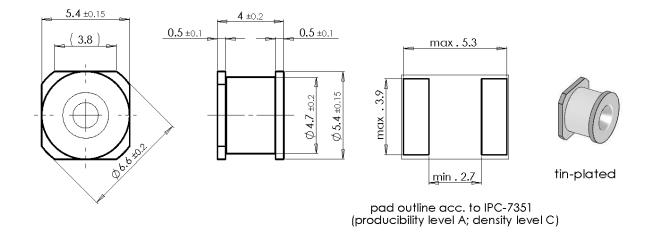


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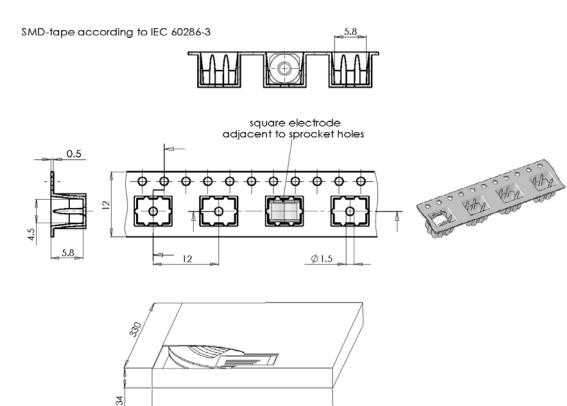
B88069X6241T902 ES90XSMD

Dimensional drawing in mm



Ordering code and packing advice

B88069X6241**T902** = 900 pcs. on SMD-tape



330

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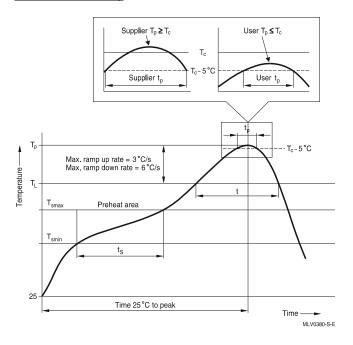
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Soldering parameter

Reflow soldering



Reflow profile features		Sn- Pb eutectic assembly	Pb-free assembly	
Preheat and soak - Temperature min - Temperature max - Time Average ramp-up	T _{smin} T _{smax} t _{smin} to t _{smax} T _{smax} to T _p	100 °C 150 °C 60 120 s max. 3 °C/ s	150 °C 200 °C 60 180 s max. 3 °C/ s	
rate Liquidous temperature Time at liquidous	T _L	183 ℃ 60 150 s	217 °C 60 150 s	
Peak package body temperature *, Classification temperature **	T _p , T _C	220 235 °C **	245 260 °C **	
Time (t_p) ** within 5 °C of the specified classification temperature (T_C)		20 s ***	30 s ***	
Average ramp-down rate	T_p to T_{smax}	max. 6 °C/ s	max. 6 °C/ s	
Time 25 °C to peak temperature		max. 6 min	max. 8 min	
 Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum. For details please refer to JEDEC J-STD-020D. Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum. 				

Cautions and warnings

- Do not operate surge arresters in power supply networks, whose maximum operating voltage exceeds the minimum spark-over voltage of the surge arresters.
- Surge arresters may become hot in the event of longer periods of current stress (burn risk). In the event of overload the connectors may fail or the component may be destroyed.
- Surge arresters must be handled with care and must not be dropped.
- Do not continue to use damaged surge arresters.
- The shown SMD pad dimensions represent a safe way to mount the arrester and are a recommendation of the manufacturer. During the reflow process it must be assured that no solder material reduces the insulation distance between the pads below the arrester.
- SMD surge arresters should be soldered within 24 month after shipment.

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