



DATA SHEET

SURFACE-MOUNT CERAMIC MULTILAYER CAPACITORS

High-Voltage

NPO/X7R I KV TO 3 KV 0.47 pF to 33 nF

RoHS compliant & Halogen Free



YAGEO Phicomp

Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

<u>SCOPE</u>

This specification describes High-Voltage NP0/X7R series chip capacitors with lead-free terminations.

APPLICATIONS

- PCs, Hard disk, Game PCs
- Power supplies
- LCD panel
- ADSL, Modem

FEATURES

- Supplied in tape on reel
- Nickel-barrier end termination
- RoHS compliant
- Halogen Free compliant

ORDERING INFORMATION - GLOBAL PART NUMBER, PHYCOMP

CTC & 12NC

All part numbers are identified by the series, size, tolerance, TC material, packing style, voltage, process code, termination and capacitance value.

YAGEO BRAND ordering code

GLOBAL PART NUMBER (PREFERRED)

CC <u>XXXX</u> <u>X</u> <u>X</u> <u>XXX</u> <u>X</u> B <u>X</u> <u>XXX</u> (1) (2) (3) (4) (5) 6() (7)

(I) SIZE - INCH BASED (METRIC)

(2) TOLERANCE

 $C = \pm 0.25 \text{ pF}$ $D = \pm 0.5 \text{ pF}$ $G = \pm 2\%$

| = ±5%

, K = ±10%

(3) PACKING STYLE

- R = Paper/PE taping reel; Reel 7 inch
- K = Blister taping reel; Reel 7 inch
- P = Paper/PE taping reel; Reel 13 inch
- F = Blister taping reel; Reel 13 inch
- C = Bulk case

(4) TC MATERIAL

NPO

X7R

(5) RATED VOLTAGE

- C = I KV
- D = 2 KV
- E = 3 KV

(6) PROCESS

N = NP0

B = Class 2 MLCC

(7) CAPACITANCE VALUE

2 significant digits+number of zeros

The 3rd digit signifies the multiplying factor, and letter R is decimal point

Example: $|2| = |2 \times |0| = |20 \text{ pF}$

^{0805 (2012) / 1206 (3216) / 1210 (3225) / 1808 (4520) / 1812 (4532)}

Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

PHYCOMP BRAND ordering codes

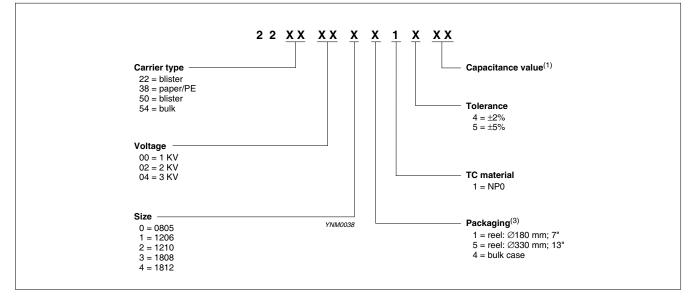
GLOBAL PART NUMBER (preferred), PHYCOMP CTC (for North America) and I2NC (traditional) codes are acceptable to order Phycomp brand products.

GLOBAL PART NUMBER (PREFERRED)

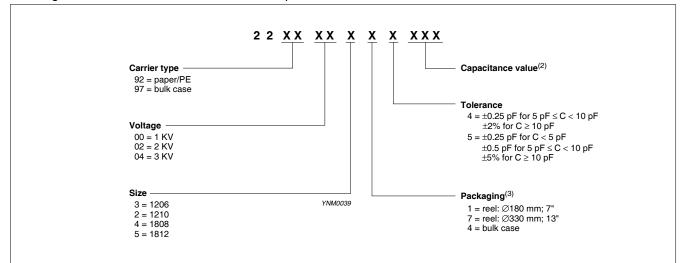
For detailed information of GLOBAL PART NUMBER and ordering example, please refer to page 2.

12NC CODE

Ordering information for NP0 1 KV to 3 KV, $C \ge 10 \text{ pF}$



Ordering information for NP0 1 KV to 3 KV, C < 10 pF



- (1) Please refer to "Last 2-digit of 12NC" in "CAPACITANCE RANGE & THICKNESS FOR NP0"
- (2) Please refer to "Last 3-digit of 12NC" in "CAPACITANCE RANGE & THICKNESS FOR NP0"
- (3) Quantity on reel depends on thickness classification; see table 5



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Ordering information for X7R | KV to 3 KV

Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

2 2 <u>X X X X X X</u> **X X XX** Carrier type Capacitance value⁽¹⁾ 50 = Blister 55 = Paper/PE54 = BulkTolerance $5 = \pm 5\%^{(2)}$ $6 = \pm 10\%$ Rated voltage - Termination 00 = 1 KV 02 = 2 KV 04 = 3 KV Temperature characteristic 5 = X7R Size -1 = 1206 2 = 1210 YNM0040 Packaging⁽³⁾ 1 = reel: \emptyset 180 mm; 7" 5 = reel: \emptyset 330 mm; 13" 4 = bulk case 3 = 1808 4 = 1812

(1) Please refer to "Last 2-digit of 12NC" in "CAPACITANCE RANGE & THICKNESS FOR X7R"

(2) Tolerance ±5% is not available for full product range, please contact local sales force before ordering

(3) Quantity on reel depends on thickness classification; see table 5

PHYCOMP CTC CODE (FOR NORTH AMERICA)

U Example: 12102R102KFBB00

1210	2R	102	К	F	В	В	0	0
Size code	Temp. Char.	Capacitance in pF	Tolerance	Voltage	Termination	Packing	Marking	Range identifier
0805 1206 1210 1808 1812	CG = NP0 2R = X7R	101 = 100 pF; the third digit signifies the multiplying factor: $8 = \times 0.01$ $9 = \times 0.1$ $0 = \times 1$ $1 = \times 10$ $2 = \times 100$ $3 = \times 1,000$	$C = \pm 0.25 \text{ pF}$ $D = \pm 0.5 \text{ pF}$ $G = \pm 2\%$ $J = \pm 5\%$ $K = \pm 10\%$	E = 1 KV F = 2 KV G = 3 KV		2 = 180 mm 7" Paper/PE 3 = 330 mm 13" Paper/PE B = 180 mm 7" Blister F = 330 mm 13" Blister P = Bulk case	0 = no marking	0 = conv. Ceramic D = Class 2 MLCC



CONSTRUCTION

The capacitor consists of a rectangular block of ceramic dielectric in which a number of interleaved metal electrodes are contained. This structure gives rise to a high capacitance per unit volume.

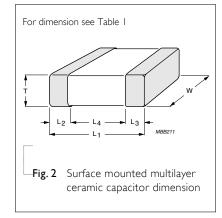
The inner electrodes are connected to the two end terminations and finally covered with a layer of plated tin (NiSn). The terminations are lead-free. A cross section of the structure is shown in Fig. I.



Table I For outlines see fig. 2

	E L _I (mm) W (mm) T (MM)		L ₂ / L ₃	(mm)	L ₄ (mm)	
TYPE	L _I (mm)	W (mm)	1 (1111)	min.	max.	min.
0805	2.0 ±0.20	1.25 ±0.20	_	0.25	0.75	0.55
1206	3.2 ±0.30	1.6 ±0.20	_	0.25	0.75	1.40
1210	3.2 ±0.30	2.5 ±0.20	Refer to table 2 to 4	0.25	0.75	1.40
1808	4.5 ±0.40	2.0 ±0.30		0.25	0.75	2.20
1812	4.5 ±0.40	3.2 ±0.20		0.25	0.75	2.20

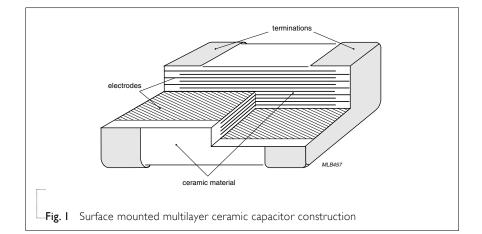
OUTLINES





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Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

Table 2	Sizes from 1206 to	o 1808						
CAP.	Last 3-digit of	1206		1210		1808		
	I2NC	I KV	2 KV	I KV	2 KV	I KV	2 KV	3 KV
0.47 pF	477							
0.56 pF	567							
0.68 pF	687							
0.82 pF	827							
1.0 pF	108							
I.2 pF	128							
I.5 pF	158							
I.8 pF	188	0.05 + 0 +		1.25.10.2	1.25 1.0.2		1.25 1.0.2	
2.2 pF	228	0.85±0.1	0.85±0.1	1.25±0.2	1.25±0.2	1.25±0.2	1.25±0.2	1.6±0.2
2.7 pF	278							
3.3 pF	338							
3.9 pF	398							
4.7 pF	478							
5.6 pF	568							
6.8 pF	688							
8.2 pF	828							

CAPACITANCE RANGE & THICKNESS FOR NPO

ΝΟΤΕ

I. Values in shaded cells indicate thickness class in mm

2. Capacitance value of non E-12 series is on request

Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

Table	a 3 Sizes from	0805 to I	812									
CAP.	Last 2-digit of		1206		1210		1808			1812		
	I2NC	I KV	I KV	2 KV	I KV	2 KV	I KV	2 KV	3 KV	I KV	2 KV	3 KV
10 pF												
I2 pF												
I5 pF												
18 pF												
22 pF		1.0±0.1										
27 pF												
33 pF												
39 pF									1.6±0.2			
47 pF				1.25±0.2								1.25±0.2
56 pF												
68 pF						1.25±0.2		1.25±0.2				
82 pF												
100 pF			1.25±0.2		1.25±0.2						1.25±0.2	
120 pF							1.25±0.2					
150 pF												
180 pF									2.0±0.2	1.25±0.2		
220 pF												
270 pF												
330 pF												1.6±0.2
390 pF												
470 pF												
560 pF												
680 pF												2.0±0.2
820 pF												
I.0 nF												
I.2 nF												
I.5 nF												
I.8 nF												
2.2 nF												
2.7 nF										1.6±0.2		
3.3 nF	56											

CAPACITANCE RANGE & THICKNESS FOR NPO

ΝΟΤΕ

1. Values in shaded cells indicate thickness class in mm

2. Capacitance value of non E-12 series is on request

Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

Table	4 Sizes from C)805 to	1812									
CAP.	Last 2-digit of		1206		1210		1808			1812		
	I2NC	I KV	I KV	2 KV	I KV	2 KV	I KV	2 KV	3 KV	I KV	2 KV	3 KV
100 pF	09											
150 pF	12											
220 pF	14											
330 pF	16								1.6±0.2			
470 pF	18											
680 pF	21			1.25±0.2				1.35±0.15				
I.0 nF	23		1.25±0.2						20102			1.6±0.2
I.5 nF	25					1.25±0.2	1.35±0.15		2.0±0.2		1.35±0.15	20102
2.2 nF	27					1.6±0.2		1.6±0.2				2.0±0.2
3.3 nF	29											
4.7 nF	32				1.25±0.2					1.35±0.15		
6.8 nF	34										1.6±0.2	
10 nF	36						1.6±0.2				2.0±0.2	
15 nF	38											
22 nF	41				1.6±0.2							
33 nF	43				2.0±0.2					1.6±0.2		
47 nF	45											
68 nF	47											
100 nF	49											

CAPACITANCE RANGE & THICKNESS FOR X7R

ΝΟΤΕ

I. Values in shaded cells indicate thickness class in mm

2. Capacitance value of non E-6 series is on request

3. For products with 5% tolerance, please contact local sales force before ordering



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Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

THICKNESS CLASSES AND PACKING QUANTITY

Table 5			GLOO MA		@220.1414		
size code	THICKNESS CLASSIFICATION	TAPE WIDTH – QUANTITY PER REEL	Ø180 MM Paper	/ 7 INCH Blister	Ø330 MM Paper	/ 13 INCH Blister	QUANTITY PER BULK CASE
0201	0.3 ±0.03 mm	8 mm	15,000		50,000		
0402	0.5 ±0.05 mm	8 mm	10,000		50,000		50,000
0603	0.8 ±0.1 mm	8 mm	4,000		15,000		15,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		10,000
0805	0.8 / 0.85 ±0.1 mm	8 mm	4,000		15,000		8,000
0805	1.00 ±0.1 mm	8 mm		3,000		10,000	
	1.25 ±0.2 mm	8 mm		3,000		10,000	5,000
	0.6 ±0.1 mm	8 mm	4,000		20,000		
	0.8 / 0.85 ±0.1 mm	8 mm	4,000		15,000		
1206	1.00 / 1.15 ±0.1 mm	8 mm		3,000		10,000	
1206	1.25 ±0.2 mm	8 mm		3,000		10,000	
	1.6 ±0.15 mm	8 mm		2,500		10,000	
	1.6 ±0.2 mm	8 mm		2,000		10,000	
	0.6 / 0.7 ±0.1 mm	8 mm		4,000		15,000	
_	0.85 ±0.1 mm	8 mm		4,000		10,000	
	1.15 ±0.1 mm	8 mm		3,000		10,000	
1210	1.15 ±0.15 mm	8 mm		3,000		10,000	
	1.25 ±0.2 mm	8 mm		3,000			
	1.5 ±0.1 mm	8 mm		2,000			
	1.6 / 1.9 ±0.2 mm	8 mm		2,000			
	2.0 ±0.2 mm	8 mm		2,000 1,000			
	2.5 ±0.2 mm	8 mm		1,000 500			
	1.15 ±0.15 mm	l2 mm		3,000			
	1.25 ±0.2 mm	I2 mm		3,000			
1808	1.35 ±0.15 mm	I2 mm		2,000			
1000	1.5 ±0.1 mm	l2 mm		2,000			
	1.6 ±0.2 mm	l2 mm		2,000			
	2.0 ±0.2 mm	l2 mm		2,000			
	0.6 / 0.85 ±0.1 mm	l2 mm		2,000			
	1.15 ±0.1 mm	l2 mm		1,000			
	1.15 ±0.15 mm	l2 mm		1,000			
	1.25 ±0.2 mm	l2 mm		1,000			
1812	1.35 ±0.15 mm	l2 mm		1,000			
	1.5 ±0.1 mm	l2 mm		1,000			
	1.6 ±0.2 mm	l2 mm		1,000			
	2.0 ±0.2 mm	l2 mm		1,000			
	2.5 ±0.2 mm	l2 mm		500			

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Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

ELECTRICAL CHARACTERISTICS

NP0/X7R DIELECTRIC CAPACITORS; NISN TERMINATIONS

Unless otherwise stated all electrical values apply at an ambient temperature of 20 ± 1 °C, an atmospheric pressure of 86 to 106 kPa, and a relative humidity of 63 to 67%.

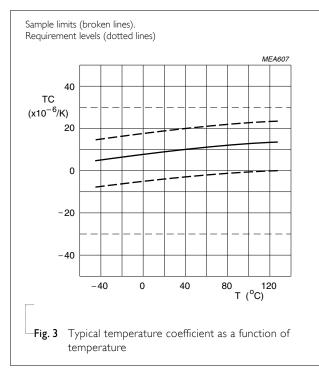
Table	e 6					
DESCRIF	PTION	VAL				
Capacita	nce range	0.47 pF to 33 nF				
Capacita	nce tolerance					
NP0	C < 10 _P F	±0.25 pF, ±0.5 pF				
	C ≥ 10 pF	±2%, ±5%				
X7R		±5% ⁽¹⁾ , ±10%				
Dissipatio	on factor (D.F.)					
NP0	C < 30 _P F	≤ I / (400 + 20C)				
	C ≥ 30 _P F	≤ 0.1 %				
X7R		≤ 2.5 %				
Insulation	n resistance after 1 minute at U _r (DC)	$R_{ins} \geq$ 10 GQ or $R_{ins} \times C \geq$ 500 seconds whichever is less				
	n capacitance change as a function of temperature ature characteristic/coefficient):					
NP0		±30 ppm/°C				
X7R		±15%				
Operatin	g temperature range:					
NP0/X7	7R	–55 °C to +125 °C				

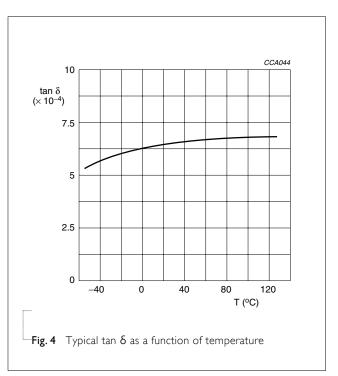
NOTE

1. ±5% tolerance of capacitance value isn't available for X7R full product range, please contact local sales force before ordering

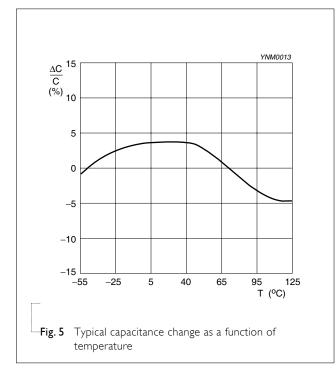


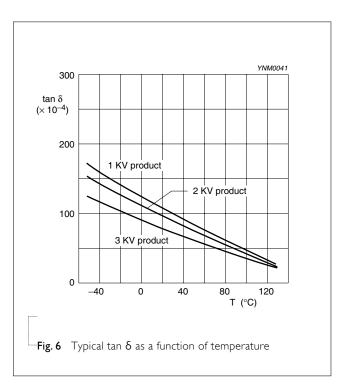
HIGH-VOLTAGE NP0





HIGH-VOLTAGE X7R







Product specification $\frac{11}{16}$

Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

SOLDERING RECOMMENDATION

Table 7					
SOLDERING METHOD	SIZE 0402	0603	0805	1206	≥ 1210
Reflow	≥ 0.1 µF	≥ 1.0 µF	≥ 2.2 µF	≥ 4.7 µF	Reflow only
Reflow/Wave	< 0.1 µF	< 1.0 µF	< 2.2 µF	< 4.7 µF	

TESTS AND REQUIREMENTS

Table 8 Test procedures and requirements

TEST	TEST MET	HOD	PROCEDURE	REQUIREMENTS
Mounting	IEC 60384- 21/22	4.3	The capacitors may be mounted on printed-circuit boards or ceramic substrates	No visible damage
Visual Inspection and Dimension Check		4.4	Any applicable method using × 10 magnification	In accordance with specification
Capacitance		4.5.1	Class I: $f = 1$ MHz for C ≤ 1 nF, measuring at voltage 1 V _{rms} at 20 °C f = 1 KHz for C > 1 nF, measuring at voltage 1 V _{rms} at 20 °C Class 2: $f = 1$ KHz for C ≤ 10 µF, measuring at voltage 1 V _{rms} at 20 °C	Within specified tolerance
Dissipation Factor (D.F.)		4.5.2	Class I: $f = MHz \text{ for } C \le nF$, measuring at voltage V_{rms} at 20 °C $f = KHz \text{ for } C > nF$, measuring at voltage V_{rms} at 20 °C Class 2: $f = KHz \text{ for } C \le 0 \ \mu F$, measuring at voltage V_{rms} at 20 °C	In accordance with specification
Insulation Resistance		4.5.3	$U_r \le 500$ V: At Ur for 1 minute $U_r > 500$ V: At 500 V for 1 minute	In accordance with specification
Temperature Coefficient		4.6	Class 1: Between minimum and maximum temperature NP0: -55 °C to +125 °C Normal Temperature: 20 °C	ΔC/C: Class I: NP0: ±30 ppm/°C
Temperature Characteristic			Class 2: Between minimum and maximum temperature X7R: -55 °C to +125 °C Normal Temperature: 20 °C	Class 2 X7R: ±15%

TEST	TEST METI	HOD	PROCEDURE	REQUIREMENTS		
Adhesion	IEC 60384- 4.7 21/22		A force applied for 10 seconds to the line joining the terminations and in a plane parallel to the substrate	Force size ≥ 0603: 5N		
Bond Strength of		4.8	Mounting in accordance with IEC 60384-22 paragraph 4.3	No visible damage		
Plating on End Face			Conditions: bending I mm at a rate of I mm/s, radius jig 340 mm	Δ C/C Class 1: NP0: within ±1% or 0.5 pF, whichever is greater Class2: X7R: ±10%		
Resistance to Soldering Heat		4.9	Precondition: $150 \pm 0/-10$ °C for 1 hour, then keep for 24 ±1 hours at room temperature Preheating: for size ≤ 1206: 120 °C to 150 °C for	Dissolution of the end face plating shall not exceed 25% of the length of the edge concerned		
			I minute Preheating: for size >1206: 100 °C to 120 °C for I minute and 170 °C to 200 °C for I minute Solder bath temperature: 260 ±5 °C Dipping time: 10 ±0.5 seconds Recovery time: 24 ±2 hours	Δ C/C Class 1: NP0: within ±0.5% or 0.5 pF, whichever is greater Class2: X7R: ±10%		
				D.F. within initial specified value R _{ins} within initial specified value		
Solderability		4.10	Preheated to a temperature of 80 °C to 140 °C and maintained for 30 seconds to 60 seconds.	The solder should cover over 95% of the critical area of each termination		
			Test conditions for lead containing solder alloy Temperature: 235 ±5 °C Dipping time: 2 ±0.2 seconds Depth of immersion: 10 mm Alloy Composition: 60/40 Sn/Pb Number of immersions: 1			
			Test conditions for lead-free containing solder alloy Temperature: 245 ±5 °C Dipping time: 3 ±0.3 seconds Depth of immersion: 10 mm Alloy Composition: SAC305 Number of immersions: 1			

Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

TEST Rapid Change of	TEST METHOD		PROCEDURE	REQUIREMENTS
	IEC 60384- 21/22	4.	Preconditioning; 150 +0/–10 °C for 1 hour, then keep for	No visual damage
Temperature	,		24 ± 1 hours at room temperature	ΔC/C
				Class 1:
			5 cycles with following detail:	NP0: within \pm 1% or 1 pF, whichever is greater
			30 minutes at lower category temperature	Class2:
			30 minutes at upper category temperature	X7R: ±15%
			Recovery time 24 \pm 2 hours	
				D.F. meet initial specified value
				R _{ins} meet initial specified value
Damp Heat		4.13	1. Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for	No visual damage after recovery
			24 ± 1 hour at room temp	ΔC/C
			2. Initial measure:	Class I:
			Spec: refer to initial spec C, D, IR	NP0: within $\pm 2\%$ or 1 pF, whichever is greater
	3. Damp heat test:	3. Damp heat test:	Class2:	
			 500 ±12 hours at 40 ±2 °C; 90 to 95% R.H. 4. Recovery: Class 1: 6 to 24 hours Class 2: 24 ±2 hours 5. Final measure: C, D, IR 	X7R: ±15%
				D.F.
				Class I:
				NP0: $\leq 2 \times \text{specified value}$
				Class2:
				$X7R: \geq 25 \ \forall : \leq 5\%$
			P.S. If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the	R_{ins} Class 1: NP0: ≥ 2,500 MΩ or $R_{ins} × C_r ≥ 25s$ whichever is less
			capacitor shall be preconditioned according	Class2:
			to "IEC 60384 4.1" and then the requirement shall be met.	X7R: $\geq 500~M\Omega$ or $R_{ins} \times C_r \geq 25 s$ whichever is less

Surface-Mount Ceramic Multilayer Capacitors High-Voltage NP0/X7R 1 KV to 3 KV

TEST	TEST METHOD		PROCEDURE	REQUIREMENTS	
Endurance	IEC 60384- 21/22	4.14	 I. Preconditioning, class 2 only: 150 +0/-10 °C /1 hour, then keep for 24 ±1 hour at room temp 	No visual damage ΔC/C	
			 Initial measure: Spec: refer to initial spec C, D, IR Endurance test: Temperature: NP0/X7R: 125 °C Specified stress voltage applied for 1,000 hours. High-Voltage series follows the stress conditions below: Applied 2.0 × U_r for < 500 V series Applied 1.3 × U_r for 500 V, 630 V series Applied 1.2 × U_r for 1 KV, 2 KV, 3 KV series Recovery time: 24 ±2 hours Final measure: C, D, IR 	Class I: NP0: within $\pm 2\%$ or 1 pF, whichever is greater Class 2: X7R: $\pm 15\%$ D.F. Class I: NP0: $\leq 2 \times$ specified value Class 2: X7R: $\geq 25 \lor : \leq 5\%$ R _{ins} Class I: NP0: $\geq 4.000 \text{ M}\Omega$ or	
			P.S. If the capacitance value is less than the minimum value permitted, then after the other measurements have been made the capacitor shall be preconditioned according to <i>"IEC 60384 4.1"</i> and then the requirement shall be met.	$\begin{aligned} R_{ins} &\propto C_r \geq 40 \text{s whichever is less} \\ Class2: \\ &\times 7R: \geq 1,000 \text{ M}\Omega \text{ or} \\ R_{ins} &\propto C_r \geq 50 \text{s whichever is less} \end{aligned}$	
Voltage Proof	IEC 60384-1	4.6	Specified stress voltage applied for 1 minute $U_r \le 100 \text{ V}$: series applied 2.5 U_r $100 \text{ V} < U_r \le 200 \text{ V}$ series applied (1.5 $U_r + 100$) $200 \text{ V} < U_r \le 500 \text{ V}$ series applied (1.3 $U_r + 100$) $U_r > 500 \text{ V}$: 1.3 U_r I: 7.5 mA	No breakdown or flashover	

Product specification 15 3 KV

REVISION HISTORY

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 4	Aug 08, 2011	-	- Product range updated
Version 3	Jan 19, 2011	-	- Dimension updated
			- Add NP0 0805 1KV
Version 2	Feb 02, 2010	-	- Change to dual brand datasheet that describe High-Voltage NP0/X7R series with RoHS compliant
			- Replace the high voltage part of pdf files: UP-NP0X7R_HV_1K-to-4KV_1 and UY-NP0X7R_HV_1K-to-4KV_1
			- Description of "Halogen Free compliant" added
			- Product range updated
			- Define global part number
			- Test method and procedure updated
Version I	Sep 30, 2005	-	- Thickness revised
Version 0	Sep 12, 2005	-	- New

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

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