Spec. No.: RAC-K-HTS-0001 /14

Date: 2017. 1. 10

Specification

Title: FIXED CHIP RESISTOR NETWORKS; RECTANGULAR TYPE

Style: RAC06 2D, RAC06 4D, RAC10 2D,

RAC10 4D, RAC16 4D, RAC16 8D

RoHS COMPLIANCE ITEM Halogen and Antimony Free

Product specification contained in this specification are subject to change at any time without notice If you have any questions or a Purchasing Specification for any quality Agreement is necessary, please contact our sales staff.



Hokkaido Research Center Approval by: T. Sannomiya Drawing by: M. Shibuya

Note: Stock conditions

Temperature: $+5^{\circ}C \sim +35^{\circ}C$ Relative humidity: $25\% \sim 75\%$

The period of guarantee: Within 2 year from shipmen t by the company.

Solderability shall be satisfied.

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1. Scope

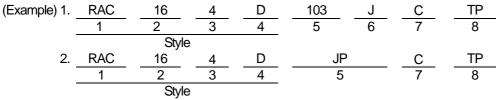
1.1 This specification covers the detail requirements for fixed chip resistors networks; rectangular type, style of RAC06 2D, 06 4D, 10 2D, 10 4D, 16 4D, 16 8D.

1.2 Applicable documents

JIS C 5201–1: 2011, JIS C 5201–9: 2006, JIS C 5201–9–1: 2006 IEC60115–1: 2008, IEC60115–9: 2003, IEC60115–9–1: 2004 EIAJ RC–2129–2000.

2. Classification

Type designation shall be the following form.



Style

1 Fixed chip resistors networks; rectangular type

- 2 Size
- 3 Number of element
- 4 Circuits
- 5 Rated resistance

103	E24 Series, 3 digit,	Ex. 103> 10 kΩ,
1000	E96 Series, 4 digit,	Ex. 1000>100Ω
	_	1022> 10.2kΩ
JP	Chip jumper	

6 Tolerance on rated resistance

F	±1%
J	±5%

7 Terminal style

С		With Corner
D	Convex Type	Flat Type
E		Flat Type Low profile

8 Packaging form

В	Bulk (loose package)
TH	Donor toning
TP	Paper taping

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3. Rating

3.1 The ratings shall be in accordance with Table-1.

Table-1

Style	Terminations style	Rated element dissipation (W)	Rated network dissipation (W)	Temperature coefficient of resistance (10 ⁻⁶ / °C)	Rated resistance range(Ω)	Preferred number series for resistors	Tolerance on rated resistance
				±200	100~100k		F(±1%)
RAC06 2D	D,E		0.063	±350	10~27	E24	J(±5%)
		0.031		±200	30~1M		3(±376)
	D,E	0.001	0.125	±200	100~100k		F(±1%)
RAC06 4D				±350	10~27	E24	J(±5%)
				±200	30~1M		
RAC102D			0.125	±400	3~9.1		
KAC102D	С	0.063	±300	10~1M	E24	J(±5%)	
RAC104D			0.25	±200	10~1101		
				±100	10~1M		F(±1%)
RAC16 4D	С	0.1	0.25	±200	10~1101	E24	
KAC104D	C	0.1	0.25	+300~+500	1~9.1	C24	J(±5%)
				+300~+300	1.1M~10M		
RAC16 8D	С	0.063	0.25	±200	10~1M	E24	J(±5%)

Style	Limiting element voltage(V)	Max over load voltage(V)	Number of element	Circuit networks	Category temperature range(°C)		
RAC06 2D	12.5	12.5 25					
RAC06 4D		25	4				
RAC10 2D	25	50	2	D	FF .40F		
RAC10 4D	50	100	4	(Independence type)	_55~ + 125		
RAC16 4D	50	100	4				
RAC16 8D	25	100	8				

Note. Rated current of chip jumper: 1(A)

Note. Resistance value of chip jumper: $50m\Omega$ max.

3.2 Derating

The derated values of dissipation (or current rating in case of chip jumper) at temperature in excess of 70 °C shall be as indicated by the following curve.

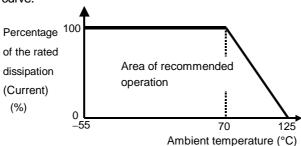


Figure-1Derating curve



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3.3 Rated voltage

d. c. or a. c. r. m. s. voltage calculated from the square root of the product of the rated resistance and the rated dissipation.

Limiting element voltage can only be applied to resistors when the resistance value is equal to or higher than the critical resistance value.

At high value of resistance, the rated voltage may not be applicable.

[So called RCWV (Rated continuous working Voltag) is determined by]

4. Packaging form

The standard packaging form shall be in accordance with Table-2.

Table-2

Symbol	F	Packaging form	Standard packaging quantity / units	Application	
В	Bulk (loose pac	kage)	1,000 pcs.	RAC06, 10,16	
TH	Paper taping	8mm width, 2mm pitches	10,000 pcs.	RAC06 2D, 4D, RAC10 2D, 4D	
TP	Paper taping	8mm width, 4mm pitches	5,000 pcs.	RAC16 4D, 8D	

Title: FIXED CHIP RESISTOR NETWORKS; RECTANGULAR TYPE

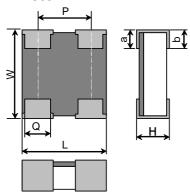
RAC06 2D, RAC06 4D, RAC10 2D, RAC10 4D, RAC16 4D, RAC16 8D Page: 4/12

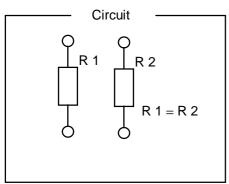
5. Dimensions

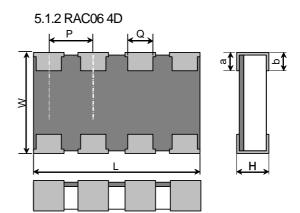
The resistor shall be of the design and physical dimensions in accordance with below.

5.1 Terminations style:E.









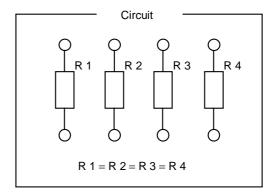


Figure-2

Figure-3

Table–3 Unit: mm

Style	Terminations style	L	W	Н	Q	а	b	*P
RAC06 2D	Е	0.8±0.05	0.6±0.05	0.23±0.10	0.2±0.1	0.2±0.1	0.2±0.1	0.5
RAC06 4D	Е	1.4±0.05	0.6±0.05	0.23±0.10	0.2±0.1	0.2±0.1	0.2±0.1	0.4

*Reference

5.1.3 Net weight (Reference)

	,	
Style	Terminations style	Net weight(mg)
RAC06 2D	E	0.38
RAC06 4D	E	0.65

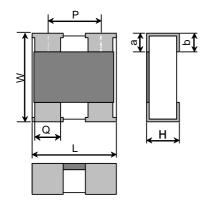
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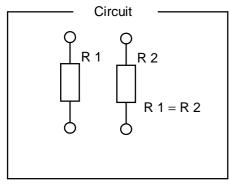
Title: FIXED CHIP RESISTOR NETWORKS; RECTANGULAR TYPE

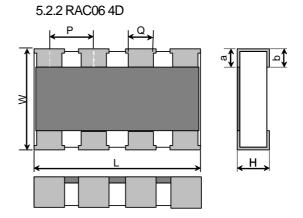
RAC06 2D, RAC06 4D, RAC10 2D, RAC10 4D, RAC16 4D, RAC16 8D Page: 5/12

5.2 Terminations style:D.

5.2.1 RAC06 2D







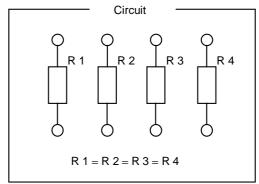


Figure-4 Figure-5

Table-4 Unit: mm

Style	Terminations style	L	W	Н	Q	а	b	*P
RAC06 2D	D	0.8±0.1	0.6±0.1	0.35±0.10	0.3±0.1	0.15±0.10	0.15±0.10	0.5
RAC06 4D	D	1.4±0.1	0.6±0.1	0.35±0.10	0.25±0.10	0.15±0.10	0.20±0.10	0.4

*Reference

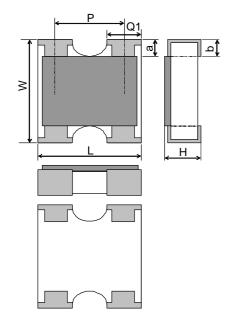
5.2.3 Net weight (Reference)

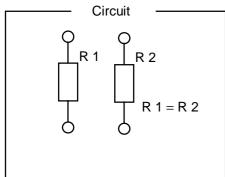
Style	Terminations style	Net weight(mg)
RAC06 2D	D	0.56
RAC06 4D	D	0.98

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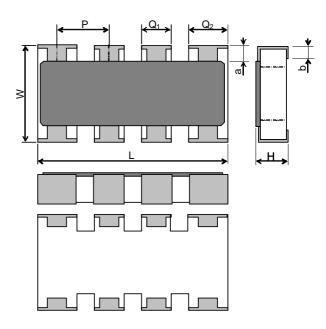
RAC06 2D, RAC06 4D, RAC10 2D, RAC10 4D, RAC16 4D, RAC16 8D Page: 6/12

5.3 RAC10 2D





5.4 RAC10 4D



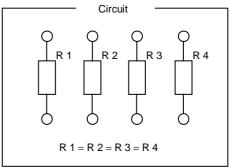


Figure-6

Figure-7 Unit: mm Table-5

				I CLOID		O
Style	Terminations style	L	W	Н	Q or Q ₁	$*Q_2$
RAC10 2D	С	1.0±0.1	1.0±0.1	0.35±0.10	0.34±0.10	
RAC10 4D	С	2.0±0.1	1.0±0.1	0.45±0.10	0.3±0.05	0.4±0.1

Style	а	b	*P
RAC102D	0.2±0.15	0.25±0.17	0.65
RAC104D	0.2±0.1	0.25±0.10	0.5

*Reference

5.5 Net weight (Reference)

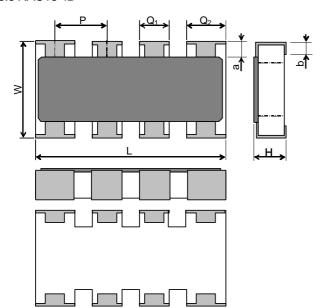
Style	Terminations style	Net weight(mg)
RAC102D	С	1.1
RAC104D	С	2.1

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5.6 RAC16 4D



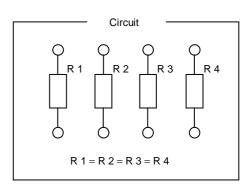


Figure-8

			l	able-6		Unit: mm
Style	Terminations style	L	W	Н	$Q_{\scriptscriptstyle 1}$	а
RAC164D	C	3.2±0.1	1.6±0.1	0.5±0.1	0.4±0.1	0.3±0.1

Style	b	*Q ₂	*P	*Reference
RAC164D	0.3±0.2	0.6±0.1	0.8	

5.7 Net weight (Reference)

Style	Terminations style	Net weight(mg)
RAC164D	С	7

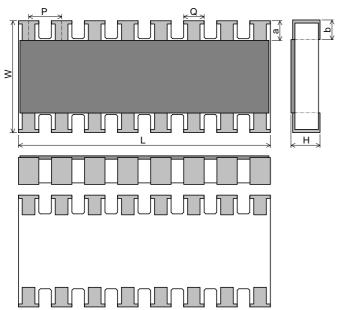
Drawing No: RAC-K-HTS-0001

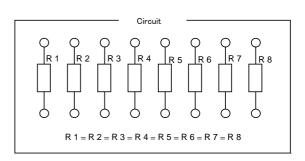
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5.8 RAC16 8D





*Reference

Figure-9

Table-7 Unit: mm Style Terminations style L W Η Q а RAC168D С 3.8 ± 0.1 1.6±0.1 0.45±0.10 0.3 ± 0.1 0.3 ± 0.1

Style	b	*P
RAC168D	0.3±0.1	0.5

5.9 Net weight (Reference)

Style	Terminations style	Net weight(mg)
RAC168D	С	8.3

6. Marking

The Rated resistance of RAC06 2D, 4D, RAC10 2D should not be marked.

6.1 For the resistors

The rated resistance shall be marked in 3 digits (E24) and marked on over coat side.

Marking example	Contents
123	12×10 ³ [Ω] → 12 [kΩ]

6.2 For the Jumper Chip

Marking example	Contents
000	JP



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7. Performance

7.1 The standard condition for tests shall be in accordance with Sub-clause 4. 2, JIS C 5201–1: 2011.

7.2 The performance shall be satisfied in Table-8.

Table-8(1)

No.	Test items	Con	dition of test (JIS C 52	201_1)	Performance requirements
1	Resistance	Sub-clause	•	1)	As in 4.5.2
'	r Colotal loc	Gub Gause 4.5		The resistance value shall correspond	
				with the rated resistance taking into	
				account the specified tolerance.	
	-				Chip jumper: 50mΩ max.
2	Temperature characteristic of resistance	4.8			See Table–1.
	oi resistance		sistance change pe	r change in	
		degree cer	-		
		TCR(10 ⁻⁶ /Ω	$x = \frac{R2-R1}{R1(t2-t1)} \times 10^6$		
		t1:20°C ‡	⁵ °C, t2: 125°C ⁺⁵ °(
		R1 : Resista	ance at t1 temperature	Э	
			ance at t2 temperature	Э	
3	Short time overload	4.13	0 E tim DOM/		Resistor:
		Test voltage	e: 2.5 times RCWV		Δ R/R: Within ±(2%+0.1 Ω)
			ntial should not e	xceed max	Chip jumper: $50 \text{m}\Omega$ max. No visible damage
			ltage as shown in Tab		140 Visible dairiage
4	Resistance to soldering heat	4.18			Resistor: Δ R/R: Within \pm (1%+0.05 Ω)
		Test by a pi		Chip jumper: $50 \text{m}\Omega$ max.	
		Temp. of solder bath: 260±5°C			No evidence of appearance damage.
		Immersion time: 10±1s After immersion into solder leaving the room			
			h or more, and then		
		resistance.	ir or more, and then	measure the	
5	Solderability	4.17			95% coverage min., good tinning and
	-	Test by a pi		no visible damage.	
		Flax: Rosin-Methanol			
		Temp. of solder bath: 235±5°C Immersion time: 2±0.5s			
6	Temperature cycling	4.19	uitie. 2±0.38		Resistor: Δ R/R: Within \pm (1%+0.05 Ω)
	Temperature cycling	Test cycle: 5 cycles for duty cycle as specified		Chip jumper: $50m\Omega$ max.	
		below.			No visible damage
			_		
		Step	Temperature(°C)	Time(min.)	
		1	-55±3	30	
		2	20 +5	2~3	
		3	+125±2	30	
		4	20 +5	2~3	



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Table-	-8(2)
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		Table $O(2)$	
No.	Test items	Condition of test (JIS C 5201–1)	Performance requirements
7	Load life	4.25 Test temp.: 70±2°C Test voltage: Cycle of 1h 30min. "ON" and 30min. "OFF" at dc rated voltage.	Resistor: $\Delta R/R$: Within $\pm (3\%+0.1\Omega)$ Chip jumper: $50m\Omega$ max. No visible damage
8	Load life in humidity	Test period: 1,000 +48 h 4.24 Test condition: 40±2°C & 90~95% R.H. Test voltage: Cycle of 1h 30min. "ON" and 30min. "OFF" at dc rated voltage.	Resistor: $\Delta R/R$: Within $\pm (3\%+0.1\Omega)$ Chip jumper: $50m\Omega$ max. No visible damage
9	Adhesion	Test period: 1,000 +48 h 4.32 Pressurizing force: 5N (RAC06: 3N) Test time: 10±1sec.	No remarkable damage or removal of the terminations
10	Insulation resistance	4.6 Test voltage: Max. overload voltage (DC) Test period: 1min.	R≥1GΩ
11	Dielectric withstanding voltage	4.7 Test voltage: Max. overload voltage (AC) Test period: 1min.	No flashover, fire and breakdown.

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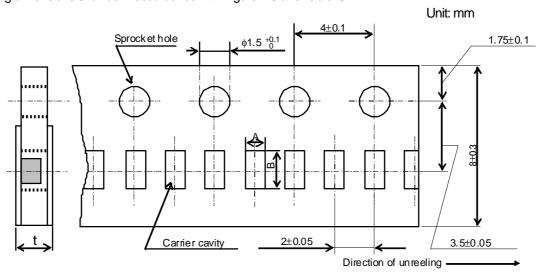
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8. Taping

- 8.1 Applicable documents JIS C 0806-3: 2014, EIAJ ET-7200C: 2010
- 8.2 Taping dimensions
- 8.2.1 RAC06 2D,RAC06 4D,RAC10 2D, RAC10 4D (Paper taping, 8mm width, 2mm pitches)

Taping dimensions shall be in accordance with Figure-10 and Table-9.



Figure–10 Table–9

1able-9			Onit. mm	
Style	Α	В	t	
RAC06 2D	0.7±0.1	0.9±0.1	0.5±0.1	
RAC06 4D	0.7±0.1	1.5±0.1	0.5±0.1	
RAC102D	1.15±0.1	1.15±0.1	0.5max.	
RAC104D	1.2±0.2	2.2±0.2	0.6max.	

8.2.2 RAC16 4D, RAC16 8D (Paper taping, 8mm width, 4mm pitches)

Taping dimensions shall be in accordance with Figure-11 and Table-10.

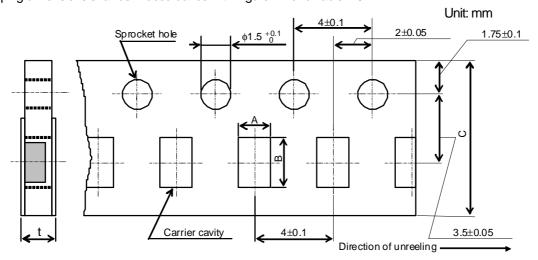


Figure-11

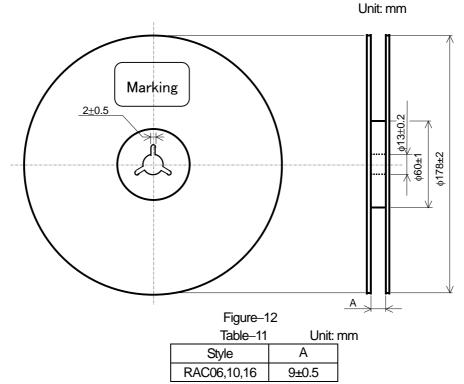
	Unit: mm			
Style	Α	В	С	t
RAC16 4D	2.0±0.2	3.6±0.2	8.0±0.3	1.0max.
RAC168D	1.9±0.15	4.1±0.15		0.85max.

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8.3 Reel dimension

Reel dimensions shall be in accordance with the following Figure–12 and Table–11.



Note: Marking label shall be marked on a place of Marking A or two place of marking A and B.

9. Marking on package

The label of a minimum package shall be legibly marked with follows.

9.1 Marking A

- (1) Classification (Style, Rated resistance, Tolerance on rated resistance, Terminal style, Packaging form)
- (2) Quantity (3) Lot number (4) Manufacturer's name or trade mark (5) Others
- 9.2 Marking B (KAMAYA Control label)

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M8340108K6202GGD03 M8340109K2002FCD03 M8340109M4701GCD03 EXB-24N121JX EXB-24N470JX EXB-A10E102J EXB-A10E104J 744C083101JTR MDP1603100KGE04 PRA100I2-1KBWNW GUS-SS4-BLF-01-1002-G ACAS06S0830339P100

ACAS06S0830343P100 ACAS06S0830344P100 RM2012A-102/104-PBVW10 RM2012A-102503-PBVW10 RM3216B-102302-PBVW10

L091S102LF ACAS06S0830341P100 ACAS06S0830342P100 ACAS06S0830345P100 EXB-14V300JX EXB-U18330JX EXB-V8V220GV

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