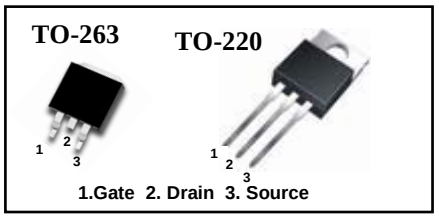
100V N-Channel MOSFET

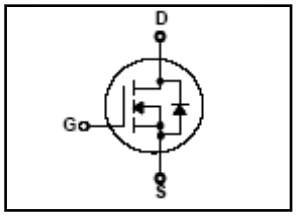
FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Absolute Maximum Ratings T_C = 25ºC, unless otherwise noted

Parameter	Symbol	Value	Unit
		TO-220,TO-263	
Drain-Source Voltage (V_{GS} = 0V)	V_{DSS}	100	V
Continuous Drain Current	I_D	70	A
Pulsed Drain Current (note1)	I_{DM}	Figure 6	A
Gate-Source Voltage	V_{GSS}	±20	V
Single Pulse Avalanche Energy (note2)	E_{AS}	1943	mJ
Avalanche Current (note1)	I_{AR}	32	A
Repetitive Avalanche Energy (note1)	E_{AR}	36	mJ
Power Dissipation (T_C = 25ºC)	P_D	200	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to175	ºC

Thermal Resistance

Parameter	Symbol	Value	Unit
		TO-220,TO-263	
Thermal Resistance, Junction-to-Case	R_{thJC}	0.75	℃/W
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62	

Specifications T_J = 25ºC, unless otherwise noted						
Parameter	**Symbol**	**Test Conditions**	**Value**			**Unit**
			Min.	**Typ.**	**Max.**	
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	100	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 100V, V_{GS} = 0V, T_J = 25ºC	--	--	1	μA
		V_{DS} = 80V, V_{GS} = 0V, T_J = 125ºC	--	--	100	
Gate-Source Leakage	I_{GSS}	V_{GS} = +20V,V_{DS}=0V	--	--	100	nA
		V_{GS}=-20V, V_{DS}=0V	--	--	-100	
Gate-Source Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250μA	2.0	--	4.0	V
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	V_{GS} = 10V, I_D = 35A	--	15	20	mΩ
Forward Transconductance	gfs	V_{DS} = 10V, I_D = 35A		85		S
Dynamic						
Input Capacitance	C_{iss}	V_{GS} = 0V, V_{DS} = 25V, f = 1.0MHz	--	2700	--	pF
Output Capacitance	C_{oss}		--	610	--	
Reverse Transfer Capacitance	C_{rss}		--	260	--	
Total Gate Charge	Q_g	V_{DD} =50V, I_D = 35A, V_{GS} = 0 to 10V	--	105	--	nC
Gate-Source Charge	Q_{gs}		--	15	--	
Gate-Drain Charge	Q_{gd}		--	45	--	
Turn-on Delay Time	$t_{d(on)}$	VDD = 50V, ID =35A, VGS= 10V R_G = 2.5 Ω	--	20	--	ns
Turn-on Rise Time	t_r		--	28	--	
Turn-off Delay Time	$t_{d(off)}$		--	65	--	
Turn-off Fall Time	t_f		--	15	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	T_C = 25 ºC	--	--	57	A
Pulsed Diode Forward Current	I_{SM}		--	--	230	
Body Diode Voltage	V_{SD}	T_J = 25ºC, I_{SD} = 35A, V_{GS} = 0V	--	--	1.5	V
Reverse Recovery Time	t_{rr}	V_{GS} = 0V,I_S = 28A, di_F/dt =100A /μs	--	195	--	ns
Reverse Recovery Charge	Q_{rr}		--	107	--	μC

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. I_{AS} = 30A, V_{DD} = 50V, R_G = 25 Ω, Starting T_J = 25 ºC
3. Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%

Typical Characteristics T_J = 25ºC, unless otherwise noted

Figure 1. Maximum Effective Thermal Impedance, Junction-to-Case

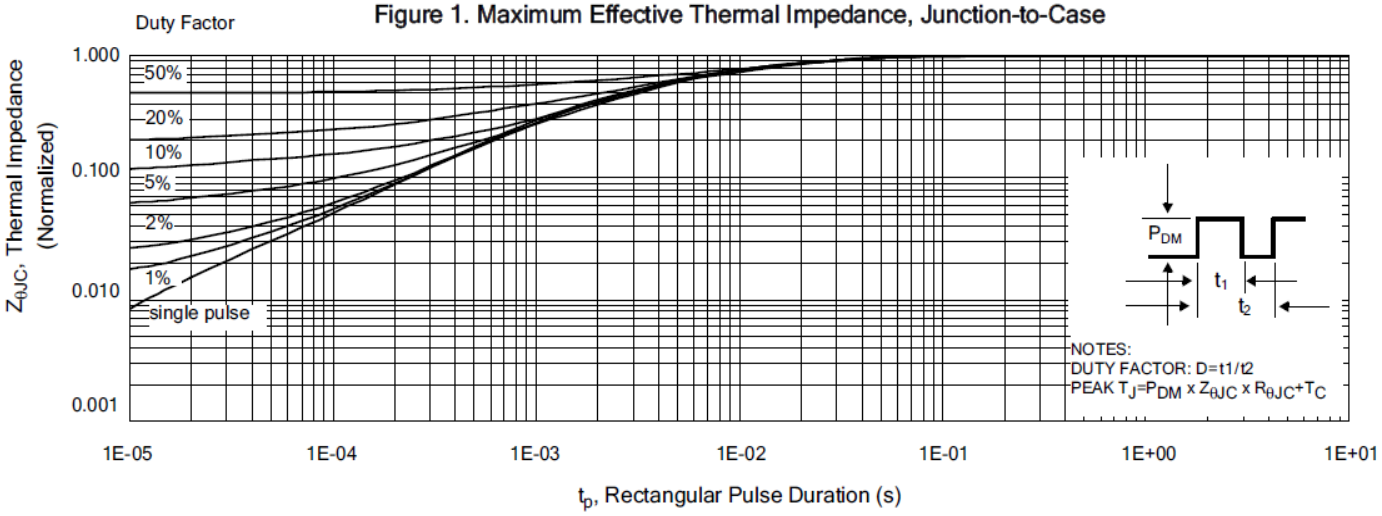


Figure 2. Maximum Power Dissipation vs Case Temperature

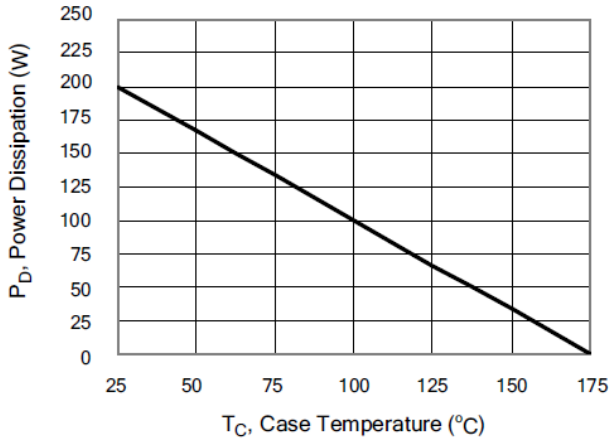


Figure 3. Maximum Continuous Drain Current vs Case Temperature

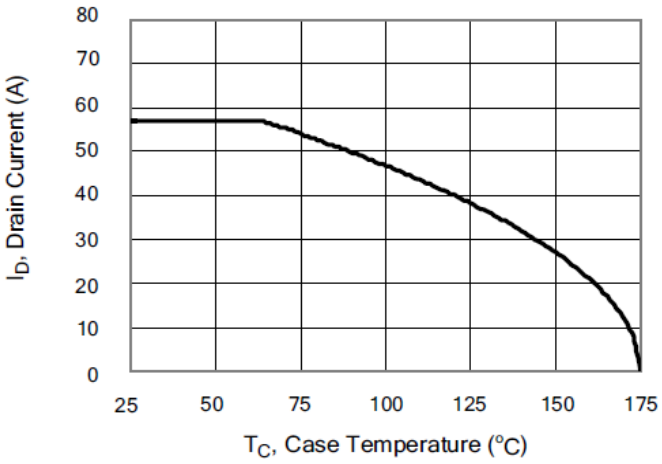


Figure 4. Typical Output Characteristics

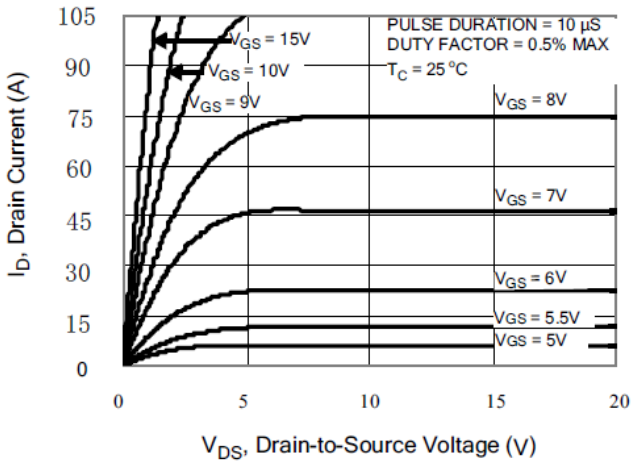
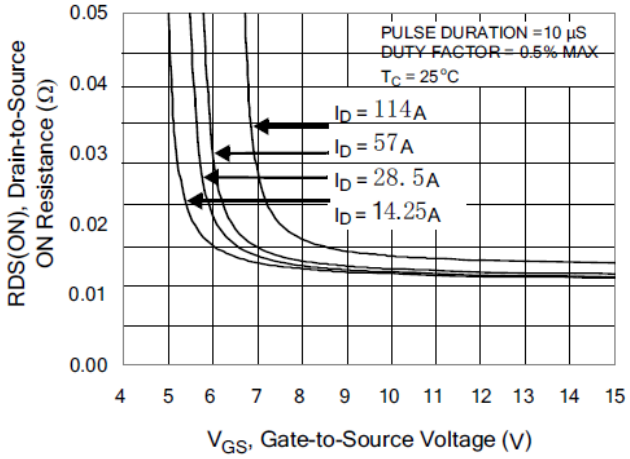


Figure 5. Typical Drain-to-Source ON Resistance vs Gate Voltage and Drain Current



Typical Characteristics T_J = 25ºC, unless otherwise noted

Figure 6. Maximum Peak Current Capability

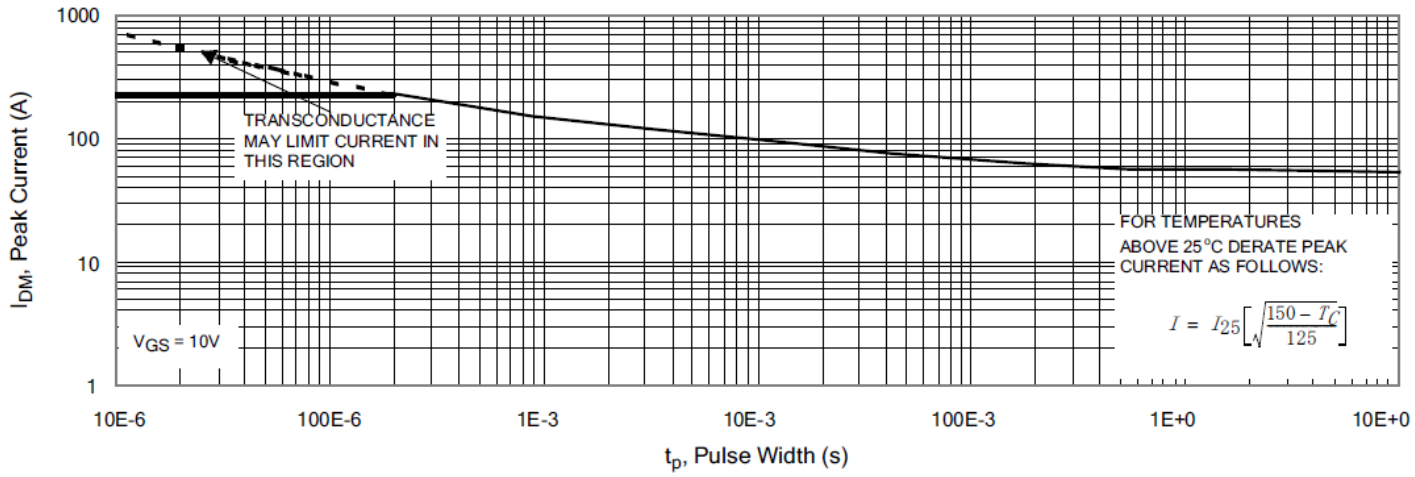


Figure 7. Typical Transfer Characteristics

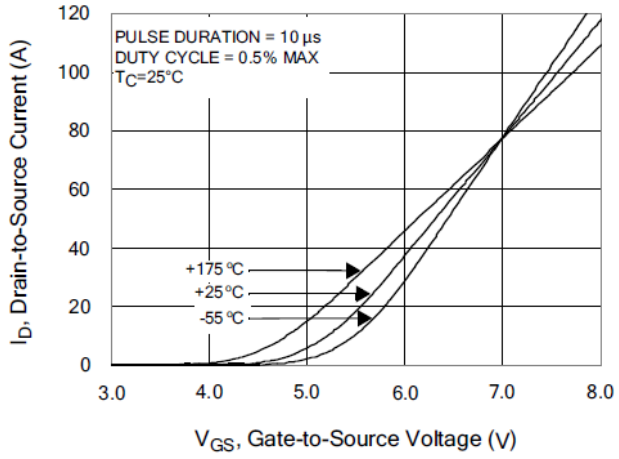


Figure 8. Unclamped Inductive Switching Capability

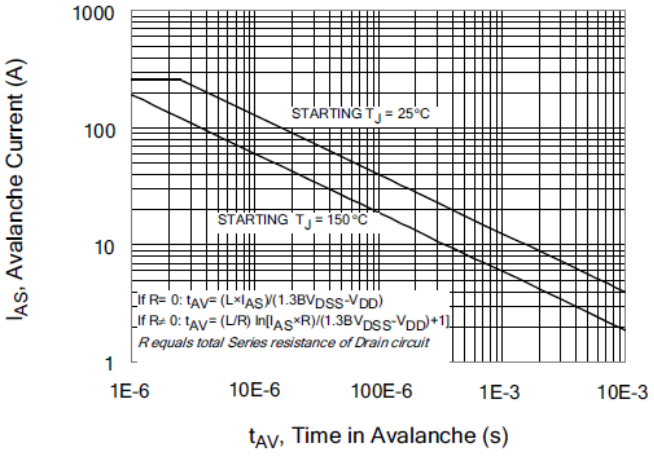


Figure 9. Typical Drain-to-Source ON Resistance vs Drain Current

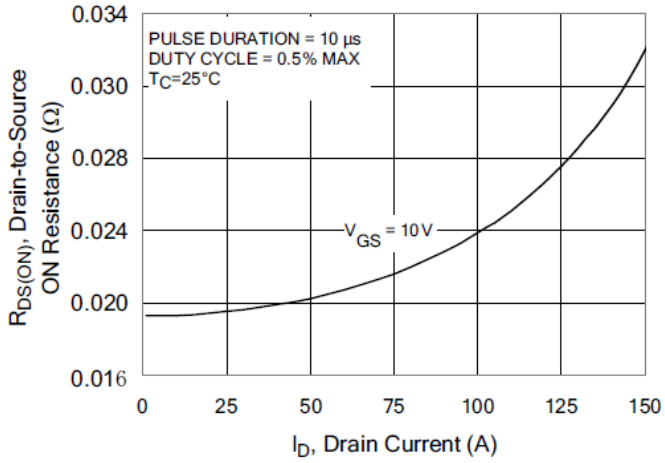
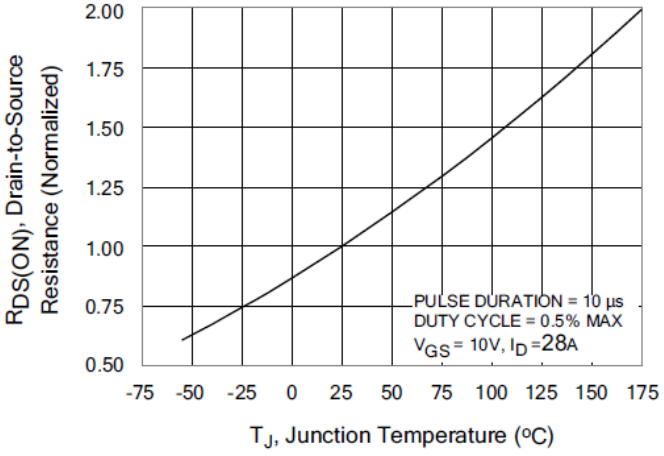


Figure 10. Typical Drain-to-Source ON Resistance vs Junction Temperature



Typical Characteristics T_J = 25°C, unless otherwise noted

Figure 11. Typical Breakdown Voltage vs Junction Temperature

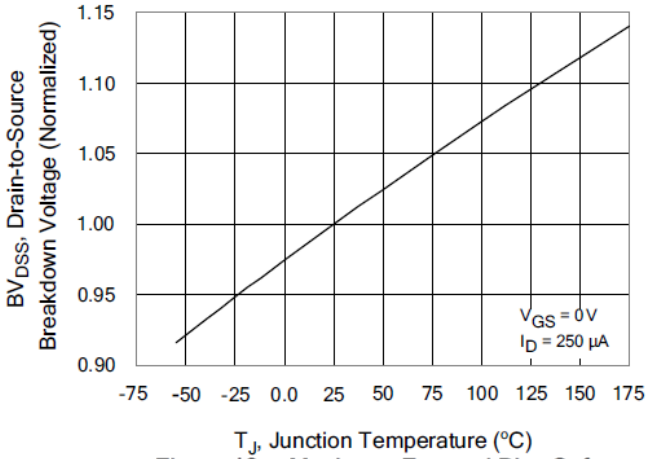


Figure 12. Typical Threshold Voltage vs Junction Temperature

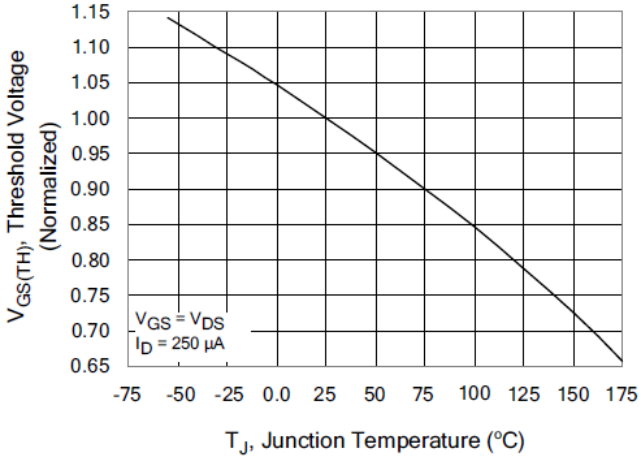


Figure 13. Maximum Forward Bias Safe Operating Area

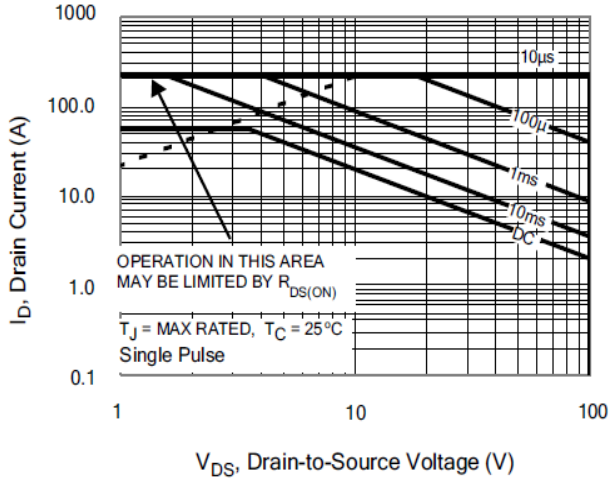


Figure 14. Capacitance vs Vds

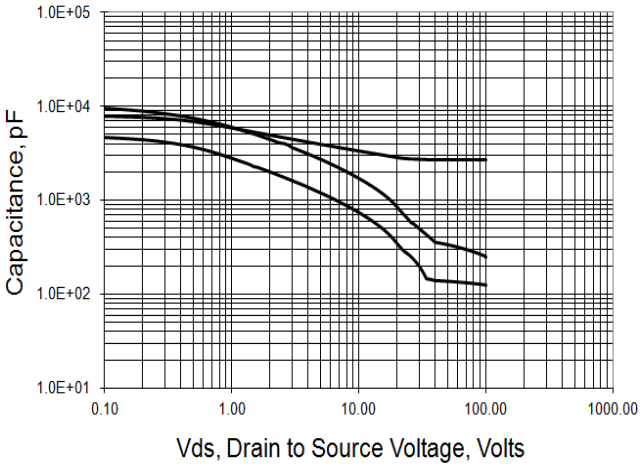


Figure 15 .Typical Gate Charge

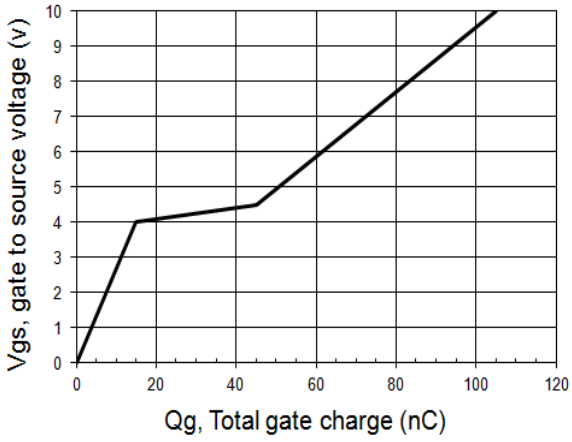


Figure 16. Typical Body Diode Transfer Characteristics

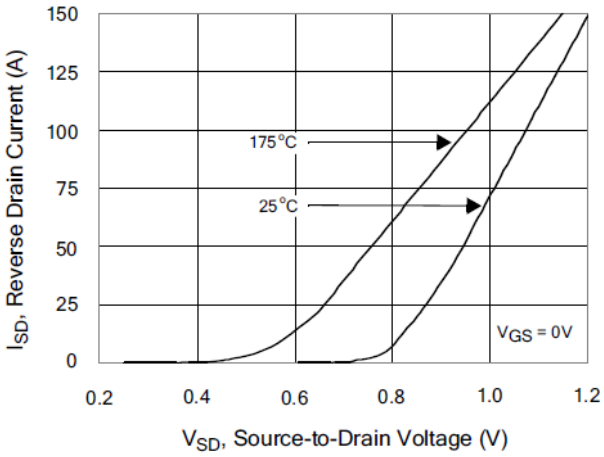


Figure A：Gate Charge Test Circuit and Waveform

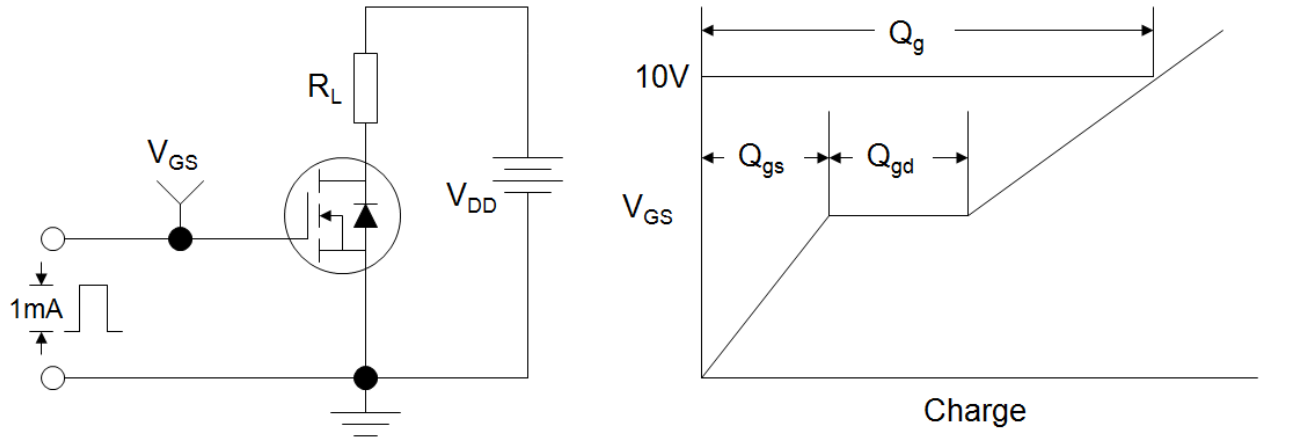


Figure B：Resistive Switching Test Circuit and Waveform

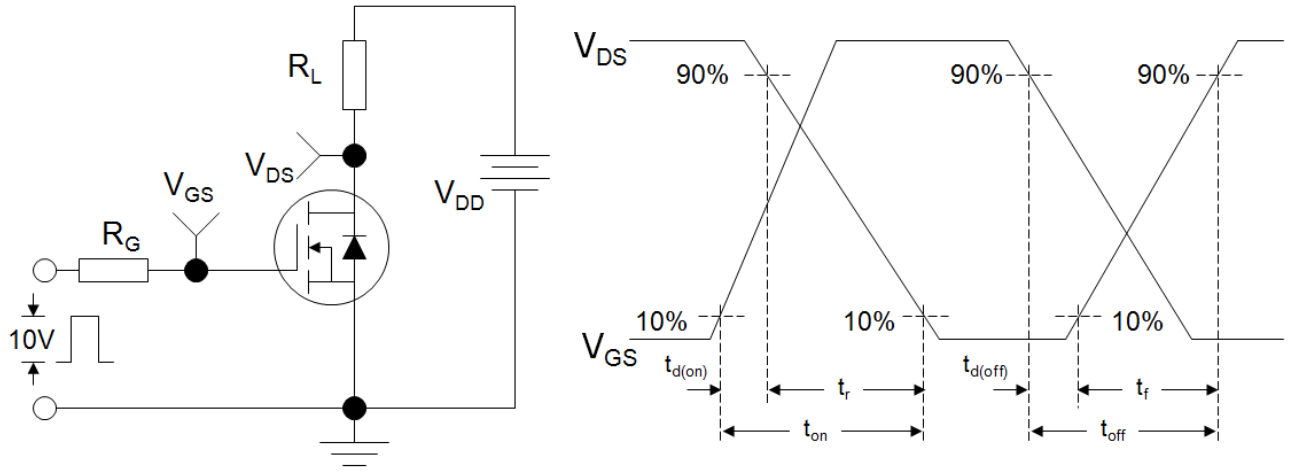
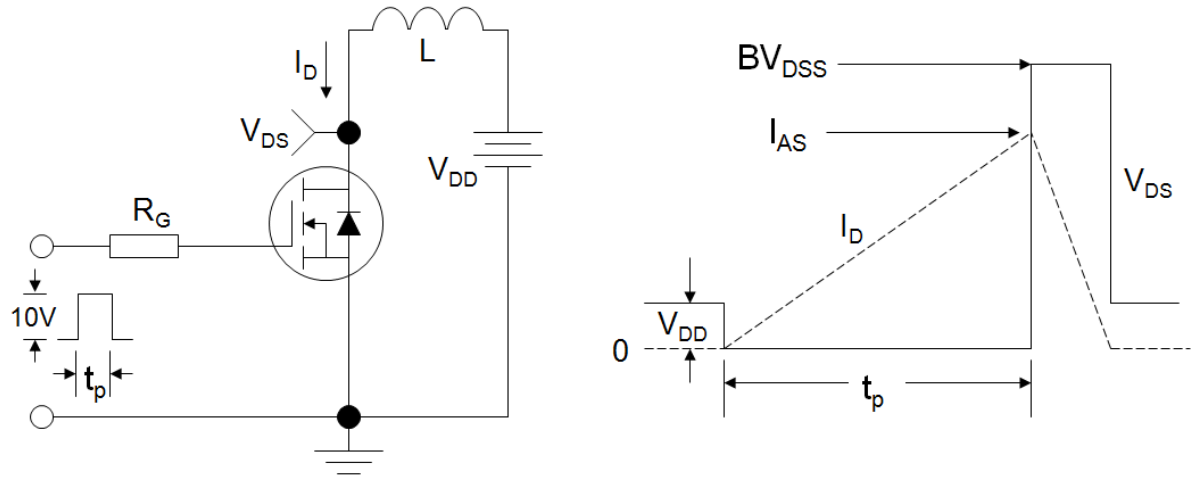
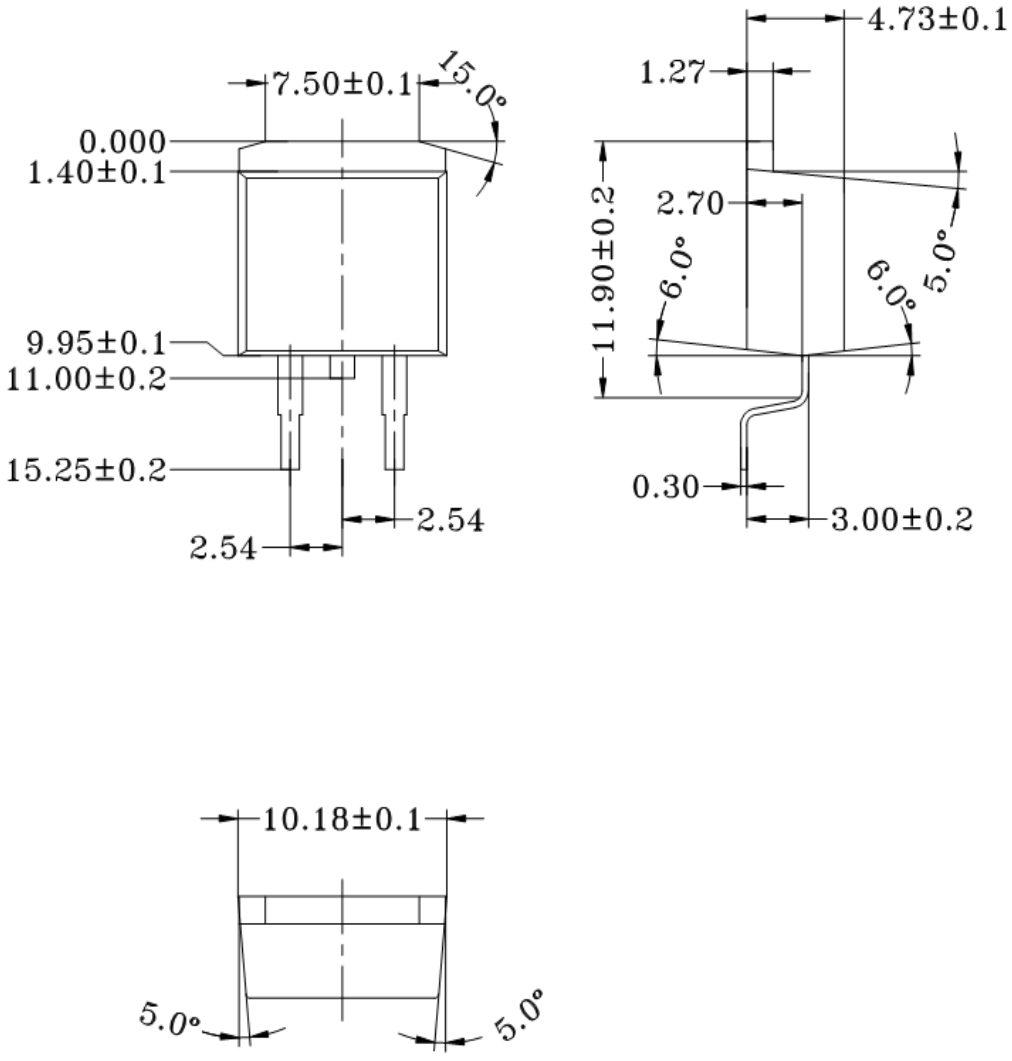


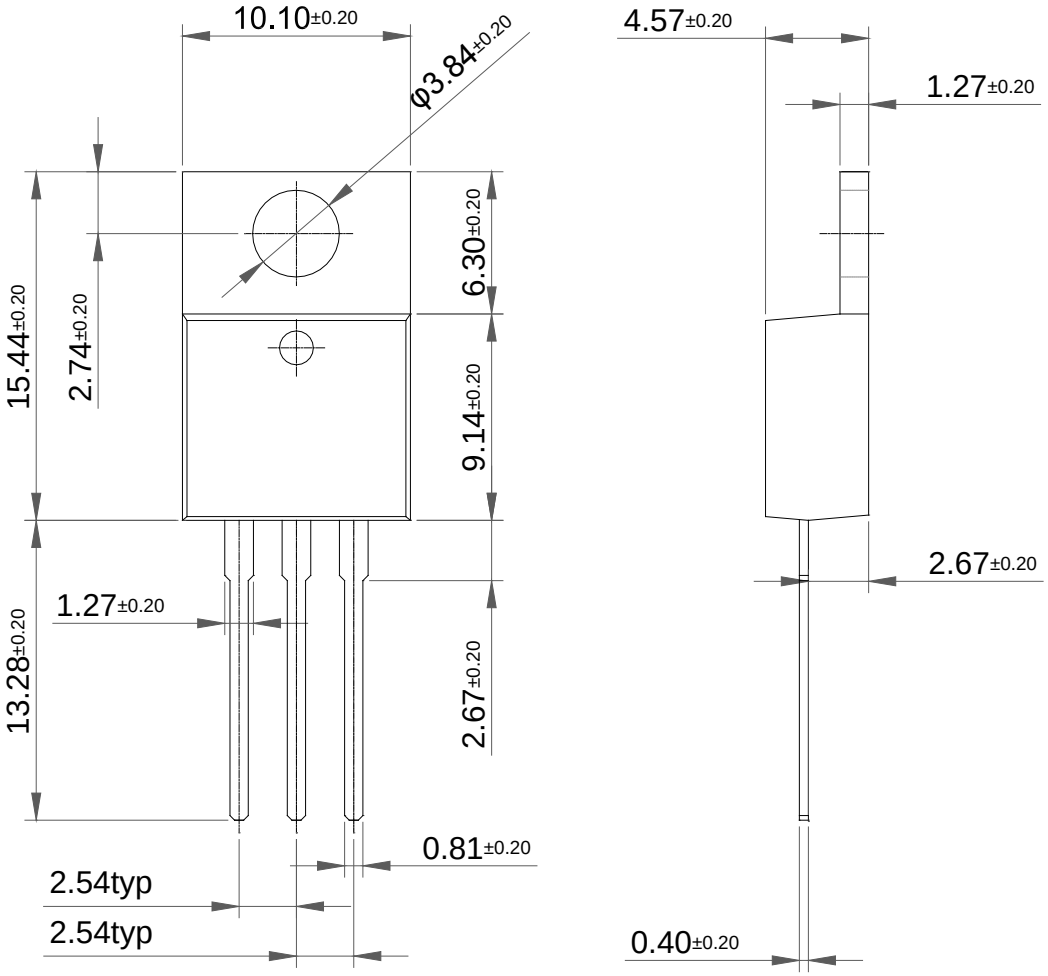
Figure C：Unclamped Inductive Switching Test Circuit and Waveform



TO-263


7.50±0.1
15.0°
0.000
1.40±0.1
9.95±0.1
11.00±0.2
15.25±0.2
2.54
2.54
4.73±0.1
1.27
11.90±0.2
2.70
6.0°
6.0°
5.0°
0.30
3.00±0.2
10.18±0.1
5.0°
5.0°

TO-220


10.10±0.20
φ3.84±0.20
4.57±0.20
1.27±0.20
6.30±0.20
15.44±0.20
2.74±0.20
9.14±0.20
2.67±0.20
1.27±0.20
13.28±0.20
2.67±0.20
0.81±0.20
2.54typ
2.54typ
0.40±0.20

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