

Multilayer Ceramic Chip Capacitor

Part Number: 2211JA250101KATSPU

Description: 2211 250Vac 50/60Hz / 2500Vdc 100pF ±10% C0G/NP0 (1B) to AEC-Q200

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Approval IEC/EN60384-14:2013
Specifications: UL-60950-1, 2nd Ed

CSA 60950-1-07 2nd Ed

Certification: Unmarked parts are uncertified but

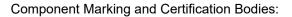
manufactured in accordance with the above

specifications.

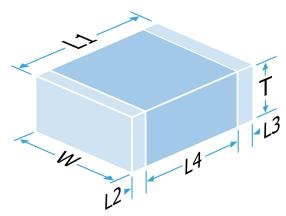
Classification: These capacitors comply with the

requirements of IEC/EN 60384-14:2013. For

class Y2/X1.



Not Applicable



Mechanical Specification

Size Code

Length (L1) in mm (")
Width (W) in mm (")

Thickness (T) in mm (")

Minimum Termination Band (L2,L3) in mm (")

Maximum Termination Band (L2,L3) in mm (")

Minimum Band Gap (L4) in mm (")

Termination Material

Solderability Packaging 2211

 $5.7 \pm 0.40 \ (0.225 \pm 0.016)$

 $2.79 \pm 0.30 \ (0.11 \pm 0.012)$

2.54 Max (0.1 Max)

0.50 (0.020)

0.80 (0.030) 4.0 (0.158)

Nickel Barrier, Sn Plated Solder (RoHS compliant)

IEC-60068-2-58

7" Reel Horizontal Orientation, 750 per reel

General Electrical Specification

Rated Voltage

Nominal Capacitance Value

Capacitance Tolerance

Tangent of Loss Angle (Tan δ)

Capacitance and Tan δ Test Conditions

Voltage Proof

(Voltage applied for 5 secs max. @ 50mA max. charge current)

Min Insulation Resistance (IR)

Dielectric Classification
Rated Temperature Range

Maximum Capacitance Change over Temperature Range

Climatic Category (IEC)
Ageing Characteristic

250Vac 50/60Hz / 2500Vdc 5kV impulse

100pF

±10%

≤0.0015

1.0Vrms @ 1MHz

3000Vdc/2000Vac

100.00GOhm @ 100Vdc

C0G/NP0 (1B) to AEC-Q200

-55°C / +125°C

No DC Voltage 0±30ppm/°C

Rated DC Voltage -

55/125/56 Zero

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Date: Friday, January 18, 2019

20190118 032651737UTC



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Part Number: 2211JA250101KATSPU

2211 250Vac 50/60Hz / 2500Vdc 100pF **Description:** ±10% C0G/NP0 (1B) to AEC-Q200

Environmental

RoHS Compliant to 2011/65/EC as amended by 2015/863/EU

Compliant

REACH Compliant

191 compliant

California Proposition 65

No exposure risk

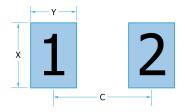
Board Layout

Knowles' conventional 2-terminal chip capacitors generally be mounted using pad designs in accordance with international specification IPC-7351, Generic Requirements for Surface Mount Design and Land Pattern Standards, but there are some other factors that have been shown to reduce mechanical stress, such as reducing the pad width to less than the chip width. In addition, the position of the chip on the board should be considered.

Some high voltage parts may require modifications to the board layout and/or the addition of a conformal coating to prevent flashover. Refer to application note AN0043 for further information.

IPC-7351 pad design

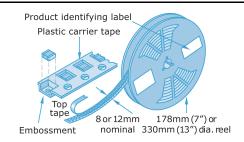
	2211	
С	5.40mm	0.213"
Υ	1.35mm	0.053"
Х	3.10mm	0.122"



Packaging

Tape packaging information for tape-and-reel parts:

Tape and reel packing of surface mounting chip capacitors for automatic placement are in accordance with IEC60286-3.



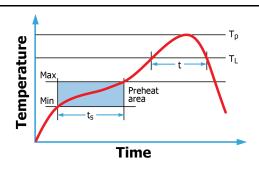
Soldering

Reflow solder in accordance with IPC-A-610. Recommended reflow profile as laid down in IPC/JEDEC J-STD-020.

Wave soldering is also possible, but care must be taken for case sizes 1210 and larger and component thickness >1.0mm. Trials are encouraged.

Hand soldering is not recommended and can lead to component damage through thermal shock.

DLI



Application notes with mounting and handling guidance are available on request.

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Novacap

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Date: Friday, January 18, 2019

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1210J5000103KXT 1210J5000223KXT D55342E07B379BR-TR D55342E07B523DR-T/R 1812J1K00103KXT 1812J1K00473KXT

1812J2K00680JCT 1812J4K00102MXT 1812J5000102JCT 1812J5000103JCT 1812J5000682JCT NIN-FB391JTRF NIN-FC2R7JTRF

NPIS27H102MTRF C1206C101J1GAC C1608C0G1E472JT000N C2012C0G2A472J 2220J2K00101JCT KHC201E225M76N0T00

1812J1K00222JCT 1812J2K00102KXT 1812J2K00222KXT 1812J2K00472KXT 2-1622820-7-CUT-TAPE 2220J3K00102KXT

2225J2500824KXT CCR07CG103KM CGA2B2C0G1H010C CGA2B2C0G1H040C CGA2B2C0G1H050C CGA2B2C0G1H060D

CGA2B2C0G1H070D CGA2B2C0G1H151J CGA2B2C0G1H1R5C CGA2B2C0G1H2R2C CGA2B2C0G1H3R3C CGA2B2C0G1H680J

CGA4J2X7R2A104K