

Three-terminal positive voltage regulator

OUTPUT CURRENT TO 1.2A

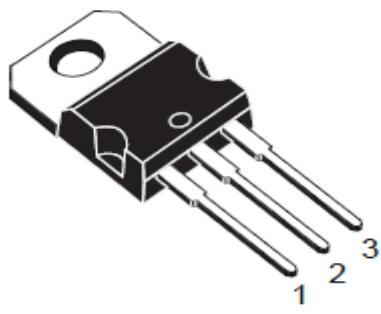
OUTPUT VOLTAGES OF 5; 6; 8; 9; 12V

THERMAL OVERLOAD PROTECTION

SHORT CIRCUIT PROTECTION

OUTPUT TRANSITION SOA PROTECTION

## 1、 Absolute Maximum Ratings $T_c=25^\circ\text{C}$

Symbol	Parameter	Value	UNIT	 <b>TO-220</b> 1 Input 2 Gnd 3 Out
VI	Input Voltage	35	V	
TOPR	Operating Temperature Range	0 ~ +125	°C	
TSTG	Storage Temperature Range	-65 ~+150	°C	

2、 Electrical Characteristics ( $T_c=25^\circ\text{C}$ ) Of 7805 (refer to the test circuits,  $T_J = -55$  to  $150^\circ\text{C}$   $VI = 10\text{V}$ ,  $I_0 = 500\text{ mA}$ ,  $C_I = 0.33\text{ }\mu\text{F}$ ,  $C_O = 0.1\text{ }\mu\text{F}$  unless otherwise specified)。

Parameter	Symbol	Test Condition		MIN	TYP	MAX	UNIT
Output Voltage	V <sub>O</sub>	$T_J = +25^\circ\text{C}$		4.8	5	5.2	V
		$I_0 = 5\text{mA}$ to $1\text{A}$ , $P_0 \leqslant 15\text{W}$ $VI = 8\text{V}$ to $20\text{V}$		4.75	5	5.25	
Line Regulation (Note1)	$\Delta V_O$	$T_J = +25^\circ\text{C}$	VI = 7V to 25V			100	mV
			VI = 8V to 12V			50	
Load Regulation (Note1)	$\Delta V_O$	$T_J = +25^\circ\text{C}$ $I_0 = 5\text{mA}$ to $1.2\text{A}$				100	mV
		$T_J = +25^\circ\text{C}$ $I_0 = 250\text{mA}$ to $750\text{mA}$				50	
Quiescent Current	I <sub>Q</sub>	$T_J = +25^\circ\text{C}$				6	mA
Quiescent Current Change	$\Delta I_Q$	$I_0 = 5\text{mA}$ to $1\text{A}$				0.5	mA
		$VI = 8\text{V}$ to $25\text{V}$				0.8	
Quiescent Current Change	$\Delta V_O/\Delta T$	$I_0 = 5\text{mA}$				0.6	mV/°C
Short Circuit Current	I <sub>SC</sub>	$T_J = +25^\circ\text{C}$ , $VI = 35\text{V}$				0.75	1.2
							A

**3、Electrical Characteristics** ( $T_c=25^\circ\text{C}$ ) Of 7806(refer to the test circuits, $T_J = -55$  to  $150^\circ\text{C}$   $VI = 11\text{V}$  ,  
 $I_0 = 500 \text{ mA}$  ,  $C_I = 0.33 \mu\text{F}$  ,  $C_O = 0.1 \mu\text{F}$  unless otherwise specified)。

Parameter	Symbol	Test Condition		MIN	TYP	MAX	UNIT
Output Voltage	$V_0$	$T_J = +25^\circ\text{C}$		5.75	6	6.25	V
		$I_0 = 5\text{mA}$ to $1\text{A}$ , $P_0 \leqslant 15\text{W}$ $VI = 9\text{V}$ to $21\text{V}$		5.65	6	6.35	
Line Regulation (Note1)	$\Delta V_0$	$T_J = +25^\circ\text{C}$	VI = 8V to 25V			100	mV
			VI = 9V to 13V			50	
Load Regulation (Note1)	$\Delta V_0$	$T_J = +25^\circ\text{C}$ $I_0 = 5\text{mA}$ to $1.2\text{A}$				100	mV
		$T_J = +25^\circ\text{C}$ $I_0 = 250\text{mA}$ to $750\text{mA}$				50	
Quiescent Current	$I_Q$	$T_J = +25^\circ\text{C}$				6	mA
Quiescent Current Change	$\Delta I_Q$	$I_0 = 5\text{mA}$ to $1\text{A}$				0.5	mA
		$VI = 9\text{V}$ to $25\text{V}$				0.8	
Quiescent Current Change	$\Delta V_0/\Delta T$	$I_0 = 5\text{mA}$				0.7	mV/°C
Short Circuit Current	$I_{SC}$	$T_J = +25^\circ\text{C}$ , $VI = 35\text{V}$			0.75	1.2	A

**4、 Electrical Characteristics (T<sub>c</sub>=25°C) Of 7808(refer to the test circuits, T<sub>J</sub> = -55 to 150°C VI = 14V, I<sub>O</sub> = 500 mA, C<sub>I</sub> = 0.33 μF, C<sub>O</sub> = 0.1 μF unless otherwise specified)。**

Parameter	Symbol	Test Condition		MIN	TYP	MAX	UNIT
Output Voltage	V <sub>O</sub>	T <sub>J</sub> = +25°C		7. 7	8	8. 3	V
		I <sub>O</sub> = 5mA to 1A, P <sub>O</sub> ≤ 15W VI = 11. 5V to 23V		7. 6	8	8. 4	
Line Regulation (Note1)	Δ V <sub>O</sub>	T <sub>J</sub> = +25°C	VI = 10. 5V to 25V			100	mV
			VI = 11V to 17V			50	
Load Regulation (Note1)	Δ V <sub>O</sub>	T <sub>J</sub> = +25°C I <sub>O</sub> = 5mA to 1. 2A				100	mV
		T <sub>J</sub> = +25°C I <sub>O</sub> = 250mA to 750mA				50	
Quiescent Current	I <sub>Q</sub>	T <sub>J</sub> = +25°C				6	mA
Quiescent Current Change	Δ I <sub>Q</sub>	I <sub>O</sub> = 5mA to 1A				0. 5	mA
		VI = 11. 5V to 25V				1	
Quiescent Current Change	Δ V <sub>O</sub> /Δ T	I <sub>O</sub> = 5mA				1	mV/°C
Short Circuit Current	I <sub>SC</sub>	T <sub>J</sub> = +25°C, VI = 35V			0. 75	1. 2	A

**5、 Electrical Characteristics (T<sub>c</sub>=25°C) Of 7809(refer to the test circuits, T<sub>J</sub> = -55 to 150°C VI = 15V, I<sub>O</sub> = 500 mA, C<sub>I</sub> = 0.33 μF, C<sub>O</sub> = 0.1 μF unless otherwise specified).**

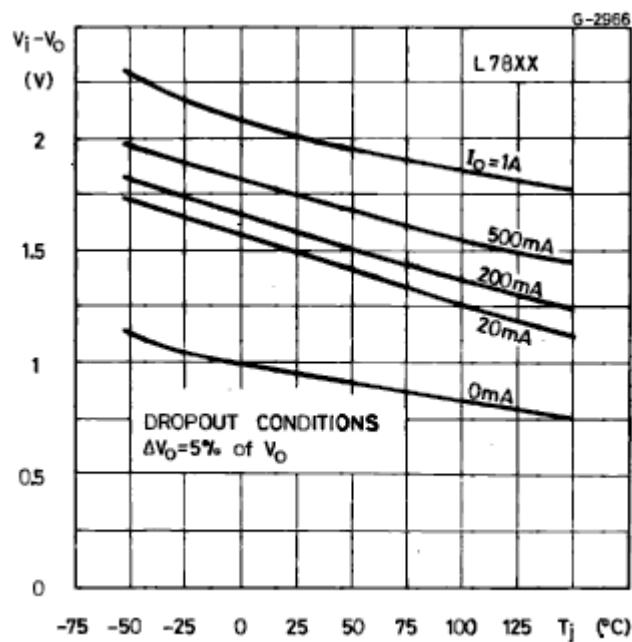
Parameter	Symbol	Test Condition		MIN	TYP	MAX	UNIT
Output Voltage	V <sub>O</sub>	T <sub>J</sub> = +25°C		8.64	9	9.36	V
		I <sub>O</sub> = 5mA to 1A, P <sub>O</sub> ≤ 15W	VI = 11.5V to 26V	8.55	9	9.45	
Line Regulation (Note1)	Δ V <sub>O</sub>	T <sub>J</sub> = +25°C	VI = 11.5V to 26V			100	mV
			VI = 12V to 18V			50	
Load Regulation (Note1)	Δ V <sub>O</sub>	T <sub>J</sub> = +25°C I <sub>O</sub> = 5mA to 1.2A				100	mV
			T <sub>J</sub> = +25°C I <sub>O</sub> = 250mA to 750mA			50	
Quiescent Current	I <sub>Q</sub>	T <sub>J</sub> = +25°C				6	mA
Quiescent Current Change	Δ I <sub>Q</sub>	I <sub>O</sub> = 5mA to 1A				0.5	mA
		VI = 11.5V to 26V				1	
Quiescent Current Change	Δ V <sub>O</sub> /Δ T	I <sub>O</sub> = 5mA			1		mV/°C
Short Circuit Current	I <sub>SC</sub>	T <sub>J</sub> = +25°C, VI = 35V			0.75	1.2	A

**6、 Electrical Characteristics (T<sub>c</sub>=25°C) Of 7812 (refer to the test circuits, T<sub>J</sub> = -55 to 150°C VI = 19V, I<sub>O</sub> = 500 mA, C<sub>I</sub> = 0.33 μF, C<sub>O</sub> = 0.1 μF unless otherwise specified).**

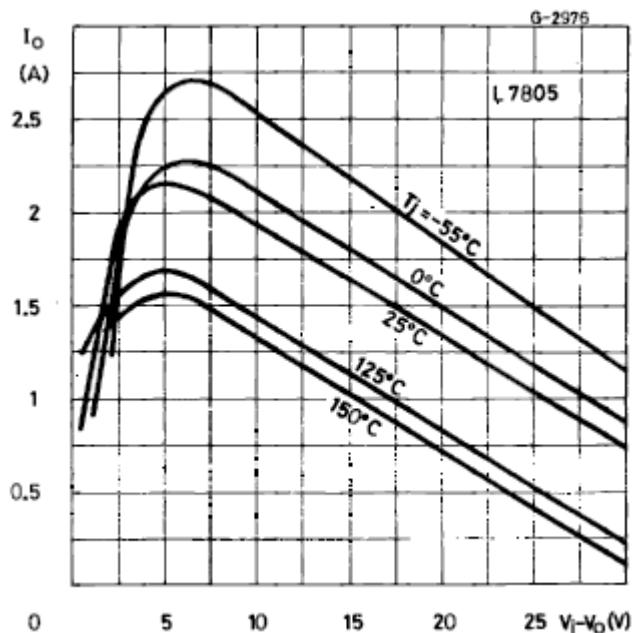
Parameter	Symbol	Test Condition		MIN	TYP	MAX	UNIT
Output Voltage	V <sub>O</sub>	T <sub>J</sub> = +25°C		11.5	12	12.5	V
		I <sub>O</sub> = 5mA to 1A, P <sub>O</sub> ≤ 15W VI = 15.5V to 27V		11.4	12	12.6	
Line Regulation (Note1)	Δ V <sub>O</sub>	T <sub>J</sub> = +25°C	VI = 14.5V to 30V			100	mV
			VI = 16V to 22V			50	
Load Regulation (Note1)	Δ V <sub>O</sub>	T <sub>J</sub> = +25°C I <sub>O</sub> = 5mA to 1.2A				100	mV
		T <sub>J</sub> = +25°C I <sub>O</sub> = 250mA to 750mA				50	
Quiescent Current	I <sub>Q</sub>	T <sub>J</sub> = +25°C				6	mA
Quiescent Current Change	Δ I <sub>Q</sub>	I <sub>O</sub> = 5mA to 1A				0.5	mA
		VI = 15V to 30V				1	
Quiescent Current Change	Δ V <sub>O</sub> /Δ T	I <sub>O</sub> = 5mA			1.5		mV/°C
Short Circuit Current	I <sub>SC</sub>	T <sub>J</sub> = +25°C, VI = 35V			0.75	1.2	A

## 7、Typical Characteristics

**Figure 1:** Dropout Voltage vs Junction Temperature

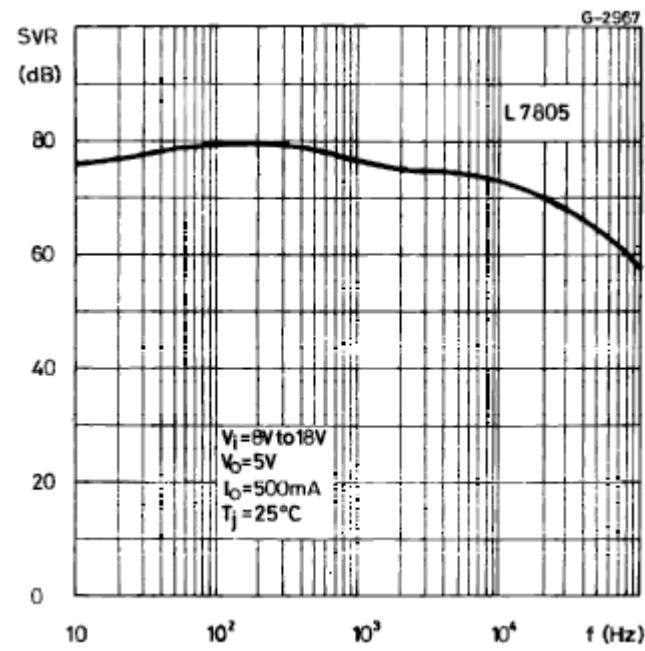


**Figure 2:** Peak Output Current vs Input/output Differential Voltage

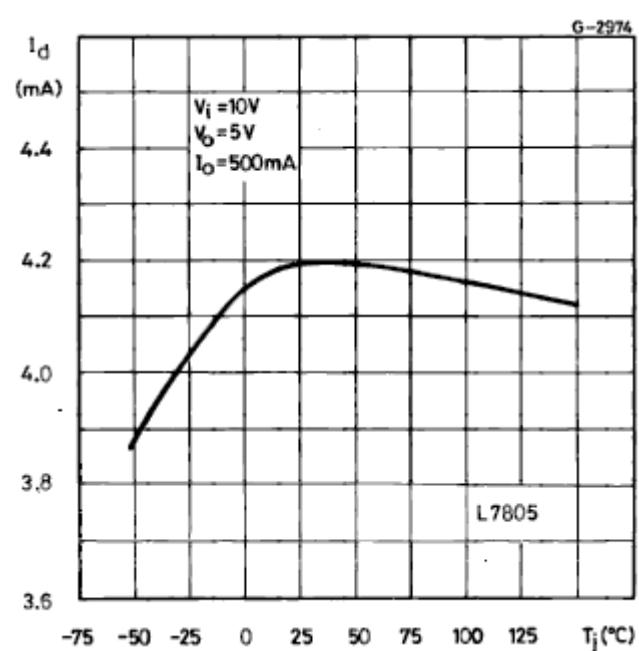


**Figure 3:** Supply Voltage Rejection vs Frequency

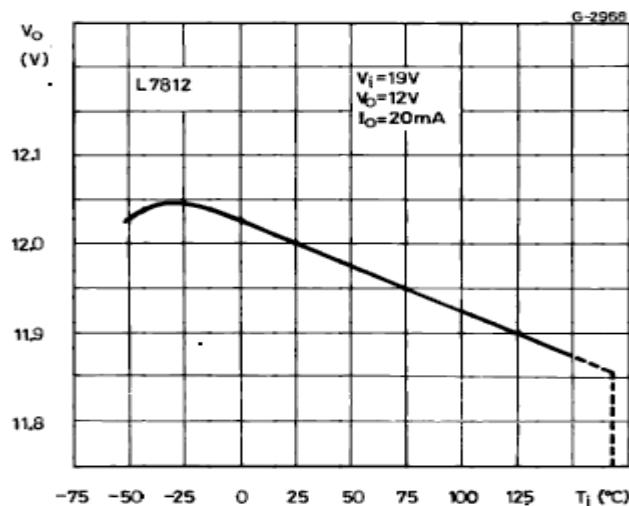
Temperature



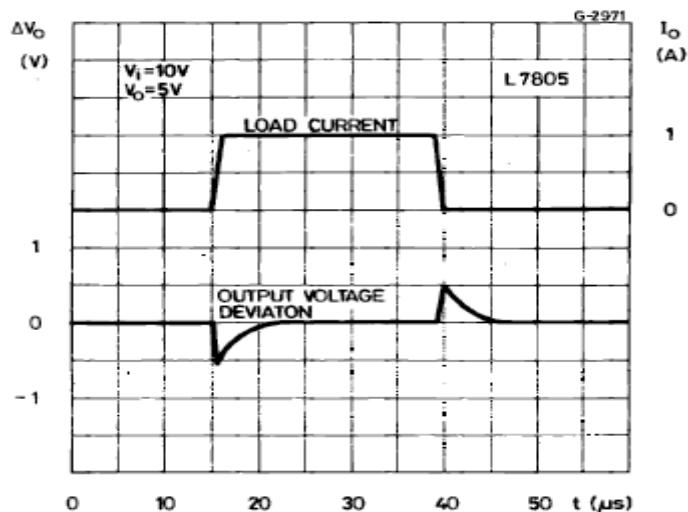
**Figure 4:** Quiescent Current vs Junction



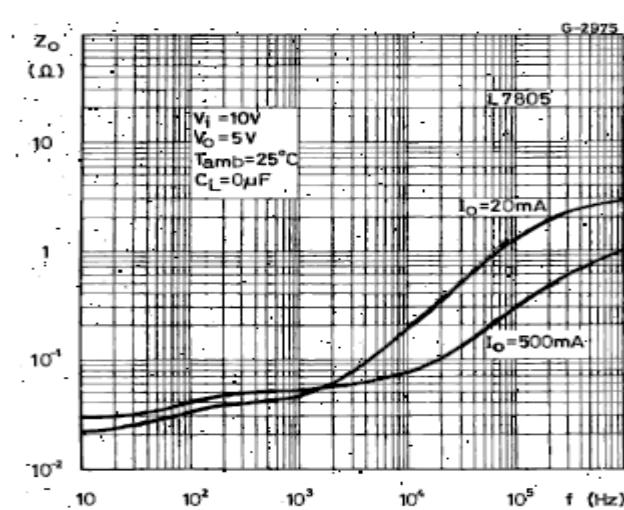
**Figure 5:** Output Voltage vs Junction Temperature



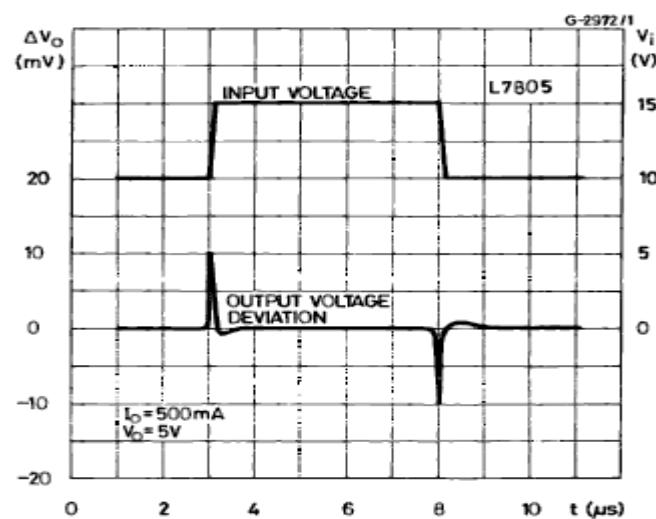
**Figure 6:** Load Transient Response



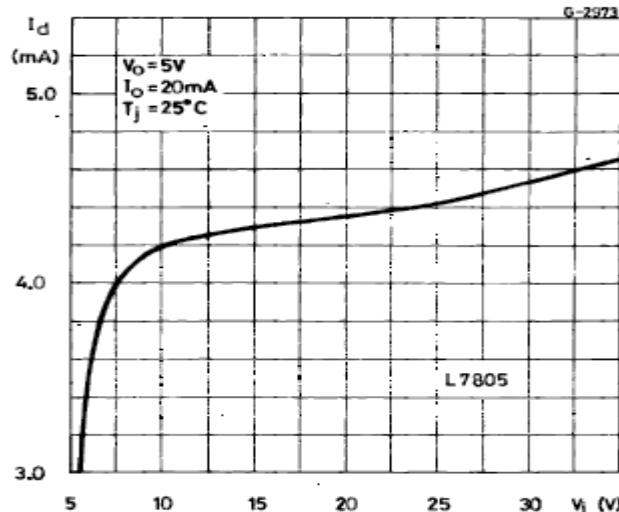
**Figure 7:** Output Impedance vs Frequency



**Figure 8:** Line Transient Response



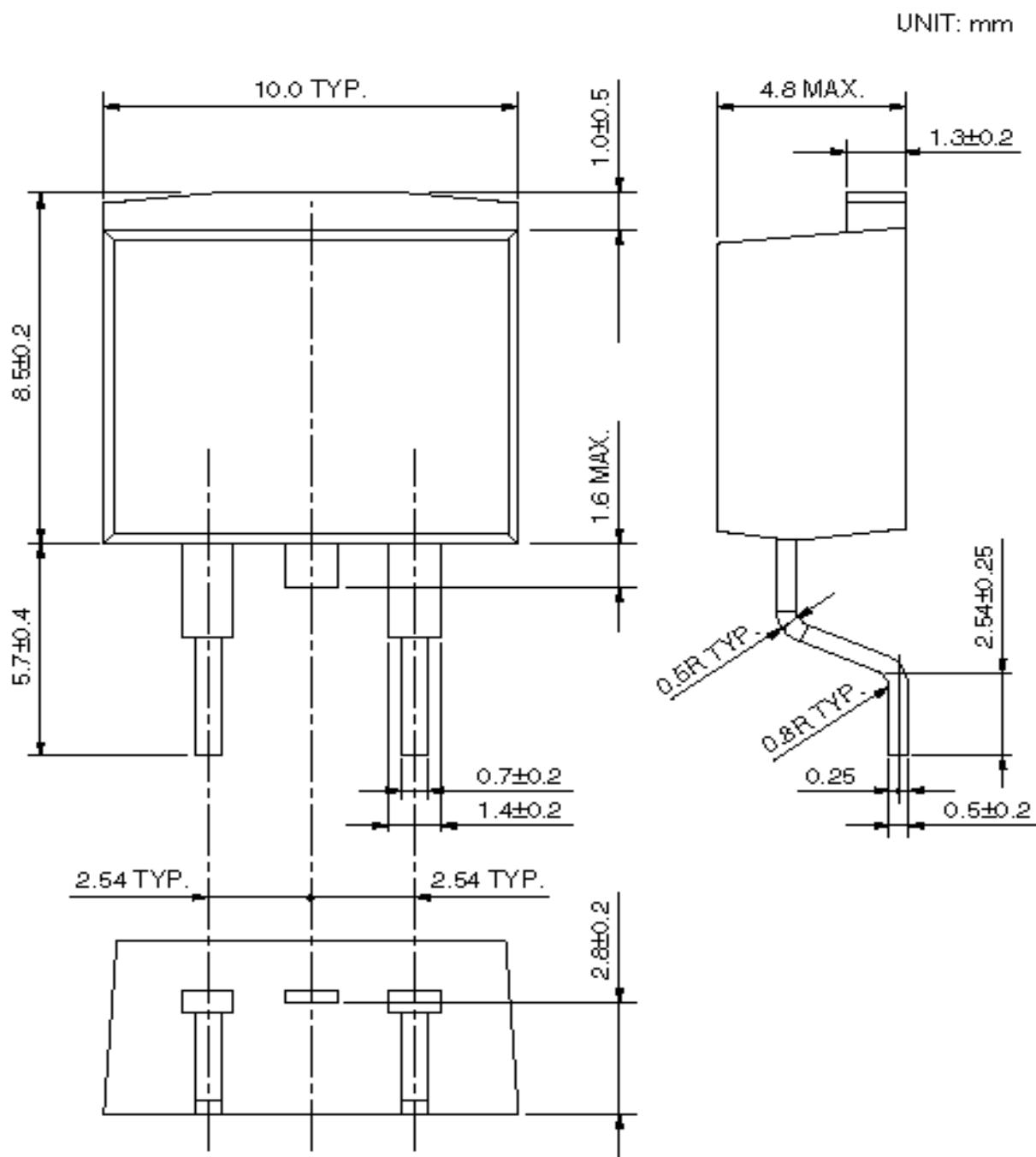
**Figure 9:** Quiescent Current vs Input Voltage



## 8、Package Demensions

TO-263-3

TO-263封装尺寸：



: The area without solder plated

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