



# Specification for Approval

Date: 2017/9/22

HCB3216KF-221T30







Customer: 天誠

TAI-TECH P/N:

	0110701150 0/11						
	CUSTOMER P/N:						
	DESCRIPTION:						
	QUANTITY:	pcs	3				
REM	IARK:						
	C	ustomer Approval Feedba	ack				

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**TAI-TECH** KBM01-170900465

# High Current Ferrite Chip Bead(Lead Free)

HCB3216KF-221T30

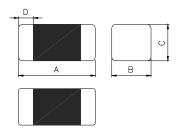
Certificate

Green Partner

#### 1.Features

- 1. Monolithic inorganic material construction.
- 2. Closed magnetic circuit avoids crosstalk.
- 3. Suitable for reflow soldering.
- 4. Shapes and dimensions follow E.I.A. spec.
- 5. Available in various sizes.
- 6. Excellent solder ability and heat resistance.
- 7. High reliability.
- 8.100% Lead(Pb) & Halogen-Free and RoHS compliant.
- 9. Low DC resistance structure of electrode to prevent wasteful electric power consumption.

#### 2. Dimensions



Chip Size				
Α	3.20±0.20			
В	1.60±0.20			
С	1.10±0.20			
D	0.50±0.30			

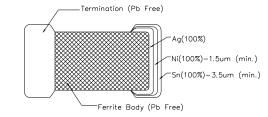
Units: mm

#### 3.Part Numbering



E: Packaging

T=Taping and Reel, B=Bulk(Bags) F: Rated Current

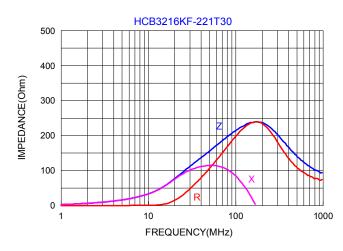


#### 4.Specification

Tai-Tech Part Number	Impedance ( $\Omega$ )	Test Frequency (Hz)	DC Resistance $(\Omega)$ max.	Rated Current (mA) max.
HCB3216KF-221T30	220±25%	60mV/100M	0.04	3000

- Rated current: based on temperature rise test
- In compliance with EIA 595

#### Impedance-Frequency Characteristics



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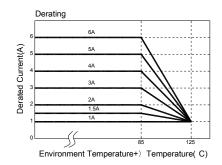
## 5. Reliability and Test Condition

Item			Performance				Te	st Cond	dition	
Series No.	FCB	FCM	<mark>HCB</mark>	GHB	FCA					
Operating Temperature		(Includir	-40~+125℃ ng self-temperat	ure rise)						
Transportation Storage Temperature			-40~+125℃ (on board)			For long storage conditions, please see the Application Notice				see the
Impedance (Z)	Pefer to standard	alactrical abo	ara eto riotico liot			Agilent42 Agilent E Agilent42 Agilent16	287			
DC Resistance	Refer to standard	electrical cha	iracteristics list			Agilent 4338				
Rated Current						DC Powe Over Rat some ris	ted Curr		ements, the	re will be
Temperature Rise Test		Rated Current < 1A ΔT 20℃Max Rated Current ≧ 1A ΔT 40℃Max						owed DC neasured	current. by digital su	ırface
Life test	Appearance: no d	±15%of initia				times.( If Reflow F Tempera Applied of Duration Measure for 24±2	PC/JEDI Profiles) ture: 12 current: : 1000± d at ro hrs.	EC J-STD  5±2°C  rated curn  12hrs.  com_tempe	rature after	sification
Load Humidity	Q : Shall not exce	Inductance: within±10%of initial value. Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value					Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Humidity: 85±2%R.H. Temperature: 85±2°C. Duration: 1000hrs Min. with 100% rated current. Measured at room temperature after placing for 24±2 hrs.			
Thermal shock	Appearance: no damage.  Impedance: within±15%of initial value. Inductance: within±10%of initial value. Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value					Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1: -40±2°C 30±5 min. Step2: 25±2°C ≦ 0.5min Step3: +125±2°C 30±5min. Number of cycles: 500 Measured at room temperature after placing for 24±2 hrs.				
Vibration	Appearance: No Impedance: withi Inductance: withi Q: Shall not exce	n±15% of init n±10% of init ed the specif	ial value ication value.	t exceed the spe	cification value	Preconditioning: Run through IR reflow for times.( IPC/JEDEC J-STD-020D Classificatio Reflow Profiles) Oscillation Frequency: 10~2K~10Hz for 2 minutes Equipment: Vibration checker Total Amplitude:1.52mm±10% Testing Time: 12 hours(20 minutes, 12 cycle each of 3 orientations)			sification z for 20	
Bending	Appearance: No Impedance: withi Inductance: withi Q: Shall not exce	n±10% of init n±10% of init ed the specif	ial value ication value.	t exceed the spe	ecification value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805inch(2012mm):40x100x1.2mm <0805inch(2012mm):40x100x0.8mm Bending depth: >=0805inch(2012mm):1.2mm <0805inch(2012mm):0.8mm Duration of 10 sec for a min.				
						Test co	ndition			
Shock	Appearance : No Impedance : withi Inductance : withi	n±10% of init n±10% of init	ial value			Туре	Peak Value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec
	Q : Shall not exce RDC : within ±159			t exceed the spe	ecification value	SMD	50	11	Half-sine	11.3
						Lead	50	11	Half-sine	11.3
Solderability	More than 95% of	the terminal e	electrode should	l be covered with	n solder.	Preheat: 150°C,60sec. Solder: Sn96.5%-Ag3%-Cu0.5% Solder temperature: 245±5°C Flux for lead free: Rosin. 9.5% Depth: completely cover the termination. Dip time: 4±1sec.				

Item	Performance	Test Condition			
			Number of heat	cycles: 1	
Resistance to Soldering	Appearance : No damage. Impedance : within±15% of initial value	Temperature (°C)	Time (s)	Temperature ramp/immersion and emersion rate	
Heat	Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed	260 ±5 (solder temp)	10 ±1	25mm/s ±6 mm/s	
			Depth: complete	ly cover th	ne termination
Terminal strength	Appearance: No damage.  Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Component mounted on a PCB apply a force >0805inch(2012mm):1kg <=0805inch(2012mm):0.5kg to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to shock the component being tested.		

#### \*\*Derating Curve

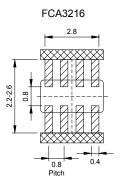
For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over  $85^{\circ}\mathrm{C}$ , the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



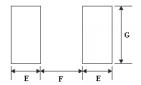
#### 6. Soldering and Mounting

#### 6-1. Recommended PC Board Pattern

	Chip Size							Land Patterns For Reflow Soldering		
Series	Type	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)	G(mm)		
	0603	0.6±0.03	0.30±0.03	0.30±0.03	0.15±0.05	0.35	0.30	0.40		
FCB	1005	1.0±0.10	0.50±0.10	0.50±0.10	0.25±0.10	0.50	0.40	0.60		
FCM	1608	1.6±0.15	0.80±0.15	0.80±0.15	0.30±0.20	0.80	0.85	0.95		
HCB	2012	2.0±0.20	1.25±0.20	0.85±0.20	0.50±0.30	1.05	1.00	1.45		
GHB	2012	2.0±0.20	1.25±0.20	1.25±0.20	0.50±0.30	1.05				
FCI	<mark>3216</mark>	3.2±0.20	1.60±0.20	1.10±0.20	0.50±0.30	<mark>1.05</mark>	<mark>2.20</mark>	<mark>1.80</mark>		
FHI FCH	3225	3.2±0.20	2.50±0.20	1.30±0.20	0.50±0.30	1.05	2.20	2.70		
HCI	4516	4.5±0.20	1.60±0.20	1.60±0.20	0.50±0.30	1.05	3.30	1.80		
1101	4532	4.5±0.20	3.20±0.20	1.50±0.20	0.50±0.30	1.05	3.30	3.40		



Land
Solder Resist



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

#### 6-2. Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools. Note.

If wave soldering is used ,there will be some risk.

Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

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#### 6-2.1 Lead Free Solder re-flow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. (Refered to J-STD-020C)

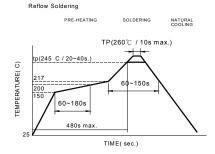
#### 6-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2.

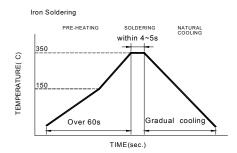
• Preheat circuit and products to 150℃

• 350 $^{\circ}$ C tip temperature (max)

- Never contact the ceramic with the iron tip1.0mm tip diameter (max)
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- Limit soldering time to 4~5sec.



Reflow times: 3 times max Fig.1



Iron Soldering times: 1 times max

#### 6-2.3 Solder Volume:

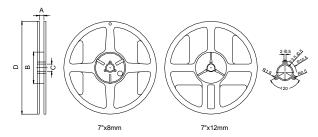
Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

Minimum fillet height = soldering thickness + 25% product height



#### 7. Packaging Information

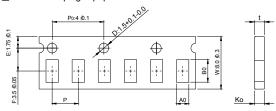
#### 7-1. Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)	
<mark>7"x8mm</mark>	9.0±0.5	60±2	13.5±0.5	178±2	
7"x12mm	13.5±0.5	60±2	13.5±0.5	178±2	

#### 7-2.1 Tape Dimension / 8mm

#### ■Material of taping is paper



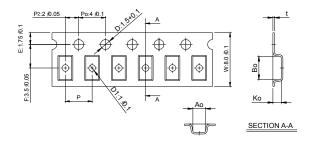
E:1.75.90.1	P22.0.1 P04.0.1 91.00.01.00.10	V:8.0 ±0.1	- t
F:3.5.0.1	P AQ	W:8.0	Ко

Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
060303	0.70±0.06	0.40±0.06	0.45max	2.0±0.05	0.45max
100505	1.12±0.03	0.62±0.03	0.60±0.03	2.0±0.05	0.60±0.03

Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
160808	1.80±0.05	0.96+0.05/-0.03	0.95±0.05	4.0±0.10	0.95±0.05
201209	2.10±0.05	1.30±0.05	0.95±0.05	4.0±0.10	0.95±0.05

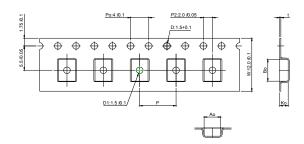
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#### ■Material of taping is plastic



Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
201212	2.10±0.10	1.28±0.10	1.28±0.10	4.0±0.10	0.22±0.05	1.0±0.10
<mark>321611</mark>	3.35±0.10	1.75±0.10	1.25±0.10	4.0±0.10	0.23±0.05	1.0±0.10
322513	3.42±0.10	2.77±0.10	1.55±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321609	3.40±0.10	1.77±0.10	1.04±0.10	4.0±0.10	0.22±0.05	1.0±0.10

#### 7-2.2 Tape Dimension / 12mm

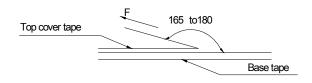


Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
451616	4.70±0.10	1.75±0.10	1.75±0.10	4.0±0.10	0.24±0.05	1.5±0.10
453215	4.70±0.10	3.45±0.10	1.60±0.10	8.0±0.10	0.24±0.05	1.5±0.10

#### 7-3. Packaging Quantity

Chip Size	453215	451616	322513	<mark>321611</mark>	321609	201212	201209	160808	100505	060303
Chip / Reel	1000	2000	2500	<mark>3000</mark>	3000	2000	4000	4000	10000	15000
Inner box	4000	8000	12500	<mark>15000</mark>	15000	10000	20000	20000	50000	75000
Middle box	20000	40000	62500	<mark>75000</mark>	75000	50000	100000	100000	250000	375000
Carton	40000	80000	125000	<mark>150000</mark>	150000	100000	200000	200000	500000	750000

#### 7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp.	oom Temp. Room Humidity		Tearing Speed	
(℃)	(%)	(hPa)	mm/min	
5~35	45~85	860~1060	300	

#### **Application Notice**

Storage Conditions(component level)

To maintain the solder ability of terminal electrodes:

- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
- 3. Recommended products should be used within 12 months from the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

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BLM21AG331BH1D BLM21PG300BH1D BLM21PG600BH1D BLM03HB401SZ1D BLM03HB401SN1D