Form No: XXXXXX-7-0000-PBM02-190701303

Specification for Approval

Date: 2019/07/31

Customer : 深圳臺慶

TAI-TECH P/N: HPC3010TF-330M

CUSTOMER P/N:

DESCRIPTION:

QUANTITY: pcs

REMARK:		
Customer Approval Feedback		

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Power Inductor HPC3010TF-330M

ECN HISTORY LIST

REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	19/07/31	新 發 行	羅宜春	梁周虎	張麗麗
備註					

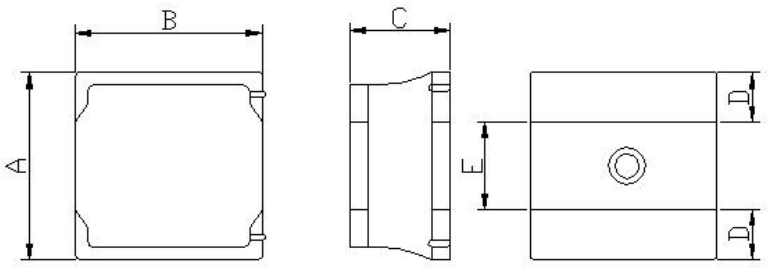
Power Inductor

HPC3010TF-330M

1. Features

1. This specification applies Low Profile Power Inductors.
2. 100% Lead(Pb) & Halogen-Free and RoHS compliant.
3. Operating temperature :-40~+125℃ (Including self - temperature rise)

2. Dimension



Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)
HPC3010TF	3.0±0.2	3.0±0.2	1.0max.	1.0 ref.	1.0 ref.

3. Part Numbering

HPC	3010	TF	-	330	M
A	B	C		D	E

A: Series
B: Dimension
C: Lead Free
D: Inductance 330=33.0uH
E: Inductance Tolerance M=±20%

4. Specification

TAI-TECH Part Number	Inductance (uH)	Tolerance (%)	Test Frequency (Hz)	DCR (Ω) ±20%	I sat (A) typ.	I sat (A)max.	I rms (A) typ.	I rms (A) max.
HPC3010TF -330M	33.0	±20%	0.1V/1M	1.25	0.40	0.35	0.50	0.40

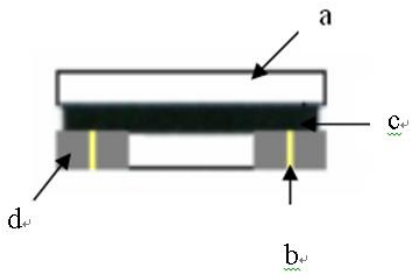
Note:

Isat：Saturation Current (Isat) will cause L0 to drop approximately 30%.

Irms：Heat Rated Current (Irms) will cause the coil temperature rise approximately Δt of 40℃

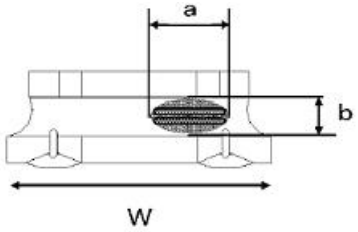
5. Material

No.	Description	Specification
a.	Core	Ferrite Core
b.	Wire	Enameled Copper Wire
c	Glue	Epoxy with magnetic powder
d	Terminal	Ag/Ni/Sn



Void appearance tolerance Limit

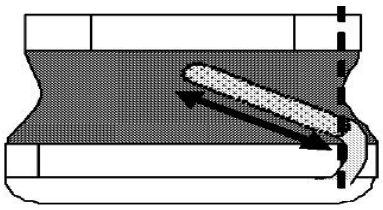
Size of voids occurring to coating resin is specified below.



Appearance of exposed wire tolerance limit:

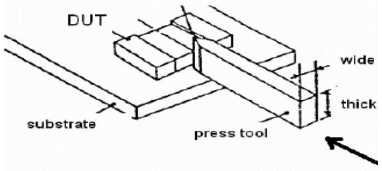
1. Width direction（dimension a）: Acceptable when a≦w/2
   Nonconforming when a＞w/2
2. Length direction（dimension b）: Dimension b is not specified.
3. The total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, and is acceptable.

External appearance criterion for exposed wire
Exposed end of the winding wire at the secondary side should be 2mm and below.



6. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125℃ (Including self - temperature rise)	
Storage temperature	1. -10~+40℃,50~60%RH (Product with taping) 2. -40~+125℃(on board)	
Electrical Performance Test		
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR		CH16502,Agilent33420A Micro-Ohm Meter.
Saturation Current (Isat)	Approximately△L30%	Saturation DC Current (Isat) will cause L0 to drop △L(%)(keep quickly).
Heat Rated Current (Irms)	Approximately △T40℃	Heat Rated Current (Irms) will cause the coil temperature rise △T(℃) without core loss. 1.Applied the allowed DC current(keep 1 min.). 2.Temperature measured by digital surface thermometer
Reliability Test		
Life Test	Appearance：No damage. Inductance：within±10% of initial value Q：Shall not exceed the specification value. RDC：within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles) Temperature： 125±2℃ Applied current：rated current Duration：1000±12hrs Measured at room temperature after placing for 24±2 hrs
Load Humidity		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Humidity：85±2％R.H, Temperature：85℃±2℃ Duration：1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs
Moisture Resistance		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles 1. Baked at50℃ for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to 65±2℃ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25℃ in 2.5hrs. 3. Raise temperature to 65±2℃ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25℃ in 2.5hrs,keep at 25℃ for 2 hrs then keep at -10℃ for 3 hrs 4. Keep at 25 ℃ 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.
Thermal shock		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Condition for 1 cycle Step1：-40±2℃ 30±5min Step2：25±2℃ ≦0.5min Step3：125±2℃ 30±5min Number of cycles： 500 Measured at room temperature after placing for 24±2 hrs
Vibration		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDECJ-STD-020DClassification Reflow Profiles) Oscillation Frequency: 10Hz～2KHz～10Hz for 20 minutes Equipment： Vibration checker Total Amplitude:10g Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations)。

<table>
<tr><th>Item</th><th>Performance</th><th>Test Condition</th></tr>
<tr><td>Bending</td><td rowspan="2">Appearance：No damage.
Impedance：within±15% of initial value
Inductance：within±10% of initial value
Q：Shall not exceed the specification value.
RDC：within ±15% of initial value and shall not exceed the specification value</td><td>Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm
<0805 inch(2012mm):40x100x0.8mm
Bending depth: >=0805 inch(2012mm):1.2mm
<0805 inch(2012mm):0.8mm
duration of 10 sec.</td></tr>
<tr><td>Shock</td><td><table><tr><th>Type</th><th>Peak value (g's)</th><th>Normal duration (D) (ms)</th><th>Wave form</th><th>Velocity change (Vi)ft/sec</th></tr><tr><td>SMD</td><td>50</td><td>11</td><td>Half-sine</td><td>11.3</td></tr><tr><td>Lead</td><td>50</td><td>11</td><td>Half-sine</td><td>11.3</td></tr></table></td></tr>
<tr><td>Solder ability</td><td>More than 95% of the terminal electrode should be covered with solder。</td><td>Preheat: 150℃,60sec.。
Solder: Sn96.5% Ag3% Cu0.5%
Temperature: 245±5℃。
Flux for lead free: Rosin. 9.5%。
Dip time: 4±1sec。
Depth: completely cover the termination</td></tr>
<tr><td>Resistance to Soldering Heat</td><td rowspan="2">Appearance：No damage.
Impedance：within±15% of initial value
Inductance：within±10% of initial value
Q：Shall not exceed the specification value.
RDC：within ±15% of initial value and shall not exceed the specification value e</td><td>Depth: completely cover the termination
<table><tr><th>Temperature(°C)</th><th>Time(s)</th><th>Temperature ramp/immersion and emersion rate</th><th>Number of heat cycles</th></tr><tr><td>260 ±5 (solder temp)</td><td>10 ±1</td><td>25mm/s ±6 mm/s</td><td>1</td></tr></table></td></tr>
<tr><td>Terminal Strength</td><td>Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles
With the component mounted on a PCB with the device to be tested, apply a force(>0805:1kg , <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.
 
</td></tr>
</table>

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

7. Soldering and Mounting

7-1. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

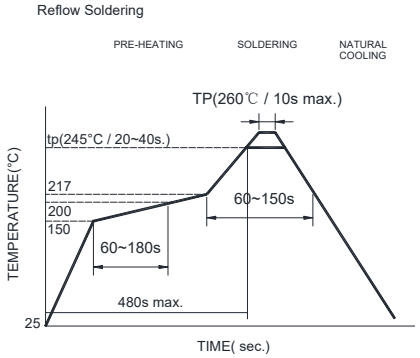
7-1.1 Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

7-1.2 Soldering Iron(Figure 2):

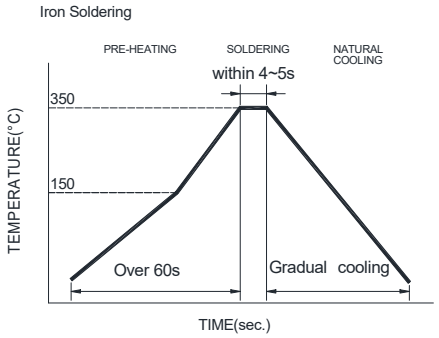
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

- Preheat circuit and products to 150℃
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- 355℃ tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5 sec.



Reflow times: 3 times max.

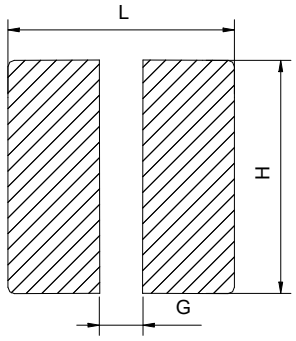
Fig.1



Iron Soldering times: 1 times max.

Fig.2

7-2. Recommended PC Board Pattern

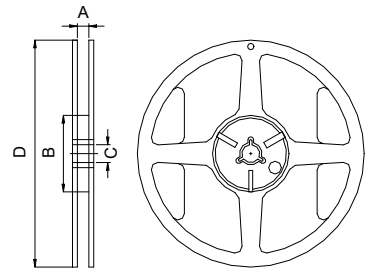


L(mm)	G(mm)	H(mm)
3.2	1.0	3.2

PC board should be designed so that products can prevent damage from mechanical stress when warping the board. Products shall be positioned in the sideway direction against the mechanical stress to prevent failure.

8. Packaging Information

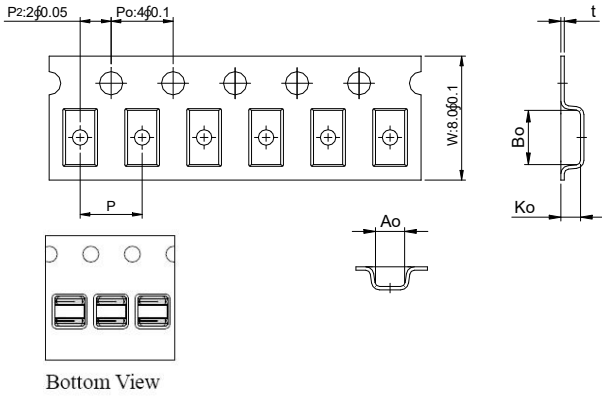
8-1. Reel Dimension



7"x8mm

Type	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	8.4±1.0	50 min.	13±0.8	178±2

8-2. Tape Dimension / 8mm



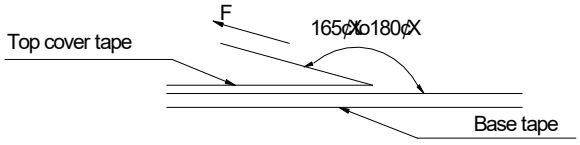
Bottom View

Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
HPC	3010	3.2±0.05	3.2±0.05	1.20±0.2	4.0±0.05	0.23±0.05

8-3. Packaging Quantity

Chip size	3010
Chip / Reel	2000

8-4. Tearing Off Force



The force for tearing off cover tape is 10 to 100 grams in the arrow direction under the following conditions.

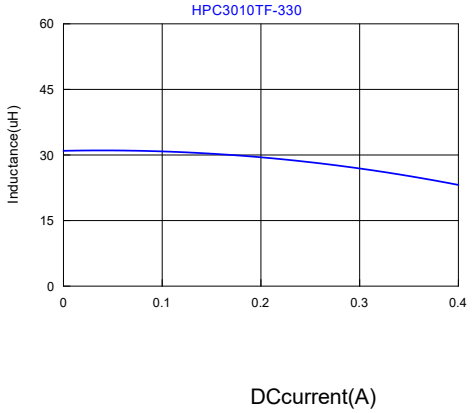
Room Temp. (℃)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

Application Notice

· Storage Conditions(component level)
To maintain the solderability of terminal electrodes:
1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
2. Temperature and humidity conditions: Less than 40℃ and 60% RH.
3. Recommended products should be used within 12 months form the time of delivery.
4. The packaging material should be kept where no chlorine or sulfur exists in the air.

· Transportation
1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

9.Typical Performance Curves



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