

Sealed Choke Coil PST041H type

■ Features

Low profile : 3.8mm x 3.8mm x 1.8mm

Low coil resistance with large currents.

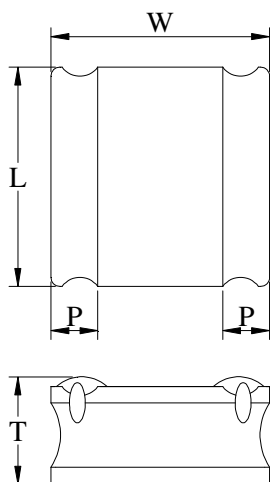
High magnetic shield construction should actualize high resolution for EMC protection.

100% lead (Pb) free meet RoHS standard

■ Application

Cellular phones, LCD displays, HDDs, DVCs, DSCs, PDAs etc..

■ Outline Dimensions



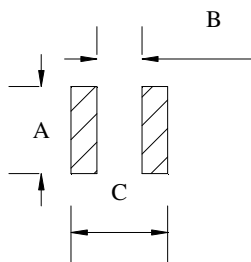
Code	Dimensions (mm)
L	3.8 ± 0.2*
W	3.8 ± 0.2*
T	1.8 Max.*
P	1.0 ± 0.2

* Dimensions are of the case not including the termination. For maximum overall dimensions including the termination, add 0.1mm.

Note : This graph is in regard to outline dimensions spec. For outer appearance, please refer to actual product.

■ Recommend Land Pattern Dimensions

The customer shall determine the land dimensions shown above after confirming and safety.



A	3.8
B	1.8
C	3.8

Unit : mm

■ Specifications

Part Number	L0 Inductance (μH) @ (0A)	R_{dc} ($\text{m}\Omega$)		Heat Rating Current DC Amps. I_{dc} (A)		Saturation Current DC Amps. I_{sat} (A)	
		Typical	Maximum	Typical	Maximum	Typical	Maximum
PST041H-R56MS	0.56	17	22	5.4	4.86	5.5	4.95
PST041H-1R0MS	1.0	20	25	3.8	3.42	3.8	3.42
PST041H-1R2MS	1.2	25	30	3.6	3.24	3.6	3.24
PST041H-1R5MS	1.5	33	40	3.2	2.88	3.5	3.15
PST041H-1R8MS	1.8	34	41	3.1	2.79	3.1	2.79
PST041H-2R2MS	2.2	35	45	3.0	2.70	3.0	2.70
PST041H-3R3MS	3.3	45	56	2.7	2.43	2.4	2.16
PST041H-4R7MS	4.7	70	90	2.2	1.98	2.0	1.80
PST041H-6R8MS	6.8	90	115	1.9	1.71	1.7	1.53
PST041H-8R2MS	8.2	105	132	1.5	1.35	1.6	1.44
PST041H-100MS	10.0	135	170	1.4	1.26	1.55	1.40
PST041H-150MS	15.0	185	222	1.25	1.12	1.0	0.90
PST041H-220MS	22.0	250	315	1.2	1.08	0.83	0.74
PST041H-330MS	33.0	405	486	0.9	0.81	0.68	0.61
PST041H-470MS	47.0	495	594	0.8	0.72	0.56	0.50
PST041H-680MS	68.0	885	1,062	0.58	0.52	0.48	0.43
PST041H-101MS	100.0	1,545	1,854	0.42	0.37	0.45	0.40
PST041H-221MS	220.0	3,150	3,780	0.30	0.27	0.33	0.30
PST041H-331MS	330.0	4,200	5,040	0.27	0.24	0.25	0.22

* : If you require another part number please contact with us.

** : Inductance Tolerance $\pm 20\%$

Note 1. : All test data is referenced to 25°C ambient.

Note 2. : Test Condition:100KHz, 1.0Vrms

Note 3. : I_{dc} : DC current (A) that will cause an approximate ΔT of 40°C

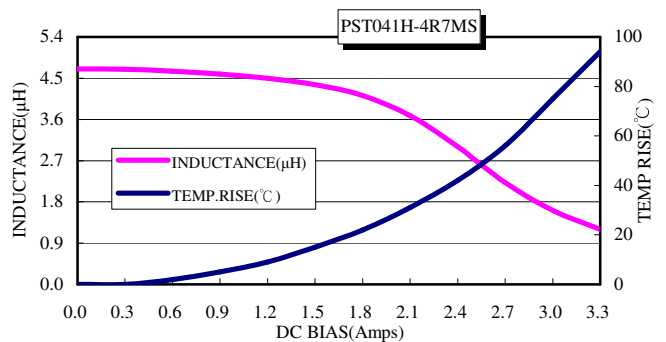
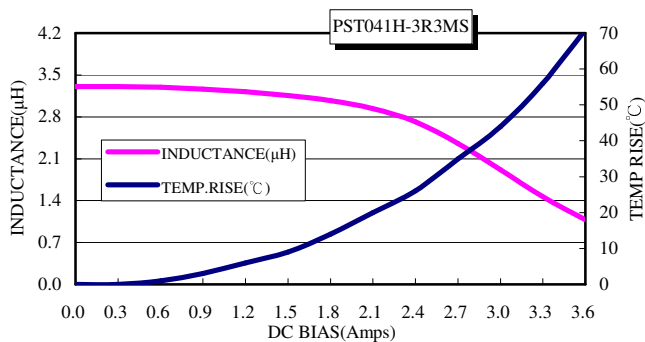
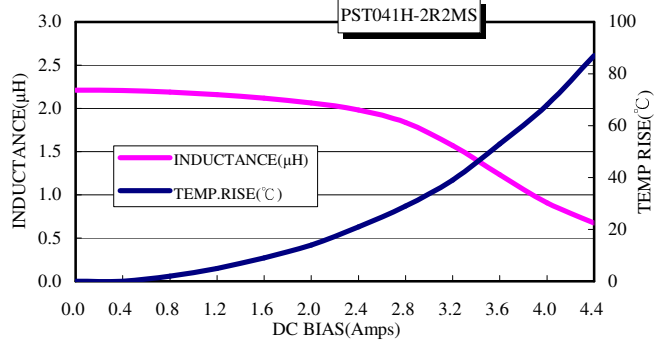
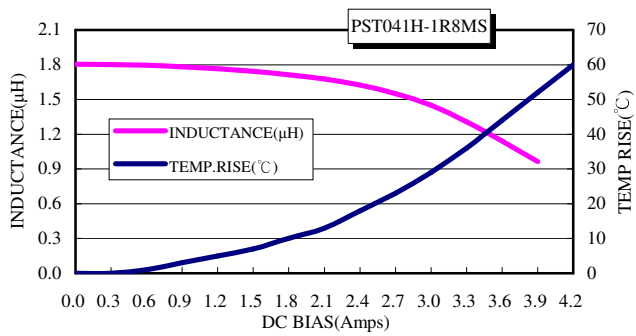
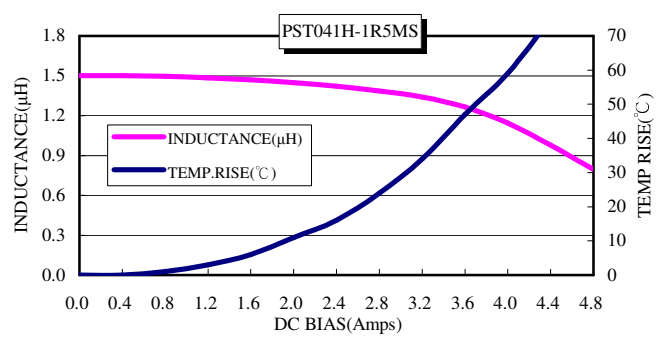
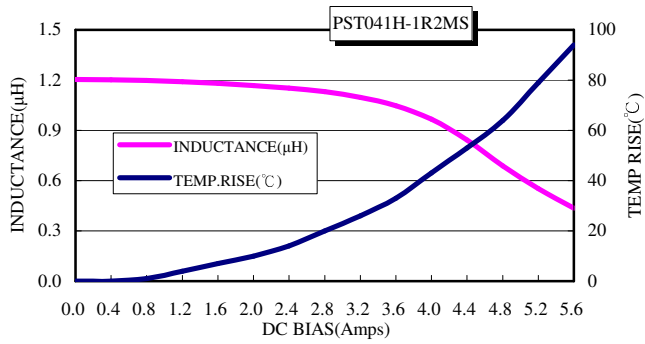
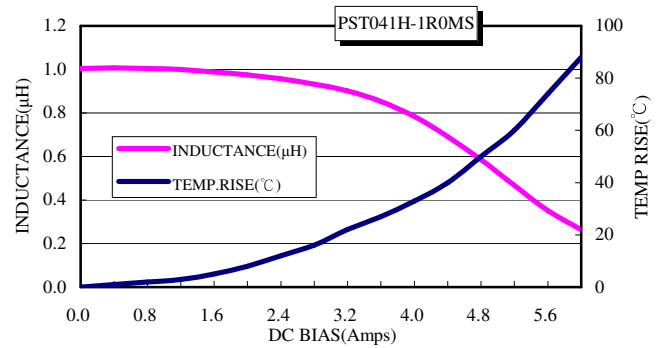
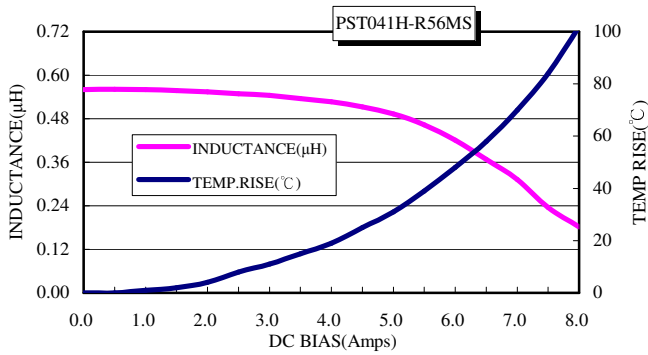
Note 4. : I_{sat} : DC current (A) that will cause L_0 to drop approximately 30%

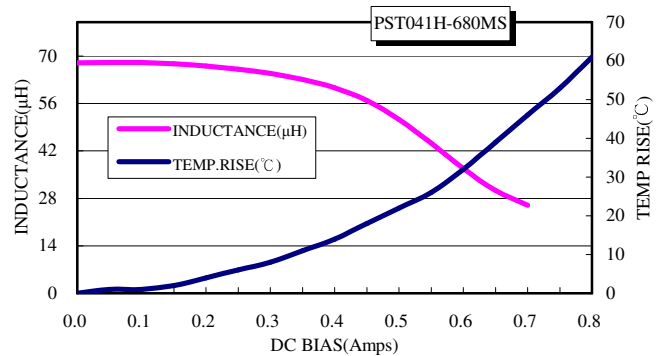
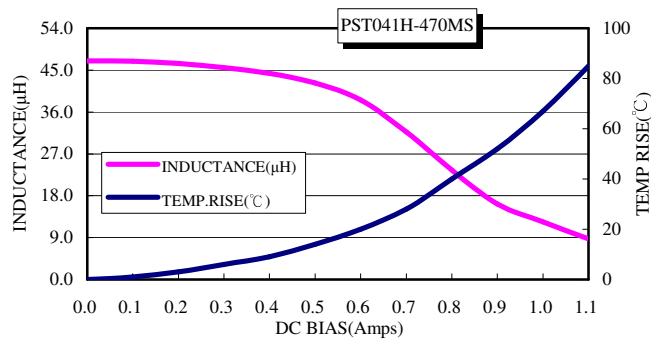
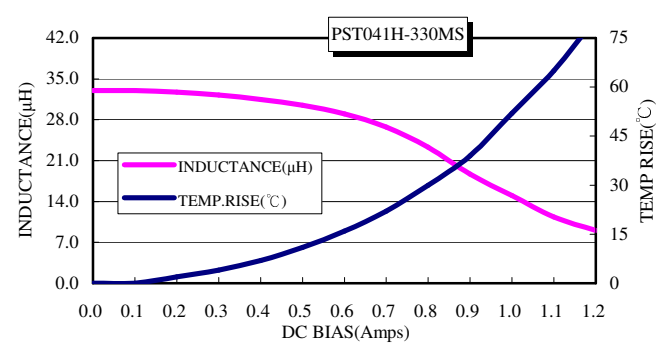
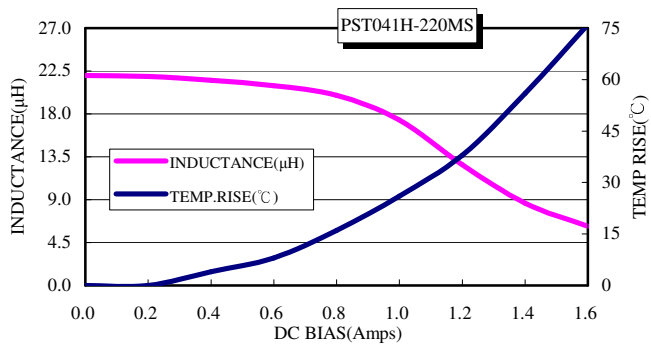
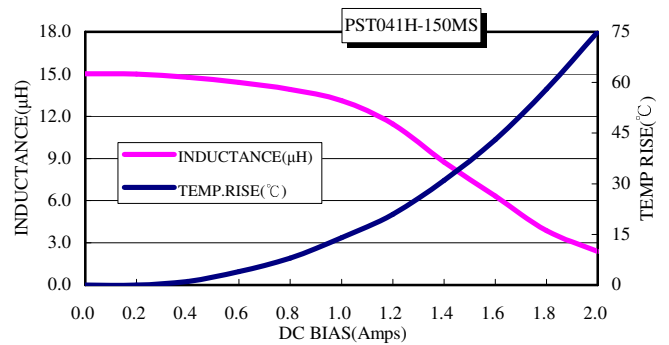
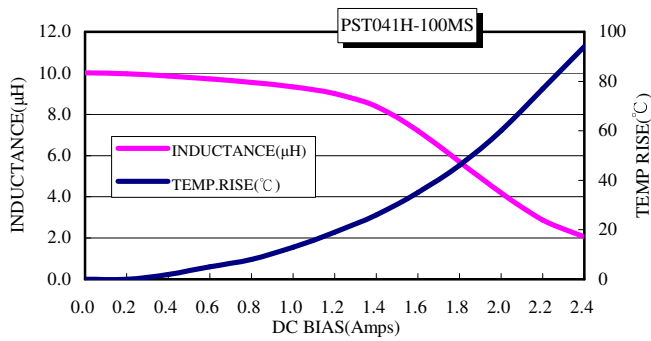
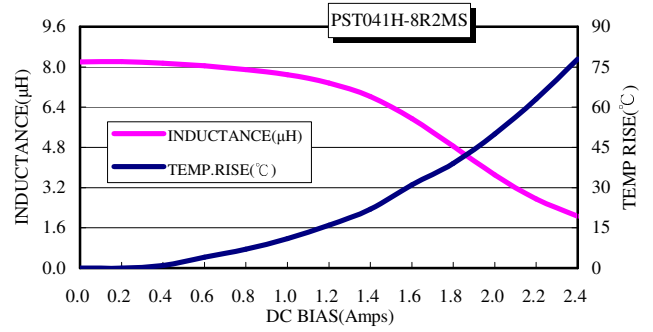
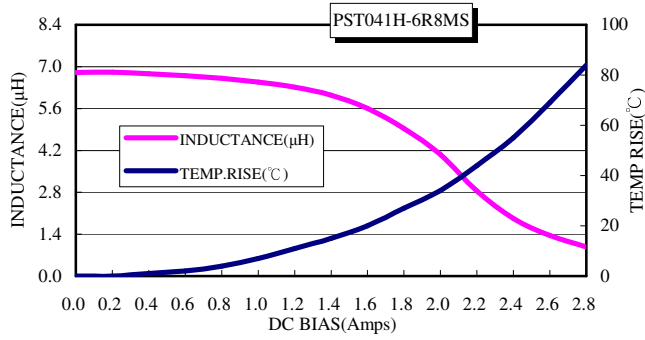
Note 5. : Operating Temperature Range -55°C to + 125°C

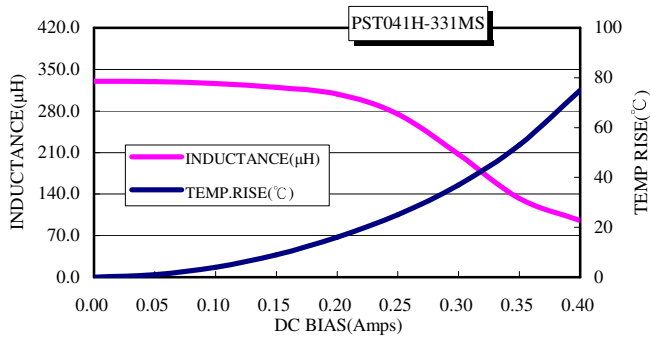
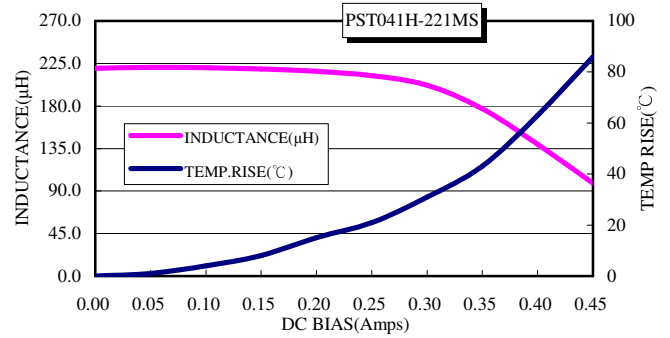
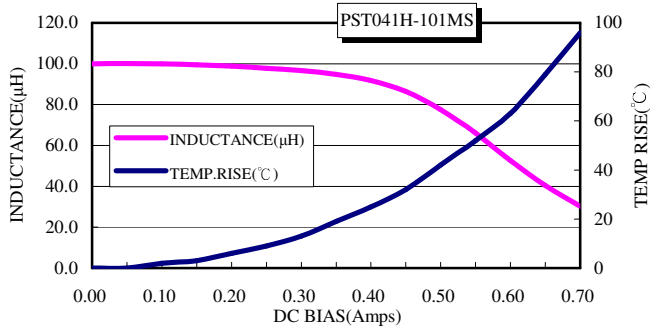
Note 6. : The part temperature (ambient + temp rise) should not exceed 125°C under worse case operating conditions. Circuit design , component placement, PWB trace size and thickness, airflow and other cooling provision all affect the part temperature. Part temperature should be verified in the end application.

Note 7. : The rated current as listed is either the saturation current or the heating current depending on which value is lower.

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