## Features

- Epitaxial Planar Die Construction
- Complementary PNP Types Available (DDA)
- Built-In Biasing Resistors
- Totally Lead-Free \& Fully RoHS Compliant (Notes 1 \& 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

| $\mathbf{P / N}$ | R1 | R2 | MARKING |
| :---: | :---: | :---: | :---: |
| DDC124EH | $22 \mathrm{~K} \Omega$ | $22 \mathrm{~K} \Omega$ | N 17 |
| DDC144EH | $47 \mathrm{~K} \Omega$ | $47 \mathrm{~K} \Omega$ | N 20 |
| DDC143EH | $4.7 \mathrm{~K} \Omega$ | $4.7 \mathrm{~K} \Omega$ | N 08 |
| DDC114YH | $10 \mathrm{~K} \Omega$ | $47 \mathrm{~K} \Omega$ | N 14 |
| DDC123JH | $2.2 \mathrm{~K} \Omega$ | $47 \mathrm{~K} \Omega$ | N 06 |
| DDC114EH | $10 \mathrm{~K} \Omega$ | $10 \mathrm{~K} \Omega$ | N 13 |
| DDC143TH | $4.7 \mathrm{~K} \Omega$ | - | N 07 |
| DDC114TH | $10 \mathrm{~K} \Omega$ | - | N 12 |

## Mechanical Data

- Case: SOT-563
- Case Material: Molded Plastic; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e3)
- Terminal Connections: See Diagram
- Weight: 0.005 grams (Approximate)

$R_{1}, R_{2}$

$R_{1}$ Only


## Ordering Information (Note 4)

|  | Device | Packaging | Shipping |
| :---: | :---: | :---: | :---: |
|  | DDC124EH-7 | SOT-563 | 3,000/Tape \& Reel |
|  | DDC144EH-7 | SOT-563 | 3,000/Tape \& Reel |
|  | DDC143EH-7 | SOT-563 | 3,000/Tape \& Reel |
|  | DDC114YH-7 | SOT-563 | 3,000/Tape \& Reel |
|  | DDC123JH-7 | SOT-563 | 3,000/Tape \& Reel |
|  | DDC114EH-7 | SOT-563 | 3,000/Tape \& Reel |
|  | DDC143TH-7 | SOT-563 | 3,000/Tape \& Reel |
|  | DDC114TH-7 | SOT-563 | 3,000/Tape \& Reel |
| Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) \& 2011/65/EU (RoHS 2) compliant. <br> 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. <br> 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total $\mathrm{Br}+\mathrm{Cl}$ ) and <1000ppm antimony compounds. <br> 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html. | 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) \& 2011/65/EU (RoHS 2) compliant. <br> 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. <br> 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine ( $<1500 \mathrm{ppm}$ total $\mathrm{Br}+\mathrm{Cl}$ ) and <1000ppm antimony compounds. <br> 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html. |  |  |

Marking Information


| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | O | N | D |

Maximum Ratings $@_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise specified.)

| Characteristic |  | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Supply Voltage |  | $\mathrm{V}_{\mathrm{Cc}}$ | 50 | V |
| Input Voltage | DDC124EH DDC144EH DDC143EH DDC114YH DDC123JH DDC114EH DDC143TH DDC114TH | VIN | $\begin{aligned} & -10 \text { to }+40 \\ & -10 \text { to }+40 \\ & -10 \text { to }+30 \\ & -6 \text { to }+40 \\ & -5 \text { to }+12 \\ & -10 \text { to }+40 \\ & -5 \mathrm{~V} \max \\ & -5 \mathrm{~V} \max \end{aligned}$ | V |
| Output Current | DDC124EH DDC144EH DDC143EH DDC114YH DDC123JH DDC114EH DDC143TH DDC114TH | lo | 30 30 100 70 100 50 100 100 | mA |
| Output Current | All | Ic (Max) | 100 | mA |
| Power Dissipation |  | $\mathrm{Pd}_{\mathrm{d}}$ | 150 | mW |
| Thermal Resistance, Junction to Ambient Air | (Note 5) | $\mathrm{R}_{\theta \mathrm{J} \mathrm{A}}$ | 833 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Operating and Storage Temperature Range |  | $\mathrm{T}_{\mathrm{j}, \mathrm{T}} \mathrm{TSTG}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

Note: 5. Mounted on FR4 Board with recommended pad layout at http://www.diodes.com/datasheets/ap02001.pdf.

DDC (XXXX) H

Electrical Characteristics ( $@ \mathrm{~T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise specified.)

| Characteristic (DDC143TH \& DDC114TH only) | Symbol | Min | Typ | Max | Unit | Test Condition |
| :--- | :---: | :---: | :---: | :---: | :---: | :--- |
| Collector-Base Breakdown Voltage | $\mathrm{BV}_{\mathrm{CBO}}$ | 50 | - | - | V | $\mathrm{I}_{\mathrm{C}}=50 \mu \mathrm{~A}$ |
| Collector-Emitter Breakdown Voltage | $\mathrm{BV}_{\mathrm{CEO}}$ | 50 | - | - | V | $\mathrm{I}_{\mathrm{C}}=1 \mathrm{~mA}$ |
| Emitter-Base Breakdown Voltage | $\mathrm{BV}_{\mathrm{EBO}}$ | 5 | - | - | V | $\mathrm{I}_{\mathrm{E}}=50 \mu \mathrm{~A}$ |
| Collector Cut-Off Current | $\mathrm{I}_{\mathrm{CBO}}$ | - | - | 0.5 | $\mu \mathrm{~A}$ | $\mathrm{~V}_{\mathrm{CB}}=50 \mathrm{~V}$ |
| Emitter Cut-Off Current | $\mathrm{I}_{\mathrm{EBO}}$ | - | - | 0.5 | $\mu \mathrm{~A}$ | $\mathrm{~V}_{\mathrm{EB}}=4 \mathrm{~V}$ |
| Collector-Emitter Saturation Voltage | $\mathrm{V}_{\mathrm{CE} \text { (sat) }}$ | - | - | 0.3 | V | $\mathrm{I}_{\mathrm{I}} / \mathrm{I}_{\mathrm{B}}=2.5 \mathrm{~mA} / 0.25 \mathrm{~mA}$ <br> $\mathrm{I}_{\mathrm{C}} / \mathrm{I}_{\mathrm{B}}=1 \mathrm{~mA} / 0.1 \mathrm{~mA}$ |
| DDC143TH |  |  |  |  |  |  |
| DDC114TH |  |  |  |  |  |  |$|$


| Characteristic |  | Symbol | Min | Typ | Max | Unit | Test Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input Voltage | DDC124EH DDC144EH DDC143EH DDC114YH DDC123JH DDC114EH | $\mathrm{V}_{1 \text { (off) }}$ | $\begin{aligned} & 0.5 \\ & 0.5 \\ & 0.5 \\ & 0.3 \\ & 0.5 \\ & 0.5 \end{aligned}$ | $\begin{aligned} & \hline 1.1 \\ & 1.1 \\ & 1.1 \\ & - \\ & \hline 1.1 \end{aligned}$ | - | V | $\mathrm{V} C \mathrm{C}=5 \mathrm{~V}, \mathrm{lo}=100 \mu \mathrm{~A}$ |
|  | DDC124EH <br> DDC144EH <br> DDC143EH <br> DDC114YH <br> DDC123JH <br> DDC114EH | $V_{\text {I(on) }}$ | - | $\begin{aligned} & \hline 1.9 \\ & 1.9 \\ & 1.9 \\ & - \\ & \hline 1.9 \end{aligned}$ | $\begin{aligned} & 3.0 \\ & 3.0 \\ & 3.0 \\ & 1.4 \\ & 1.1 \\ & 3.0 \end{aligned}$ |  | $\begin{aligned} & \hline \mathrm{V}_{\mathrm{O}}=0.3 \mathrm{~V}, \mathrm{IO}=5 \mathrm{~mA} \\ & \mathrm{~V}_{\mathrm{O}}=0.3 \mathrm{~V}, \mathrm{I}=2 \mathrm{~mA} \\ & \mathrm{~V}_{\mathrm{O}}=0.3 \mathrm{~V}, \mathrm{I}=20 \mathrm{~mA} \\ & \mathrm{~V}_{\mathrm{O}}=0.3 \mathrm{~V}, \mathrm{I}=1 \mathrm{~mA} \\ & \mathrm{~V}_{\mathrm{O}}=0.3 \mathrm{~V}, \mathrm{I}=5 \mathrm{~mA} \\ & \mathrm{~V}_{\mathrm{O}}=0.3 \mathrm{~V}, \mathrm{I}=10 \mathrm{~mA} \end{aligned}$ |
| Output Voltage | DDC124EH DDC144EH DDC143EH DDC114YH DDC123JH DDC114EH | $\mathrm{V}_{\text {O(on) }}$ | - | 0.1 | 0.3 | V | $\begin{aligned} & \mathrm{IO} / \mathrm{I}=10 \mathrm{~mA} / 0.5 \mathrm{~mA} \\ & \mathrm{I} / \mathrm{I}=10 \mathrm{~mA} / 0.5 \mathrm{~mA} \\ & \mathrm{I} / \mathrm{I}=10 \mathrm{~mA} / 0.5 \mathrm{~mA} \\ & \mathrm{I} / \mathrm{I}=5 \mathrm{~mA} / 0.25 \mathrm{~mA} \\ & \mathrm{I} / \mathrm{I}=5 \mathrm{~mA} / 0.25 \mathrm{~mA} \\ & \mathrm{I} / \mathrm{II}=10 \mathrm{~mA} / 0.5 \mathrm{~mA} \end{aligned}$ |
| Input Current | DDC124EH DDC144EH DDC143EH DDC114YH DDC123JH DDC114EH | $\\|$ | - | - | $\begin{gathered} \hline 0.36 \\ 0.18 \\ 1.8 \\ 0.88 \\ 3.6 \\ 0.88 \\ \hline \end{gathered}$ | mA | $\mathrm{V}_{1}=5 \mathrm{~V}$ |
| Output Current |  | 10 (off) | - | - | 0.5 | $\mu \mathrm{A}$ | $V_{C C}=50 \mathrm{~V}, \mathrm{~V}_{1}=0 \mathrm{~V}$ |
| DC Current Gain | DDC124EH DDC144EH DDC143EH DDC114YH DDC123JH DDC114EH | G\| | $\begin{aligned} & 56 \\ & 68 \\ & 20 \\ & 68 \\ & 80 \\ & 30 \end{aligned}$ | - | - | - | $\begin{aligned} & \mathrm{V}_{\mathrm{O}}=5 \mathrm{~V}, I_{\mathrm{O}}=5 \mathrm{~mA} \\ & \mathrm{~V}_{\mathrm{O}}=5 \mathrm{~V}, I_{\mathrm{O}}=5 \mathrm{~mA} \\ & \mathrm{~V}_{\mathrm{O}}=5 \mathrm{~V}, I_{\mathrm{O}}=10 \mathrm{~mA} \\ & \mathrm{~V}_{\mathrm{O}}=5 \mathrm{~V}, I_{\mathrm{O}}=10 \mathrm{~mA} \\ & \mathrm{~V}_{\mathrm{O}}=5 \mathrm{~V}, I_{O}=10 \mathrm{~mA} \\ & \mathrm{~V}_{\mathrm{O}}=5 \mathrm{~V}, I_{\mathrm{O}}=5 \mathrm{~mA} \end{aligned}$ |
| Gain-Bandwidth Product* |  | $\mathrm{f}_{T}$ | - | 250 | - | MHz | $\mathrm{V}_{\mathrm{CE}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{E}}=5 \mathrm{~mA}, \mathrm{f}=100 \mathrm{MHz}$ |

[^0]DDC (XXXX) H

## Typical Curves - DDC143EH



Fig. 1 Derating Curve


Fig. 3 DC Current Gain


Fig. 5 Collector Current vs. Input Voltage


Fig. $2 \mathrm{~V}_{\mathrm{CE}(\mathrm{SAT})} \mathrm{vs}$. $\mathrm{I}_{\mathrm{C}}$


Fig. 4 Output Capacitance


Fig. 6 Input Voltage vs. Collector Current

## Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.


| SOT563 |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Dim | Min | Max | Typ |  |  |  |
| A | 0.15 | 0.30 | 0.20 |  |  |  |
| B | 1.10 | 1.25 | 1.20 |  |  |  |
| C | 1.55 | 1.70 | 1.60 |  |  |  |
| D | - | - | 0.50 |  |  |  |
| G | 0.90 | 1.10 | 1.00 |  |  |  |
| H | 1.50 | 1.70 | 1.60 |  |  |  |
| K | 0.55 | 0.60 | 0.60 |  |  |  |
| L | 0.10 | 0.30 | 0.20 |  |  |  |
| M | 0.10 | 0.18 |  |  |  | 0.11 |
| All Dimensions in | $\mathbf{~ m m}$ |  |  |  |  |  |

## Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.


| Dimensions | Value (in mm) |
| :---: | :---: |
| $\mathbf{Z}$ | 2.2 |
| $\mathbf{G}$ | 1.2 |
| $\mathbf{X}$ | 0.375 |
| $\mathbf{Y}$ | 0.5 |
| $\mathbf{C 1}$ | 1.7 |
| $\mathbf{C 2}$ | 0.5 |

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[^0]:    * Transistor - For Reference Only

