V _{RRM}	=	200 V
I _{FAVM}	=	7110 A
I _{FRMS}	=	11200 A
I _{FSM}	=	55000 A
V_{F0}	=	0.74 V
r _F	=	0.026 mW

Rectifier Diode **5SDD 71X0200**

Doc. No. 5SYA1156-01 July 06

- Optimized for high current rectifiers
- Very low on-state voltage
- Very low thermal resistance

Blocking

V _{RRM}	Repetitive peak reverse voltage	200 V	Half sine wave, $t_P = 10 \text{ ms}$, $f = 50 \text{ Hz}$	
V _{RSM}	Maximum peak reverse voltage	300 V	Half sine wave, t _P = 10 ms	
I _{RRM}	Repetitive peak reverse current	\leq 50 mA	$T_j = 170 \ ^{\circ}C$ $V_R = V_{RRM}$	

Mechanical

Fм	Mounting force	min.	20	kN
		max.	24	kN
а	Acceleration:			
	Device unclamped		50	m/s²
	Device clamped		200	m/s ²
m	Weight		0.14	kg
Ds	Surface creepage distance		4	mm
Da	Air strike distance		4	mm

Fig. 1 Outline drawing. All dimensions are in millimeters and represent nominal values unless stated otherwise.

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On-state

I _{FAVM}	Max. average on-state current	7110 A	Half sine wave, $T_c =$	85 °C
I _{FRMS}	Max. RMS on-state current	11200 A		
I _{FSM}	Max. peak non-repetitive surge current	55000 A	t _p = 10 ms	Before surge
		60000 A	t _p = 8.3 ms	T _j = 170 °C
∫l²dt	Max. surge current integral	15100 kA ² s	$t_p = 10 \text{ ms}$	After surge:
		15000 kA ² s	t _p = 8.3 ms	$V_R \approx 0V$
$V_{F\text{max}}$	Maximum on-state voltage	≤ 1.05 V	I _F = 5000 A	T _j = 25 °C
V _{F0}	Threshold voltage	0.74 V	Approximation for	T _j = 170 °C
r _F	Slope resistance	0.026 mΩ	I _F = 5 - 15 kA	

Thermal characteristics

Tj	Operating junction temperature range	-40.	170 °C		
T _{stg}	Storage temperature range	-40.	170 °C		
R _{th(j-c)}	Thermal resistance	≤	20 K/kW	Anode side cooled	
	junction to case	≤	20 K/kW	Cathode side cooled	
		≤	10 K/kW	Double side cooled	F _M = 20…24 kN
R _{th(c-h)}	Thermal resistance case to heatsink	≤	10 K/kW	Single side cooled	
		≤	5 K/kW	Double side cooled	

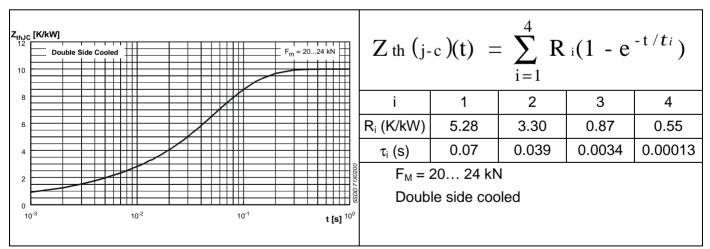
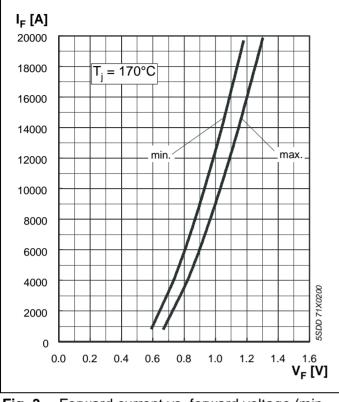
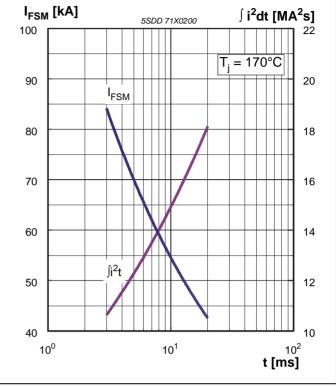


Fig. 2 Transient thermal impedance (junction-to-case) vs. time in analytical and graphical forms.



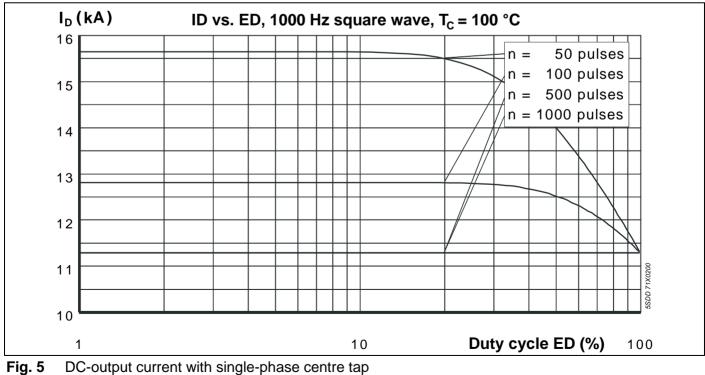
On-state characteristics

Fig. 3 Forward current vs. forward voltage (min. and max. values).



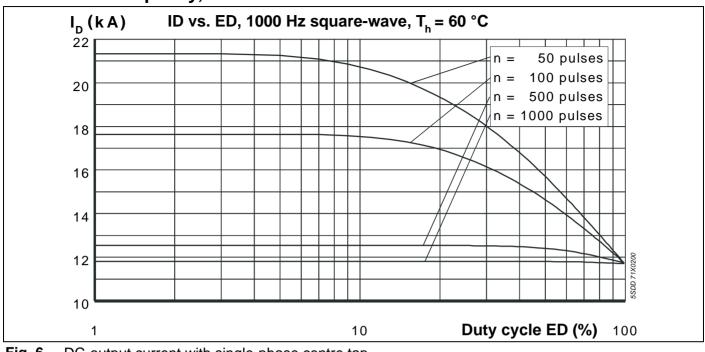
Surge current characteristics

Fig. 4 Surge current and fusing integral vs. pulse width (max. values) for non-repetitive, half-sinusoidal surge current pulses.

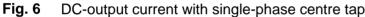


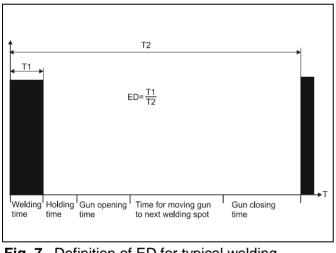
Current load capability

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Current load capacity, cont.





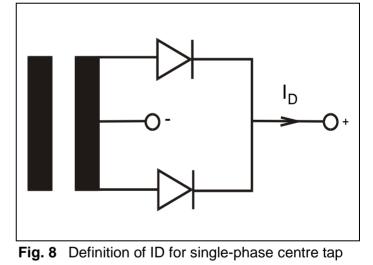


Fig. 7 Definition of ED for typical welding sequence

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