





### **1** Product profile

#### 1.1 General description

Two planar PIN diodes in an SOT23 small SMD plastic package.

#### **1.2 Features and benefits**

- Two elements in common cathode configuration
- High voltage, current controlled
- RF resistor for RF switches
- Low diode capacitance
- Low diode forward resistance (low loss)
- AEC-Q101 qualified

#### 1.3 Applications

- RF attenuators and switches
- Bandswitch for TV tuners
- · Series diode for mobile communication transmit/receive switch



### 2 Pinning information

Table '	1. Discrete pinning				
Pin	Description	Simplified outline	Graphic symbol		
1	anode (a <sub>1</sub> )				
2	anode (a <sub>2</sub> )		3		
3	common cathode	Top view	1 2 aaa-029921		

# **3** Ordering information

Table 2. Ordering information							
Type number	Package	ackage					
	Name	Description	Version				
BAP65-05	-	plastic surface-mounted package; 3 leads	SOT023				

### 4 Marking

Table 3. Marking					
	Type number	Marking code			
	BAP65-05	7K%			

### 5 Limiting values

#### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Мах	Unit
V <sub>R</sub>	continuous reverse voltage		-	30	V
l <sub>F</sub>	continuous forward current		-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>sp</sub> ≤ 90 °C	-	250	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		-65	+150	°C

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### **6** Thermal characteristics

Table 5. Th	nermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point		220	K/W

### 7 Characteristics

#### Table 6. Characteristics

 $T_i = 25$  °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit	
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 50 mA		-	0.9	1.1	V	
I <sub>R</sub>	reverse leakage current	V <sub>R</sub> = 20 V		-	-	20	nA	
C <sub>d</sub>	diode capacitance	f = 1 MHz (see Figure 1)			I			
		V <sub>R</sub> = 0 V		-	0.7	-	pF	
		V <sub>R</sub> = 1 V		-	0.575	0.9	pF	
		V <sub>R</sub> = 3 V		-	0.525	0.8	pF	
		V <sub>R</sub> = 20 V		-	0.425	-	pF	
ſD	diode forward resistance	f = 100 MHz (see Figure 2)						
		I <sub>F</sub> = 1 mA		-	1	-	Ω	
		I <sub>F</sub> = 5 mA	[1]	-	0.65	0.95	Ω	
		I <sub>F</sub> = 10 mA	[1]	-	0.56	0.9	Ω	
		I <sub>F</sub> = 100 mA		-	0.35	-	Ω	
ISL	isolation	V <sub>R</sub> = 0 V (see <u>Figure 4</u> )		1				
		f = 900 MHz		-	9.4	-	dB	
		f = 1800 MHz		-	4.8	-	dB	
		f = 2450 MHz		-	3.1	-	dB	
L <sub>ins</sub>	insertion loss	See Figure 3.						
		I <sub>F</sub> = 1 mA						
		f = 900 MHz		-	0.1	-	dB	
		f = 1800 MHz		-	0.18	-	dB	
		f = 2450 MHz		-	0.28	-	dB	
		I <sub>F</sub> = 5 mA						
		f = 900 MHz		-	0.08	-	dB	
		f = 1800 MHz		-	0.16	-	dB	
		f = 2450 MHz		-	0.26	-	dB	
		I <sub>F</sub> = 10 mA						
		f = 900 MHz		-	0.07	-	dB	
		f = 1800 MHz		-	0.15	-	dB	
		f = 2450 MHz		-	0.25	-	dB	
L <sub>ins</sub>	insertion loss	I <sub>F</sub> = 100 mA						
		f = 900 MHz		-	0.06	-	dB	
		f = 1800 MHz		-	0.14	-	dB	
		f = 2450 MHz		-	0.24	-	dB	
				1		1		

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BAP65-05

Silicon PIN diode

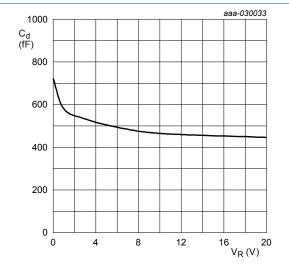
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
τι		when switched from $I_F = 10 \text{ mA}$ to $I_R = 6 \text{ mA}$ ; $R_L = 100 \Omega$ ; measured at $I_R = 3 \text{ mA}$	-	0.17	-	μs
L <sub>S</sub>	series inductance	I <sub>F</sub> = 100 mA; f = 100 MHz	-	1.4	-	nH

[1] Guaranteed on AQL basis; inspection level S4, AQL 1.0

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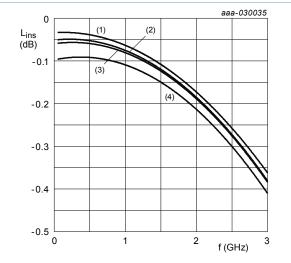
BAP65-05 Silicon PIN diode

### 8 Graphical data



f = 1 MHz; T<sub>i</sub> = 25 °C.

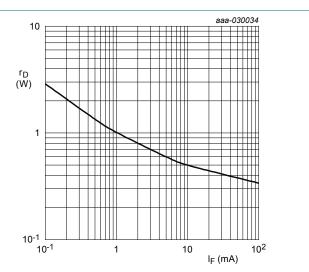
Figure 1. Diode capacitance as a function of reverse voltage (typical values)



Diode inserted in series with a 50  $\Omega$  strip line circuit and biased via the analyzer T-network. T<sub>amb</sub> = 25 °C.

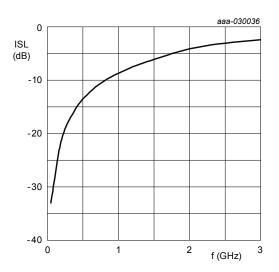
- (1) I<sub>F</sub> = 100 mA
- (2)  $I_F = 10 \text{ mA}$
- (3)  $I_F = 5 \text{ mA}$
- (3)  $I_F = 1 \text{ mA}$
- $(3) I_{\rm F} = 1 \, {\rm mA}$

Figure 3. Insertion loss of the diode in on-state as a function of frequency (typical values)



f = 100 MHz; T<sub>i</sub> = 25 °C.

Figure 2. Diode forward resistance as a function of forward current (typical values)



Diode zero biased and inserted in series with a 50  $\Omega$  strip line circuit. T<sub>amb</sub> = 25 °C.

Figure 4. Isolation of the diode in off-state as a function of frequency (typical values)

### 9 Package outline

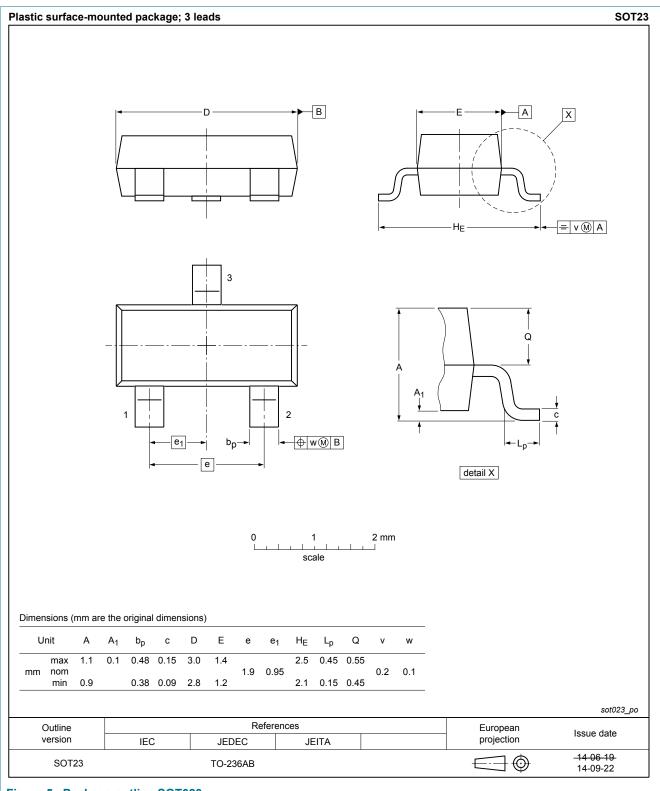


Figure 5. Package outline SOT023

# **10 Revision history**

Table 7. Revision history						
Document ID	Release date	Data sheet status	Change notice	Supersedes		
BAP65-05 v.2.1	20190128	Product data sheet	-	BAP65-05 v.2		
Modifications:	Changed title to S	Silicon PIN diode				
BAP65-05 v.2	20181211	Product data sheet	-	BAP65-05 v.1		
Modifications:	<ul> <li><u>Section 1.2</u> "Features and benefits" has been updated.</li> <li>The "Legal information" pages have been updated.</li> </ul>					
BAP65-05 v .1	20010507	Product data sheet	-	-		

### **11 Legal information**

### 11.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or completing a design. [1]

[2] [3] The term 'short data sheet' is explained in section "Definitions".

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### BAP65-05 Silicon PIN diode

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# BAP65-05 Silicon PIN diode

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