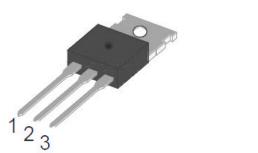


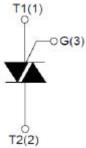
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8A TRIAC



BT137-600D





TO-220 Plastic Package

For use in General Purpose Bidirectional Swiching and Phase Control Applications

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Repetitive Peak Off-State Voltage (Tj=25°C)	Vdrm	600	V
Repetitive Peak Reverse Voltage (Tj=25°C)	Vrrm	600	V
Non Repetitive Surge Peak Off-State Voltage	V _{DSM}	700	V
Non Repetitive Peak Reverse Voltage	V _{RSM}	700	V
RMS On-State Current	I _{T(RMS)}	8	А
Non Repetitive Surge Peak On-State Current (Full Cycle, f = 50MHz)	I _{TSM}	65	А
I ² t Value For Fusing (tp=10ms)	l ² t	21	A ² s
Critical Rate of Rise of On-State Current $(I_G = 2 X I_{GT})$	dl/dt	50	A/μs
Peak Gate Current	I _{GM}	I _{GM} 2	
Average Gate Power Dissipation	P _{G(AV)}	0.5	W
Peak Gate Power	P _{GM}	5	W
Maximum Thermal Resistance Junction to case	R _{th(j-c)}	3	°C/W

ELECTRICAL CHARACTERISTICS (Tj = 25°C unless otherwise specified)

PARAMETER	TEST CONDITION	SYMBOL	QUADRANT	VALUE	UNIT	
Gate Trigger Current	$V_{D} = 12V, R_{I} = 30\Omega$	L.	- -	<5	mA	
Gate mgger Current	$v_{\rm D} = 12 v, \ n_{\rm L} = 3032$	I _{GT}	IV	<10	ША	
Gate Trigger Voltage	$V_D=12V, R_L=30\Omega$	V _{GT}	ALL	<1.5	V	
Off-State Gate voltage	$V_D = V_{DRM}$, Tj=125°C, R _L = 3.3K Ω		ALL	>0.2	V	
Latching Current	I _G =1.2 X I _{GT}	١	-	<15	mA	
	IG-1.2 / IG	۰L	II - IV	<20	ША	
Holding Current I _T = 100mA		Ін		<10	mA	
Critical Rate of Rise of Off-State Voltage	V _D = 2/3 V _{DRM} , Gate Open, Tj=125°C	dV/dt		>5	V/µs	



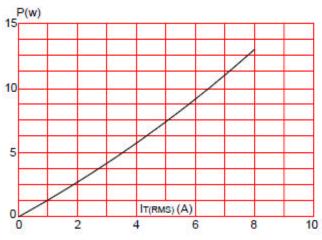
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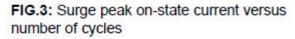


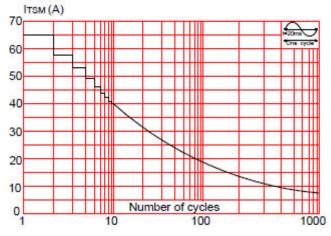
STATIC CHARACTERISTICS					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
On-State Voltage	I _{TM} =10A, tp=380μs	V _{TM}	<1.65	V	
Off-State Leakage Current	V _D =V _{DRM} , Tj=25°C	I _{DRM}	<5	μA	
OII-State Leakage Guilent	V _R =V _{RRM} , Tj=25°C	I _{RRM}	<5	μΑ	
Off-State Leakage Current	V _D =V _{DRM} , Tj=125°C	I _{DRM}	<1	mA	
On-State Leakage Current	V _R =V _{RRM} , Tj=125°C	I _{RRM}	<1	IIIA	

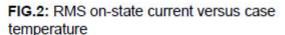
CHARACTERISTICS CURVES

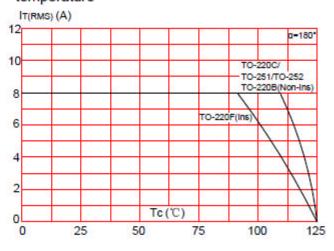
FIG.1 Maximum power dissipation versus RMS on-state current

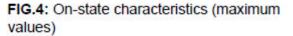












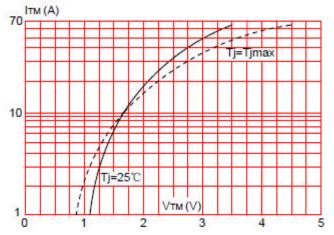




FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<20ms, and corresponging value of I^2t (dI/dt < 50A/µs)

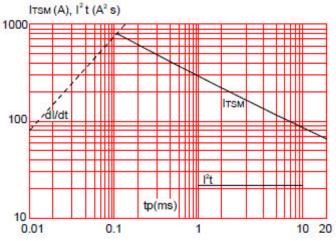


FIG.7: Relative variations of holding current versus junction temperature

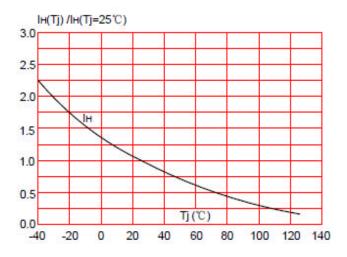


FIG.6: Relative variations of gate trigger current versus junction temperature

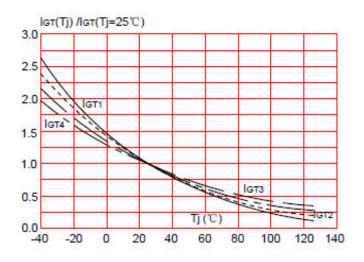
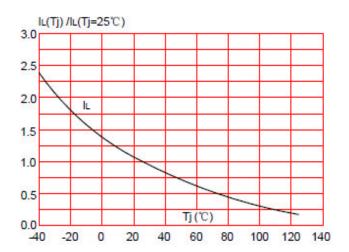
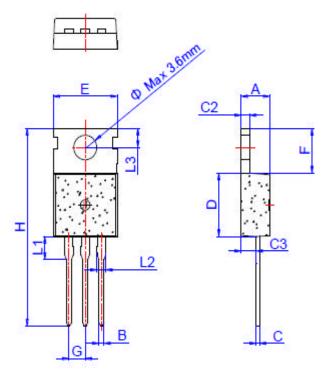


FIG.8: Relative variations of latching current versus junction temperature





PACKAGE OUTLINE AND DIMENSION



Ref.	-		Dime	ensions		
	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.
А	4.40		4.60	0.173		0.181
В	0.70		0.90	0.028		0.035
С	0.45		0.60	0.018		0.024
C2	1.23		1.32	0.048		0.052
C3	2.20		2.60	0.087		0.102
D	8.90		9.90	0.350		0.390
Е	9.90		10.3	0.390		0.406
F	6.30		6.90	0.248		0.272
G		2.54			0.1	
Н	28.0		29.8	1.102		1.173
L1	8 - E	3.39			0.133	24
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
Φ	-	3.6			0.142	<u> </u>



Customer Notes

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- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

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