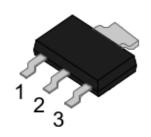
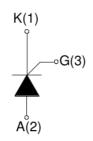




SENSITIVE GATE SCR





BT168GW SOT-223 Plastic Package

BT168GW SCR provides high dv/dt rate with strong resistance to electromagnetic interface. It is specially recommended for use on residual current circuit breaker, straigh hair, igniter etc.

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Storage junction temperature range	T _{stg}	-40 to 150	°C
Operating junction temperature range	T _j	-40 to 110	°C
Repetitive peak off-state voltage	V _{DRM}	800	V
Repetitive peak reverse voltage	V _{RRM}	800	V
RMS on-state current (T _c =75°C)	I _{T(RMS)}	1	А
Non repetitive surge peak onstate current (t _p =10ms)	I _{TSM}	12	А
I²t value for fusing (t _p =10ms)	l²t	0.72	A ² s
Critical rate of rise of on-state current	dI/dt	50	A/μs
Peak gate current (t_p =20 μ s, T_j =110°C)	I _{GM}	0.3	А
Peak gate power (t_p =20 μ s, T_j =110°C)	P _{GM}	0.5	W
Average gate power dissipation (T _j =110°C)	P _{G(AV)}	0.1	W





ELECTRICAL CHARACTERISTICS (T_i=25°C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	VALUE			UNIT
PARAMETER	STWBOL	TEST CONDITION	MIN.	MIN. TYP. MAX.		
Gate Trigger Current	I _{GT}	-V _D =12V, R _I =33Ω	-	40	200	μΑ
Gate Trigger Voltage	V_{GT}	V _D -12 V, IV _L -0032	-	0.6	0.8	V
Non-trigger gate voltage	V _{GD}	V _D =V _{DRM} , T _j =110°C	0.2	-	-	V
Latching Current	IL	I _G =1.2I _{GT}	-	-	5	mA
Holding Current	I _H	I _T =0.05A	-	-	4	mA
Critical rate of rise of off-state voltage	dV/dt	$V_D = 2/3V_{DRM}$, $T_j = 110^{\circ}C$, $R_{GK} = 1k\Omega$	100	200	-	V/μA

STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIO	VALUE (MAX.)	UNIT	
Peak on-state voltage drop	V_{\scriptscriptstyleTM}	I _T =2A, t _p =380μs	T _j =25°C	1.7	\
Maximum forward leakage current	I _{DRM}	\/ -\/ \/ -\/	T _j =25°C	5	μΑ
Maximum reverse leakage current	I _{RRM}	$V_{D} = V_{DRM}, V_{R} = V_{RRM}$	T _j =110°C	100	μΑ

THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Thermal resistance junction to case	R _{th(j-c)}	25	°C/W





CHARACTERISTICS CURVES

FIG.1: Maximum power dissipation versusRMS on-state current

0.9 0.6 0.3 0 0 0.5 1.0 1.

FIG.2: RMS on-state current versus case temperature

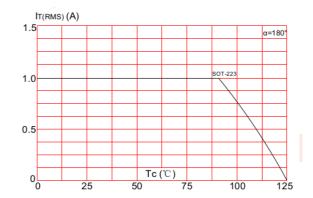


FIG.3: Surge peak on-state current versus number of cycles

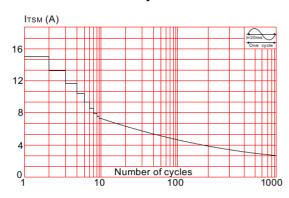


FIG.4: On-state characteristics (maximum values)

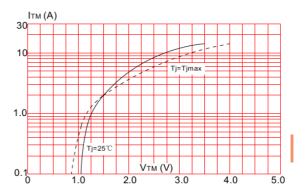
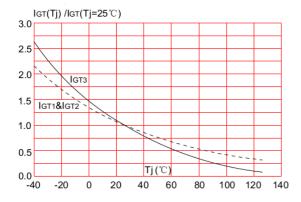
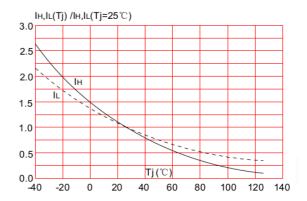


FIG.5: Relative variation of gate trigger **FIG.6:** Relative variation of holding current, current versus junction temperature latching current versus junction temperature



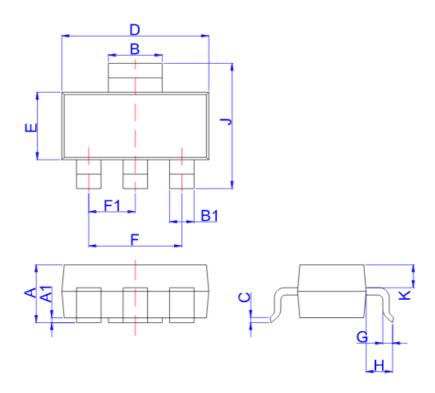








SOT-223 PACKAGE OUTLINE AND DIMENSIONS



	Dimensions						
Ref.	Millimeters			Inches			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α	1.5	1.6	1.8	0.059	0.063	0.071	
A1	0	0.06	0.10	0	0.002	0.004	
В	2.9	3.0	3.1	0.114	0.118	0.122	
B1	0.6	0.7	0.8	0.024	0.028	0.031	
С	0.22	0.26	0.32	0.009	0.010	0.013	
D	6.3	6.5	6.7	0.248	0.256	0.264	
E	3.3	3.5	3.7	0.130	0.138	0.146	
F		4.6			0.181		
F1		2.3			0.091		
G	0.7	0.9	1.1	0.028	0.035	0.043	
Н	1.5	1.75	2.0	0.059	0.069	0.079	
J	6.7	7.0	7.3	0.264	0.276	0.287	
K	0.8	0.9	1.0	0.031	0.035	0.039	





Customer Notes:

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

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C-120 Naraina Industrial Area, New Delhi 110 028, India.
Telephone + 91-11-2579 6150, 4141 1112 Fax + 91-11-2579 5290, 4141 1119
email@cdil.com www.cdil.com
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