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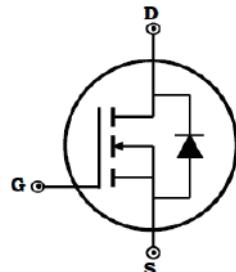
DACMI160N1200

Silicon Carbide Enhancement Mode MOSFET

Features

Preliminary

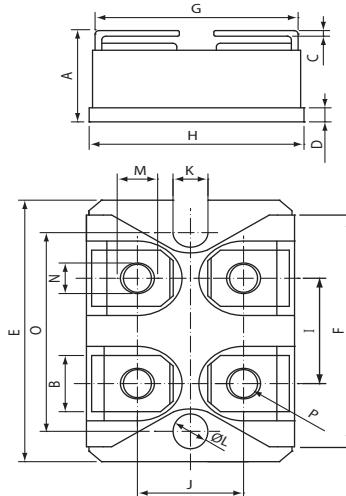
- ◆ $V_{DSS} = 1200V$
- ◆ $R_{DS(ON)} < 20m\Omega @ V_{GS} = 20V$
- ◆ Fully Avalanche Rated
- ◆ Pb Free & RoHS Compliant
- ◆ Isolation Type Package
- ◆ Electrically Isolation base plate



Applications

- ◆ Solar Inverters
- ◆ Power Converters
- ◆ Motor Drive
- ◆ Switch Mode Power Supplies
- ◆ Battery Chargers

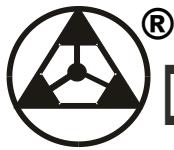
Dimensions in inches and (millimeters)



Absolute Maximum Ratings ($T_c=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	1200	V
Gate-Source Voltage	V_{GS}	-5/+20	V
Drain Current-Continuous @ $T_c = 25^\circ C$ @ $T_c = 100^\circ C$	I_D	168 110	A
Drain Current-Pulsed @ $T_c = 25^\circ C$ ^{Note1}	I_{DM}	400	A
Maximum Power Dissipation	P_D	580	W
Storage Temperature Range	T_{STG}	-50 to +150	°C
Operating Junction Temperature Range	T_J	-50 to +150	°C
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.22	°C/W
Isolation Voltage (A.C. 1 minute)	V_{iso}	4000	V
Mounting torque (M5 Screw)	M_d	3-5	N _m

DIM	INCHES		MM	
	MIN	MXA	MIN	MXA
A	.500	.519	12.70	13.60
B	.307	.322	7.80	8.20
C	.029	.033	.75	.84
D	.077	.082	1.95	2.10
E	1.487	1.502	37.80	38.20
F	1.250	1.258	31.75	32.00
G	.931	.956	23.65	24.30
H	.996	1.007	25.30	25.60
I	.586	.594	14.90	15.10
J	.492	.516	12.50	13.10
K	.161	.169	4.10	4.30
L	.161	.169	4.10	4.30
M	.181	.191	4.60	4.95
N	.165	.177	4.20	4.50
O	1.184	1.192	30.10	30.30
P			M4*8	

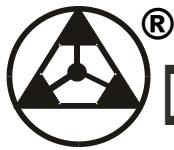


Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
OFF Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}$, $I_{\text{DS}}=100\mu\text{A}$	1200	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=1200\text{V}$	-	-	200	μA
Gate-Body Leakage	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	-	-	200	nA
ON Characteristics						
Gate Threshold Voltage	V_{TH}	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{DS}}=8\text{mA}$	2.0	2.5	3.5	V
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}}=20\text{V}$, $I_{\text{DS}}=100\text{A}$	-	16	20	$\text{m}\Omega$
Gate Resistance	R_{G}		-	1.6	2.9	Ω
Forward Transconductance	g_{fs}	$V_{\text{DS}}=20\text{V}$, $I_{\text{D}}=100\text{A}$ Note1	-	30	-	S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{\text{DS}}=800\text{V}$ $V_{\text{GS}}=0\text{V}$ $V_{\text{AC}}=25\text{mV}$ Freq.=1MHz	-	6000	-	pF
Output Capacitance	C_{oss}		-	368	-	
Reverse Transfer Capacitance	C_{rss}		-	80	-	
Turn-On Switching Energy	E_{on}	$V_{\text{DD}}=800\text{V}$, $V_{\text{GS}}=-5\text{V}/+20\text{V}$ $I_{\text{D}}=100\text{A}$, $R_{\text{G(ext)}}=5.1\Omega$ Load=200 μH , $T_J=125^\circ\text{C}$	-	3.9	-	mJ
Turn-Off Switching Energy	E_{off}		-	1.8	-	
Switching Characteristics						
Turn-On Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=600\text{V}$ $V_{\text{GS}}=20\text{V}$ $I_{\text{DS}}=150\text{A}$ $R_{\text{G}}=2.5\Omega$	-	41	-	ns
Rise Time	t_r		-	30	-	
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	72	-	
Fall Time	t_f		-	30	-	
Total Gate Charge at 10V	Q_g	$V_{\text{DS}}=800\text{V}$ $V_{\text{GS}}=20\text{V}$ $I_{\text{DS}}=100\text{A}$	-	360	-	nC
Gate to Source Charge	Q_{gs}		-	90	-	
Gate to Drain Charge	Q_{gd}		-	98	-	
Reverse Diode Characteristics						
Drain-Source Diode Forward Voltage	V_F	$T_J=25^\circ\text{C}$, $I_F=168\text{A}$	-	-	6.5	V
Diode Continuous Forward Current	I_F		-	-	110	A
Diode Pulsed Current Note1	$I_{F,\text{pulse}}$		-	-	400	A
Reverse Recovery time	T_{RR}	$I_F=0.5\text{V}$, $I_R=1.0\text{A}$, $I_{\text{RR}}=0.25\text{A}$	-	-	185	ns

Notes:

1. Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $> 2\%$.



Typical Characteristics

Figure 1. Maximum Power Dissipation (MOSFET) Derating vs. Case Temperature

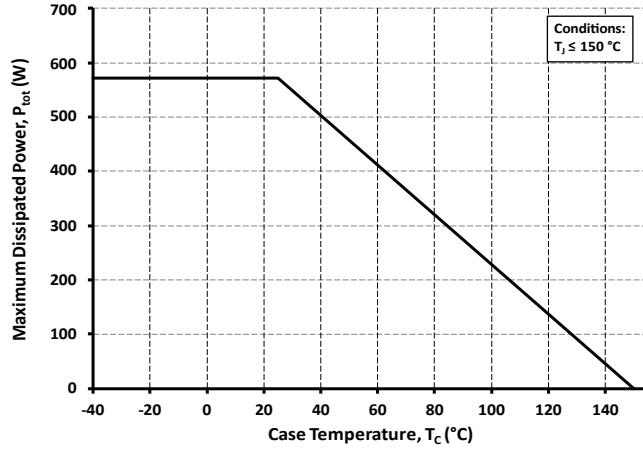


Figure 3. Maximum Power Dissipation (MOSFET) Derating vs. Case Temperature

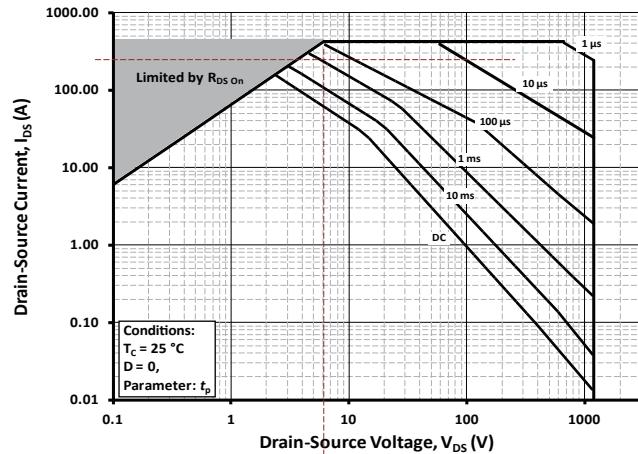


Figure 5. Output Characteristics $T_j = 25$ °C

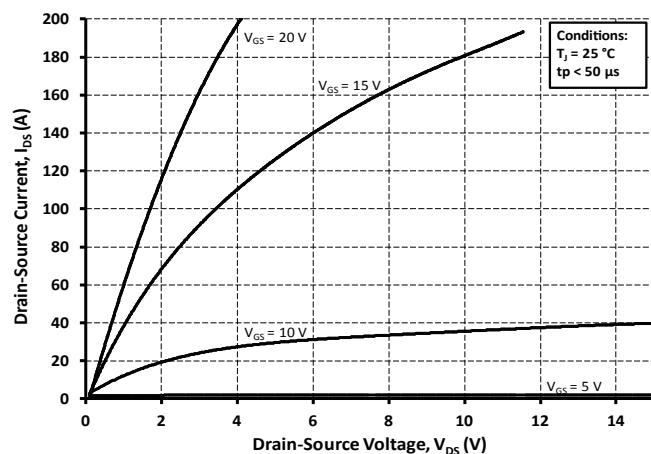


Figure 2. Continuous Drain Current (MOSFET) Derating vs Case Temperature

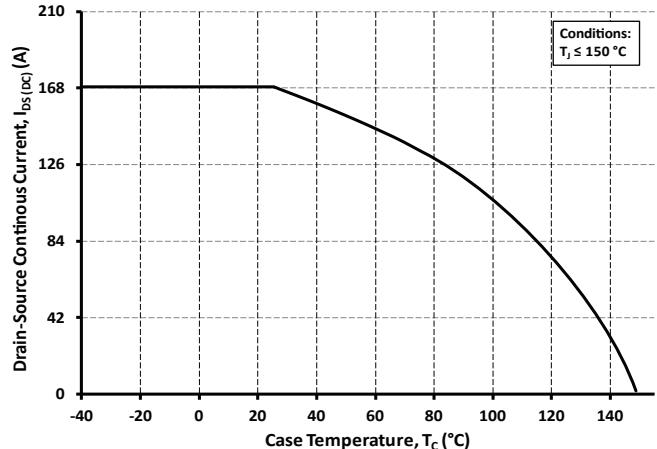


Figure 4. MOSFET Junction to Case Thermal Impedance

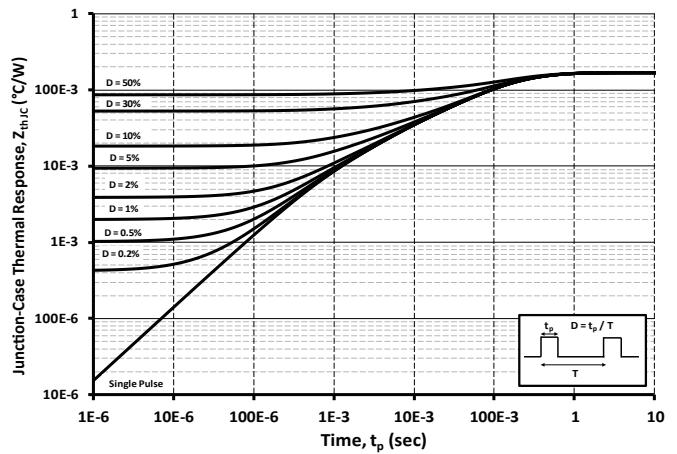
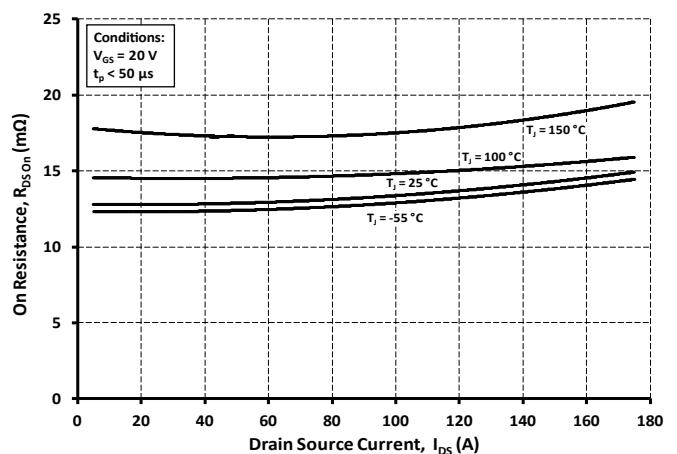


Figure 6. On-Resistance vs. Drain Current For Various Temperatures





Typical Characteristics

Figure 7. On-Resistance vs. Temperature For Various Gate-Source Voltage

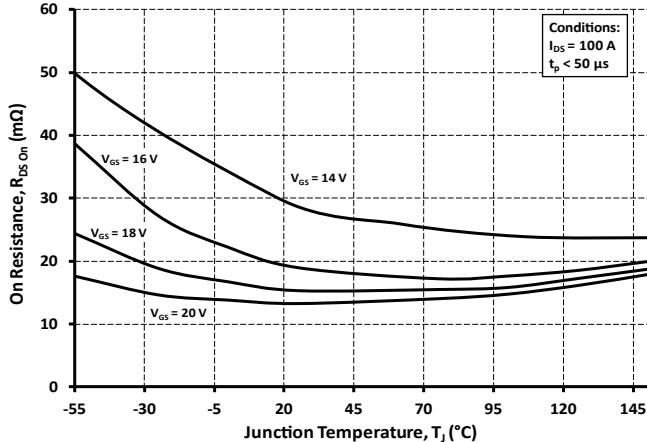


Figure 9. Transfer Characteristic for Various Junction Temperatures

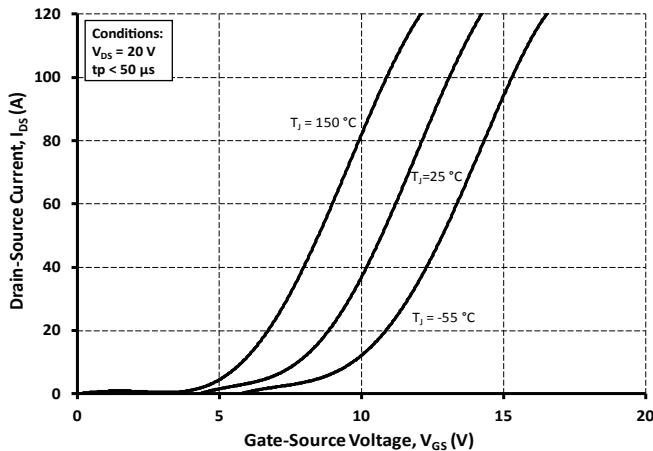


Figure 11. Typical forward characteristics of reverse diode

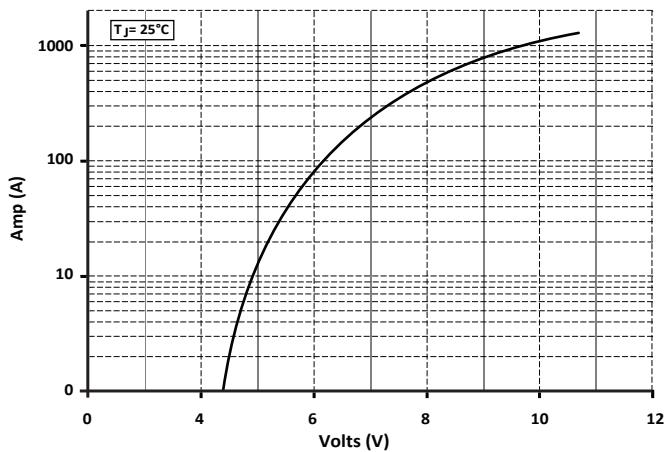


Figure 8. Threshold Voltage vs. Temperature

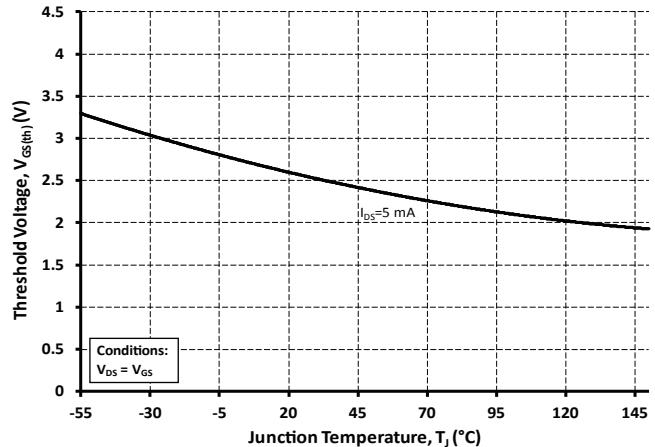


Figure 10. Capacitances vs. Drain-Source Voltage (0 - 1 kV)

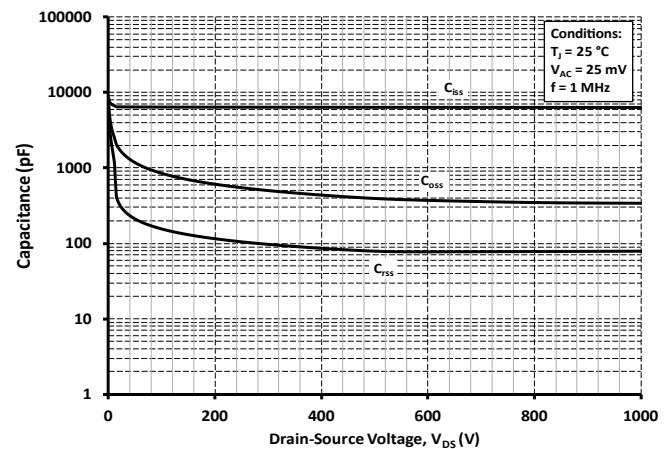
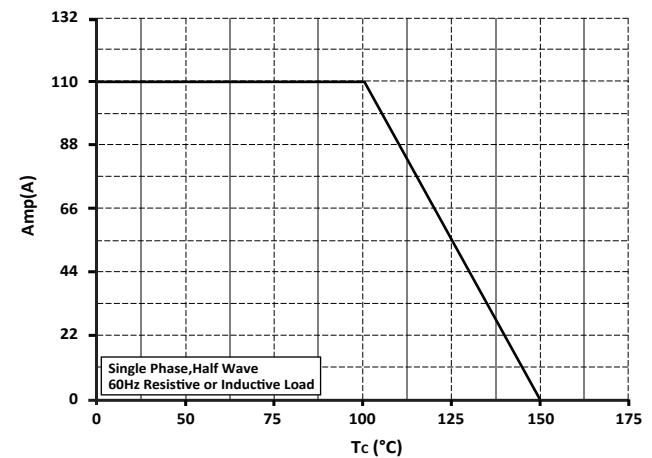


Figure 12. Forward derating curve of reverse diode





Typical Characteristics

Figure 13. Peak forward surge current of reverse diode

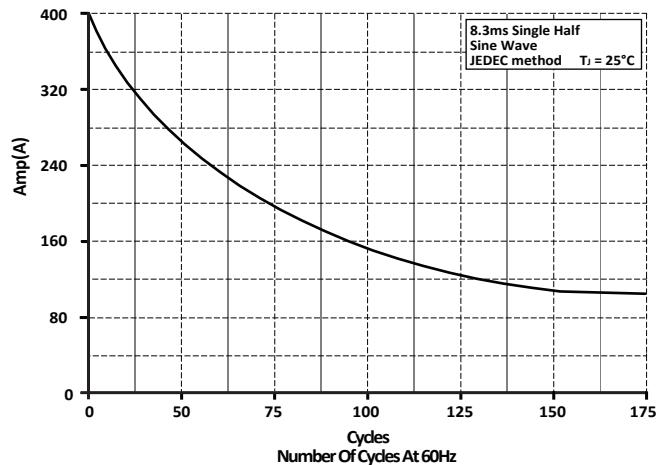


Figure 15. Gate Charge Characteristics

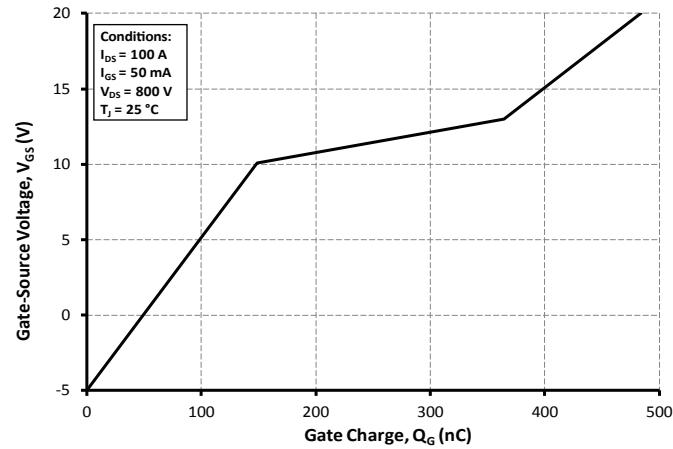


Figure 17. Timing vs. $R_{G(\text{ext})}$

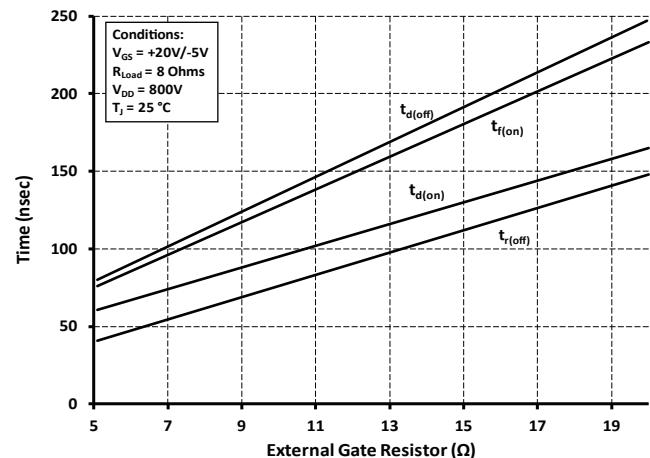


Figure 14. Typical reverse diode characteristics

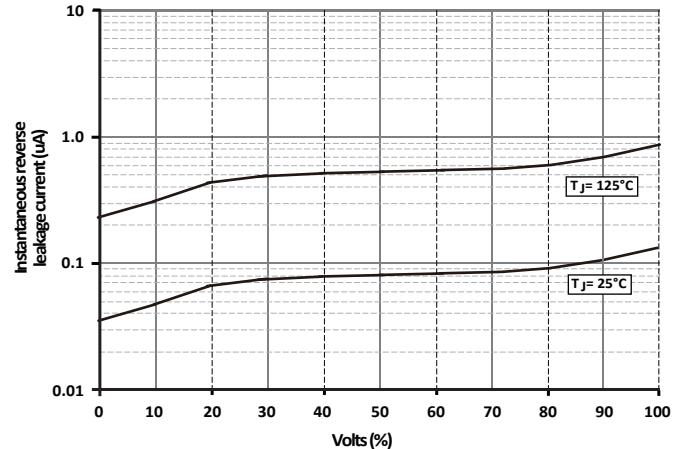
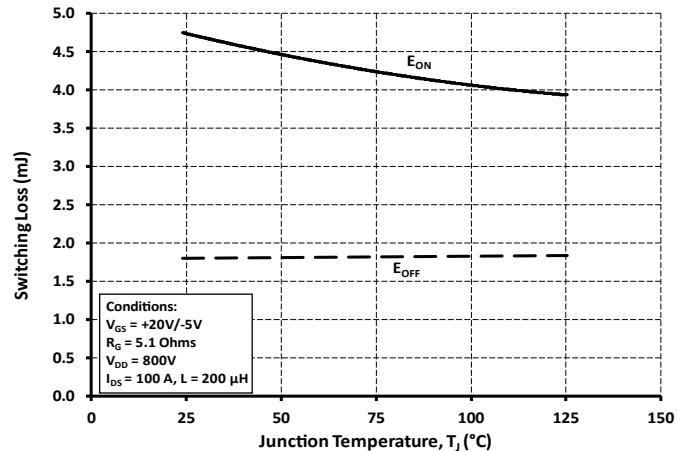


Figure 16. Inductive Switching Energy vs. Temperature



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