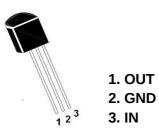


THREE TERMINAL POSITIVE VOLTAGE REGULATOR



LM78L09 TO-92 Plastic Package

Features

Maximum Output Current $I_{OM} = 0.1A$

Output Voltage $V_0 = 9V$

Continuous total dissipation $P_D = 0.625W$

MAXIMUM RATINGS (Operating temperature range applies unless otherwise specified)

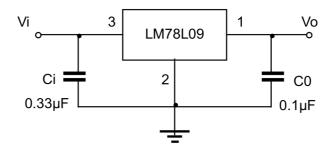
PARAMETERS	SYMBOL	VALUE	UNIT
Input Voltage	V _I	30	V
Operating Junction Temperature Range	T _{OPR}	0 to 125	°C
Storage Temperature Range	T _{STG}	-55 to 150	°C

ELECTRICAL CHARACTERISTICS (V_i=16V, I_o=40mA, C_i=0.33µF, C_o=0.1µF, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION		MIN.	TYP.	MAX.	UNIT
Output voltage	V _o		25°C	8.64	9.0	9.36	V
		12≤V _I ≤24V, I _o =1mA-40mA	0-125°C	8.55	9.0	9.45	V
		I _o =1mA-70mA		8.55	9.0	9.45	V
Load Regulation	ΔV _o	I _o =1mA-100mA	25°C	-	19	90	mV
		I _o =1mA-40mA	25°C	-	11	40	mV
Line Regulation	Δν _ο	12V≤V _I ≤24V	25°C	-	45	175	mV
		13V≤V _I ≤24V	25°C	-	40	125	mV
Quiescent Current	l _q		25°C	-	4.1	6.0	mA
Quiescent Current Change	Δl _q	13V≤V _I ≤24V	0-125°C	-	-	1.5	mA
	ΔI _q	1mA≤l _° 40mA	0-125°C	-	-	0.1	mA
Output Noise Voltage	V _N	10Hz≤f≤100kHz	25°C	-	58	-	μV
Ripple Rejection	RR	15V≤V _I ≤25V, f=120Hz	0-125°C	-	45	-	dB
Dropout Voltage	V _d		25°C	-	1.7	-	V

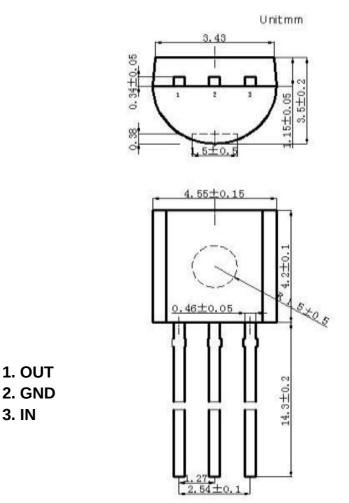


TYPICAL APPLICATION



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.

TO-92 PACKAGE OUTLINE AND DIMENSIONS



Customer Notes:



Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.

2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

DISCLAIMER

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