

30.06.2014

## **Revision History**

VERSION	DATE	REVISED PAGE NO.	Note
0	10.03.2011		First release
1	02.07.2011		Change version
2	15.10.2012		Modify picture Modify CIE
3	09.12.2013		Update Rev.

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### **1. General Specification**

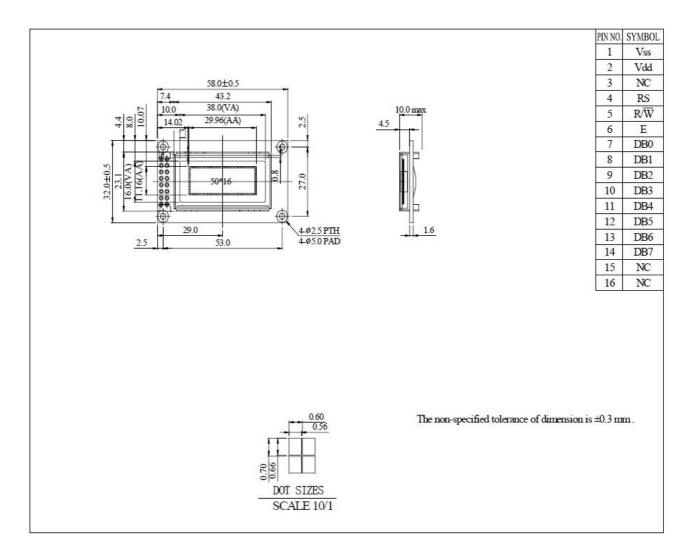
The Features is described as follow:

- Module Dimension: 58.00 x 32.00 x 10.00 mm
- View Area: 38.00 x 16.00 mm
- Active Area: 29.96 x 11.16 mm
- Dot Matrix: 50 x 16 Dots
- Dot Size: 0.56 x 0.66 mm
- Dot Pitch: 0.60x 0.70 mm
- Duty: 1/16
- Emitting Color: OLED , Yellow

### 2. Interface Pin Function

Pin No.	Symbol	Level	Description
1	VSS	0V	Ground
2	VDD	5.0V	Supply Voltage for logic
3	NC	—	No Connection
4	RS	H/L	H: DATA, L: Instruction code
5	R/W	H/L	H: Read(Module $\rightarrow$ MPU) L: Write(MPU $\rightarrow$ Module)
6	E	H,H→L	Chip enable signal
7	DB0	H/L	Data bit 0
8	DB1	H/L	Data bit 1
9	DB2	H/L	Data bit 2
10	DB3	H/L	Data bit 3
11	DB4	H/L	Data bit 4
12	DB5	H/L	Data bit 5
13	DB6	H/L	Data bit 6
14	DB7	H/L	Data bit 7
15	NC	—	No Connection
16	NC	_	No Connection

### 3. Counter Drawing & Block Diagram



### <u>DEP 050016A-Y</u>

### **Production Specification**

MPU	FS	1		COMI~16	
MIC	RW		RS0010	>	
68 Series	Е		Controller IC	SEG1-50	50X16 Pixels OLED
	1180-1187	N			
	N	/			

Address Format	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
GXA(Graphic X-axis Address)	1	ADD6	ADD5	ADD4	ADD3	ADD2	ADD1	ADD0
GYA(Graphic Y-axis Address)	0	1	0	0	0	0	0	OGA0

	1	2	3	4	··· <b>-</b> ··	··· <b>-</b> ··	47	48	49	50
CGA=0	GXA=10000000 GYA=01000000	GXA=10000001 GYA=01000000	GXA=10000010 GYA=01000000	GXA=10000011 GYA=01000000			GXA=10101110 GYA=01000000	GXA=10101111 GYA=01000000	GXA=10110000 GYA=01000000	GXA=10110001 GYA=01000000
CGA=1	GXA=10000000 GYA=01000001	GXA=10000001 GYA=01000001	GXA=10000010 GYA=01000001	GXA=10000011 GYA=01000001			GXA=10101110 GYA=01000001	GXA=10101111 GYA=01000001	GXA=10110000 GYA=01000001	GXA=10110001 GYA=01000001

## 4. Absolute Maximum Ratings

ltem	Symbol	Min	Мах	Unit	Notes
Operating Temperature	T <sub>OP</sub>	-40	+80	°C	
Storage Temperature	T <sub>ST</sub>	-40	+80	°C	
Supply Voltage For Logic	V <sub>DD</sub> -V <sub>SS</sub>	-0.3	5.3	V	

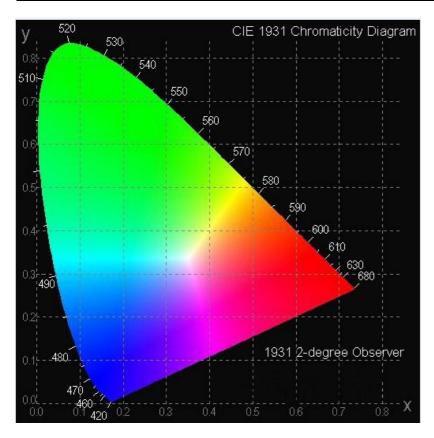
## **5. Electrical Characteristics**

Item	Symbol	Condition	Min	Тур	Мах	Unit
Supply Voltage For Logic	VDD-VSS	—	4.8	5.0	5.3	V
Input High Volt.	VIH	—	0.8 VDD	—	VDD	V
Input Low Volt.	VIL	—	GND	—	0.2 VDD	V
Output High Volt.	VOH	IOH=-0.5mA	0.8 VDD	—	VDD	V
Output Low Volt.	VOL	IOL=0.5mA	GND	—	0.2 VDD	V
50% Check Board Operating Current	IDD	VDD=5V	15	19	24	mA

Note: In order to avoid any possible damages, 3V or 3.3V logic I/O for VDD 5V OLED module is not recommended.

## 6. Optical Characteristics

Item	Symbol	Condition	Min	Тур	Мах	Unit
View Angle	(V)θ	_	160	—	_	deg
	(H)φ	-       160       -       -       de         -       160       -       -       de         Dark       2000:1       -       -       de         Dark       2000:1       -       -       -         -       -       100       -       μs         -       -       100       -       μs         ghtness       100       120       -       cd/         (CIE1931)       0.45       0.47       0.49       -	deg			
Contrast Ratio	CR	Dark	2000:1	_	_	—
Response Time	T rise	—	_	10	—	μs
incoporise nine	T fall	_	—	10	—	μs
Display with 50% check I	Board Brightness		100	120	_	cd/m2
CIEx(Yellow)		(CIE1931)	0.45	0.47	0.49	—
CIEy(Yellow)		(CIE1931)	0.48	0.50	0.52	—



# 7. OLED Lifetime

ITEM	Conditions	Min	Тур	Remark
Operating Life Time	Ta=25°C / Initial 50% check Board Typical Brightness Value	80,000 Hrs	100,000 Hrs	Note

Note:

- 2. This analysis method uses life data obtained under accelerated conditions to extrapolate an estimated probability density function (*pdf*) for the product under normal use conditions.
- 3. Screen saving mode will extend OLED lifetime.

<sup>1.</sup> Life time is defined the amount of time when the luminance has decayed to <50% of the initial value.

## 8. Reliability

#### Content of Reliability Test

Environmenta	I Test		
Test Item	Content of Test	Test Condition	Applicable Standard
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 240hrs	
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-40°C 240hrs	
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	80°C 240hrs	
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-40°C 240hrs	
High Temperature/ Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time.	60°C,90%RH 240hrs	
Temperature Cycle	Endurance test applying the low and high temperature cycle. -40°C 25°C 80°C 30min 5min 30min 1 cycle	-40°C/80°C 100 cycles	
Mechanical Te	st		
Vibration test	Endurance test applying the vibration during transportation and using.	10~22Hz→1.5mmp-p 22~500Hz→1.5G Total 0.5hr	
Shock test	Constructional and mechanical endurance test applying the shock during transportation.	50G Half sin wave 11 ms 3 times of each direction	
Atmospheric pressure test	Endurance test applying the atmospheric pressure during transportation by air.	115mbar 40hrs	
Others			
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5kΩ CS=100pF 1 time	

\*\*\* Supply voltage for OLED system =Operating voltage at 25°C

#### **DEP 050016A-Y**

#### Test and measurement conditions

- 1. All measurements shall not be started until the specimens attain to temperature stability. After the completion of the described reliability test, the samples were left at room temperature for 2 hrs prior to conducting the failure test at 23±5°C; 55±15% RH.
- 2. All-pixels-on is used as operation test pattern.
- 3. The degradation of Polarizer are ignored for High Temperature storage, High Temperature/ Humidity Storage, Temperature Cycle

#### **Evaluation criteria**

- 1. The function test is OK.
- 2. No observable defects.
- 3. Luminance: > 50% of initial value.
- 4. Current consumption: within ± 50% of initial value.

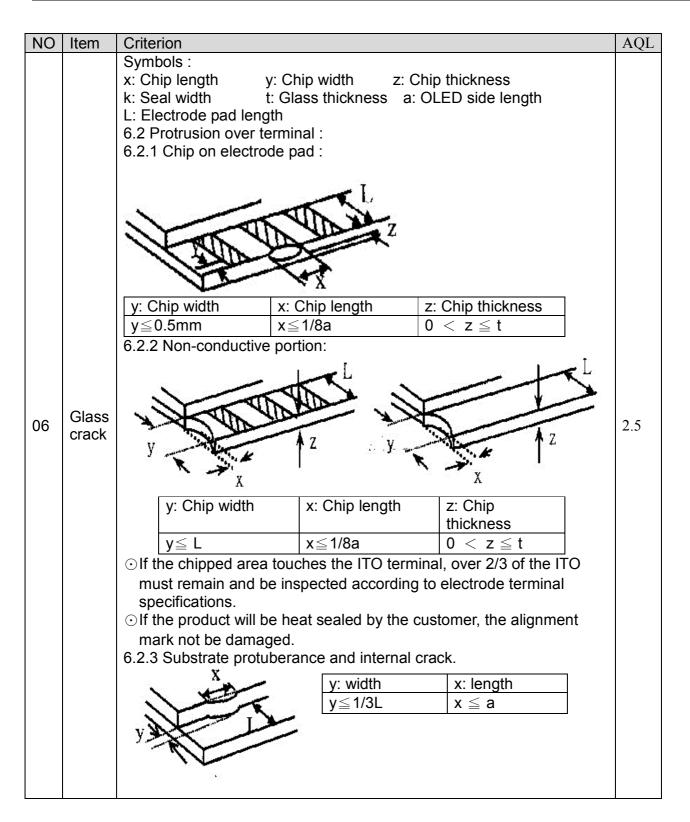
#### APPENDIX: RESIDUE IMAGE

Because the pixels are lighted in different time, the luminance of active pixels may reduce or differ from inactive pixels. Therefore, the residue image will occur. To avoid the residue image, every pixel needs to be lighted up uniformly.

# 9.Inspection specification

NO	Item	Criterion				AQL
01	Electrical Testing	<ul> <li>1.1 Missing vertical, I defect.</li> <li>1.2 Missing characte</li> <li>1.3 Display malfuncti</li> <li>1.4 No function or no</li> <li>1.5 Current consump</li> <li>1.6 OLED viewing an</li> <li>1.7 Mixed product typ</li> <li>1.8 Contrast defect.</li> </ul>	r , dot or on. display. otion exce	icon. eeds product sp		0.65
02	Black or white spots on OLED (display only)	<ul><li>2.1 White and black s</li><li>three white or black s</li><li>2.2 Densely spaced:</li><li>3mm.</li></ul>	spots pre	sent.		2.5
03	OLED black spots, white spots, contamina tion (non-displ ay)	3.1 Round type : As following drawing $\Phi = (x + y) / 2$ $X \leftarrow \frac{1}{7}$		SIZE $\Phi \le 0.10$ $0.10 <$ $\Phi \le 0.20$ $0.20 <$ $\Phi \le 0.25$ $0.25 < \Phi$	Acceptable Q TY Accept no dense 2 1 0	2.5
04	Polarizer		ngth W W 3.0 0. 2.5 0.	rawing) /idth $2 \le 0.02$ $02 < W \le 0.03$ $03 < W \le 0.05$ 05 < W	Acceptable Q TY Accept no dense 2 As round type	2.5
04	bubbles	If bubbles are visible, judge using black spo specifications, not ea to find, must check in specify direction.	ot ¢ isy 0 0 1	ize $\Phi$ $\leq 0.20$ $20 < \Phi \leq 0.50$ $50 < \Phi \leq 1.00$ $.00 < \Phi$ otal Q TY	Acceptable Q TY Accept no dense 3 2 0 3	2.5

NO	Item	Criterion			AQL
05	Scratches	Follow NO.3 OLED b	lack spots, white spo	ots, contamination	
05	Scratches	Follow NO.3 OLED b Symbols Define: x: Chip length k: Seal width L: Electrode pad leng 6.1 General glass ch 6.1.1 Chip on panel s	y: Chip width z: t: Glass thickness a gth: ip :	Chip thickness a: OLED side length	
06	Chipped glass	z: Chip thickness $Z \le 1/2t$ $1/2t < z \le 2t$	y: Chip width Not over viewing area Not exceed 1/3k	x: Chip length $x \le 1/8a$ $x \le 1/8a$	2.5
		<ul> <li>○ If there are 2 or mo</li> <li>6.1.2 Corner crack:</li> <li>x z z</li> </ul>	y: Chip width	x: Chip length	
		Z≦1/2t	Not over viewing area	x≦1/8a	
		$1/2t < z \leq 2t$	Not exceed 1/3k	x≦1/8a	
		$\odot$ If there are 2 or mo			



### <u>DEP 050016A-Y</u>

NO	Item	Criterion	AQL
07	Cracked glass	The OLED with extensive crack is not acceptable.	
08	Backlight elements	<ul> <li>8.1 Illumination source flickers when lit.</li> <li>8.2 Spots or scratched that appear when lit must be judged. Using OLED spot, lines and contamination standards.</li> <li>8.3 Backlight doesn't light or color wrong.</li> </ul>	
09	Bezel	<ul> <li>9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.</li> <li>9.2 Bezel must comply with job specifications.</li> </ul>	
10	PCB、COB	<ul> <li>10.1 COB seal may not have pinholes larger than 0.2mm or contamination.</li> <li>10.2 COB seal surface may not have pinholes through to the IC.</li> <li>10.3 The height of the COB should not exceed the height indicated in the assembly diagram.</li> <li>10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.</li> <li>10.5 No oxidation or contamination PCB terminals.</li> <li>10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.</li> <li>10.7 The jumper on the PCB should conform to the product characteristic chart.</li> <li>10.8 If solder gets on bezel tab pads, OLED pad, zebra pad or screw hold pad, make sure it is smoothed down.</li> </ul>	<ol> <li>2.5</li> <li>2.5</li> <li>2.5</li> <li>2.5</li> <li>0.65</li> <li>2.5</li> <li>0.65</li> <li>2.5</li> </ol>
11	Soldering	<ul> <li>11.1 No un-melted solder paste may be present on the PCB.</li> <li>11.2 No cold solder joints, missing solder connections, oxidation or icicle.</li> <li>11.3 No residue or solder balls on PCB.</li> <li>11.4 No short circuits in components on PCB.</li> </ul>	2.5 2.5 2.5 0.65

### <u>DEP 050016A-Y</u>

NO	Item	Criterion	AQL
NO 12	Item General appearance	<ul> <li>12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.</li> <li>12.2 No cracks on interface pin (OLB) of TCP.</li> <li>12.3 No contamination, solder residue or solder balls on product.</li> <li>12.4 The IC on the TCP may not be damaged, circuits.</li> <li>12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever.</li> <li>12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.</li> <li>12.7 Sealant on top of the ITO circuit has not hardened.</li> <li>12.8 Pin type must match type in specification sheet.</li> <li>12.9 OLED pin loose or missing pins.</li> <li>12.10 Product packaging must the same as specified on packaging specification sheet.</li> </ul>	AQL 2.5 0.65 2.5 2.5 2.5 2.5 2.5 0.65 0.65 0.65 0.65
		12.11 Product dimension and structure must conform to product specification sheet.	

Check Item	Classification	Criteria
No Display	Major	
Missing Line	Major	
Pixel Short	Major	
Darker Short	Major	
Wrong Display	Major	
Un-uniform B/A x 100% < 70% A/C x 100% < 70%	Major	A Hymnel D Data Sixel C B Data Sixel C B Data Sixel C B Data Sixel C B Data Sixel

### **10. Precautions in use of OLED Modules**

- (1) Avoid applying excessive shocks to module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of OLED display module.
- (3) Don't disassemble the OLED display module.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist OLED display module.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.
- (8) It's pretty common to use "Screen Saver" to extend the lifetime and Don't use fix information for long time in real application.
- (9) Don't use fixed information in OLED panel for long time, that will extend "screen burn" effect time..
- (10) DISPLAY has the right to change the passive components, including R2and R3 adjust resistors. (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)

(11) DISPLAY have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, DISPLAY have the right to modify the version.)

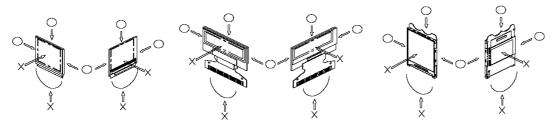
#### **10.1 Handling Precautions**

- (1) Since the display panel is being made of glass, do not apply mechanical impacts such us dropping from a high position.
- (2) If the display panel is broken by some accident and the internal organic substance leaks out, be careful not to inhale nor lick the organic substance.
- (3) If pressure is applied to the display surface or its neighborhood of the OLED display module, the cell structure may be damaged and be careful not to apply pressure to these sections.
- (4) The polarizer covering the surface of the OLED display module is soft and easily scratched. Please be careful when handling the OLED display module.
- (5) When the surface of the polarizer of the OLED display module has soil, clean the surface. It takes advantage of by using following adhesion tape.

\* Scotch Mending Tape No. 810 or an equivalent

Never try to breathe upon the soiled surface nor wipe the surface using cloth containing solvent Also, pay attention that the following liquid and solvent may spoil the polarizer:

- \* Water
- \* Ketone
- \* Aromatic Solvents
- (6) Hold OLED display module very carefully when placing OLED display module into the System housing. Do not apply excessive stress or pressure to OLED display module. And, do not over bend the film with electrode pattern layouts. These stresses will influence the display performance. Also, secure sufficient rigidity for the outer cases.



- (7) Do not apply stress to the LSI chips and the surrounding molded sections.
- (8) Do not disassemble nor modify the OLED display module.
- (9) Do not apply input signals while the logic power is off.
- (10) Pay sufficient attention to the working environments when handing OLED display modules to prevent occurrence of element breakage accidents by static electricity.
- \* Be sure to make human body grounding when handling OLED display modules.
- \* Be sure to ground tools to use or assembly such as soldering irons.
- \* To suppress generation of static electricity, avoid carrying out assembly work under dry environments.
- \* Protective film is being applied to the surface of the display panel of the OLED display module. Be careful since static electricity may be generated when exfoliating the protective film.
- (11) Protection film is being applied to the surface of the display panel and removes the protection film before assembling it. At this time, if the OLED display module has been stored surface of the display panel after removed of the film. In such case, remove the residue material by the method introduced in the above Section 5.
- (12) If electric current is applied when the OLED display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful to avoid the above.

#### **10.2 Storage Precautions**

- (1) When storing OLED display modules, put them in static electricity preventive bags avoiding exposure to direct sun light nor to lights of fluorescent lamps. And, also, avoiding high temperature and high humidity environment or low temperature (less than 0°C) environments. (We recommend you to store these modules in the packaged state when they were shipped from DISPLAY. At that time, be careful not to let water drops adhere to the packages or bags nor let dewing occur with them.
- (2) If electric current is applied when water drops are adhering to the surface of the OLED display module, when the OLED display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful about the above.

#### **10.3 Designing Precautions**

- (1) The absolute maximum ratings are the ratings which cannot be exceeded for OLED display module, and if these values are exceeded, panel damage may be happen.
- (2) To prevent occurrence of malfunctioning by noise, pay attention to satisfy the VIL and VIH specifications and, at the same time, to make the signal line cable as short as possible.
- (3) We recommend you to install excess current preventive unit (fuses, etc.) to the power circuit (VDD). (Recommend value: 0.5A)

(4) Pay sufficient attention to avoid occurrence of mutual noise interference with the neighboring devices.

- (5) As for EMI, take necessary measures on the equipment side basically.
- (6) When fastening the OLED display module, fasten the external plastic housing section.
- (7) If power supply to the OLED display module is forcibly shut down by such errors as taking out the main battery while the OLED display panel is in operation, we cannot guarantee the quality of this OLED display module. Connection (contact) to any other potential than the above may lead to rupture of the IC.

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