



**MMBT5401** 

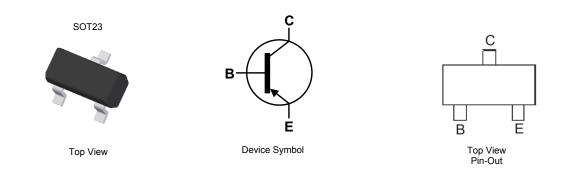
#### **150V PNP SMALL SIGNAL TRANSISTOR IN SOT23**

#### Features

- Epitaxial Planar Die Construction
- Complementary NPN Type MMBT5551
- Ideal for Low Power Amplification and Switching
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP capable (Note 4)

#### **Mechanical Data**

- Case: SOT23
- Case material: molded plastic, "Green" molding compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208<sup>(3)</sup>
- Weight: 0.008 grams (Approximate)



#### Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
MMBT5401-7-F	AEC-Q101	K4M	7	8	3,000
MMBT5401-13-F	AEC-Q101	K4M	13	8	10,000
MMBT5401Q-7-F	Automotive	K4M	7	8	3,000

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

 See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

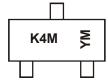
4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product\_compliance\_definitions/.

5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

#### **Marking Information**

Notes:

Code



5

K4M = Product Type Marking Code YM = Date Code Marking Y = Year (ex: N = 2002)

8

Х

Sep

9

M = Month (ex: 9 = September)

7

Date Code	e Key											
Year	1998	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009
Code	J	K	L	М	N	Р	R	S	Т	U	V	W
Month	Jar	1	Feb	Ma	r	Apr	Ma	у	Jun	Ju	1	Aug

4

1

2

3

6

В

С

Dec

D

2010 2011 2012 2013 2014 2015

А

Nov

Ν

Ζ

Oct

0



### Absolute Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-160	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-150	V
Emitter-Base Voltage	V <sub>EBO</sub>	-5.0	V
Collector Current	Ι <sub>C</sub>	-600	mA

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Power Dissipation	(Note 6)	P	310	mW	
Fower Dissipation	(Note 7)	P <sub>D</sub>	350	11100	
Thermal Desistance Junction to Ambient	(Note 6)	P	403	°C/W	
Thermal Resistance, Junction to Ambient	(Note 7)	R <sub>0JA</sub>	357	°C/W	
Thermal Resistance, Junction to Leads (Note 8)		R <sub>θJL</sub>	350	°C/W	
Operating and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 to +150	°C		

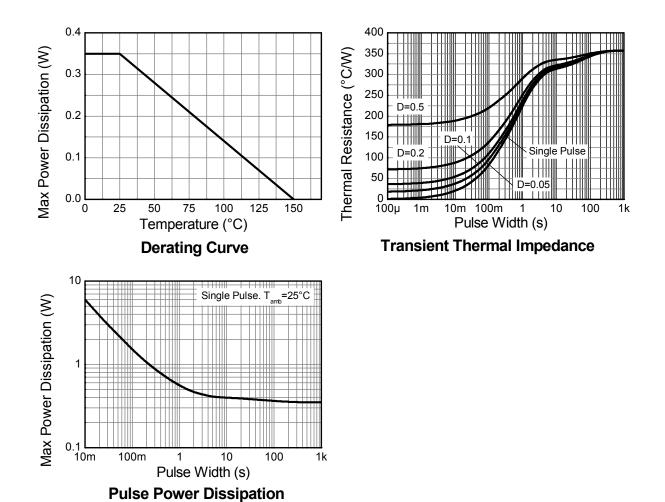
### ESD Ratings (Note 9)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes: 6. For a device mounted on minimum recommended pad layout 1oz copper that is on a single-sided FR4 PCB; device is measured under still air For a device monted on minimum recommended paragout 102 copper that is a conditions whilst operating in a steady-state.
Same as note (6), except the device is mounted on 15 mm x 15mm 1oz copper.
Thermal resistance from junction to solder-point (at the end of the leads).
Refer to JEDEC specification JESD22-A114 and JESD22-A115.



## **Thermal Characteristics and Derating Information**





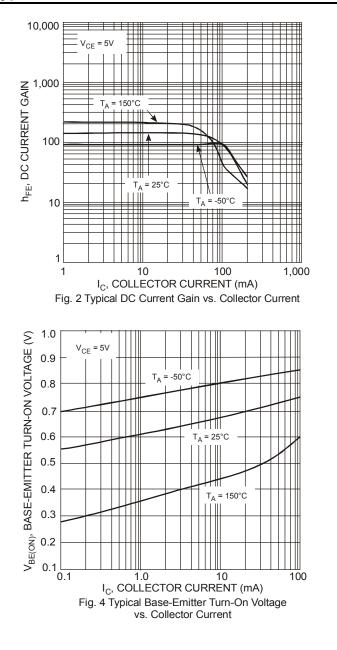
# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

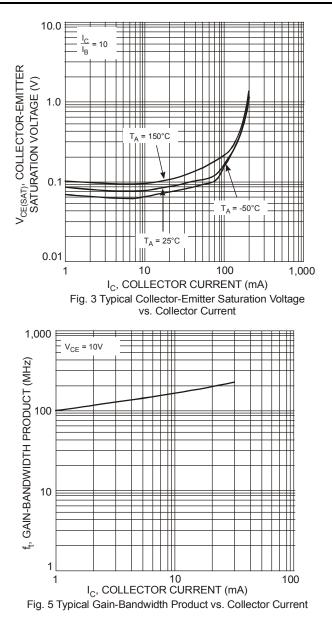
Characteristic	Symphol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 10)	Symbol	Min	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-160	_	V	$I_{\rm C} = -100\mu A, I_{\rm E} = 0$
Collector-Emitter Breakdown Voltage	BVCEO	-150	_	V	$I_{\rm C} = -1.0 {\rm mA}, I_{\rm B} = 0$
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	-5.0	_	V	$I_{\rm E} = -10\mu A, I_{\rm C} = 0$
Collector Cutoff Current	Ісво		-50	nA	$V_{CB} = -120V, I_E = 0$
	ICBO		-50	μA	V <sub>CB</sub> = -120V, I <sub>E</sub> = 0, T <sub>A</sub> = +100°C
Emitter Cutoff Current	I <sub>EBO</sub>	_	-50	nA	$V_{EB} = -4.0V, I_C = 0$
ON CHARACTERISTICS (Note 10)			1	r	
		50			$I_{C} = -1.0 \text{mA}, V_{CE} = -5.0 \text{V}$
DC Current Gain	h <sub>FE</sub>	60	240	_	$I_{C} = -10 \text{mA}, V_{CE} = -5.0 \text{V}$
		50			$I_{C} = -50 \text{mA}, V_{CE} = -5.0 \text{V}$
Collector-Emitter Saturation Voltage	<b>M</b>	_	-0.2	V	I <sub>C</sub> = -10mA, I <sub>B</sub> = -1.0mA
	V <sub>CE(SAT)</sub>		-0.5	v	I <sub>C</sub> = -50mA, I <sub>B</sub> = -5.0mA
Base-Emitter Saturation Voltage	<i>\</i> /	SAT) —	-1.0	V	I <sub>C</sub> = -10mA, I <sub>B</sub> = -1.0mA
Base-Emiller Saturation voltage	V <sub>BE(SAT)</sub>				I <sub>C</sub> = -50mA, I <sub>B</sub> = -5.0mA
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	C <sub>obo</sub>		6.0	pF	V <sub>CB</sub> = -10V, f = 1.0MHz, I <sub>E</sub> = 0
Small Signal Current Gain	h <sub>fe</sub>	40	200		V <sub>CE</sub> = -10V, I <sub>C</sub> = -1.0mA,
	Tite	-10	200		f = 1.0kHz
Current Gain-Bandwidth Product	f⊤	100	300	MHz	$V_{CE} = -10V, I_{C} = -10mA,$
	.1				f = 100MHz
Noise Figure	NF	NF —	8.0	dB	$V_{CE}$ = -5.0V, $I_{C}$ = -200 $\mu$ A,
					R <sub>S</sub> = 10Ω, f = 1.0kHz

Notes: 10. Measured under pulsed conditions. Pulse width  $\leq$  300µs. Duty cycle  $\leq$  2%.



# Typical Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

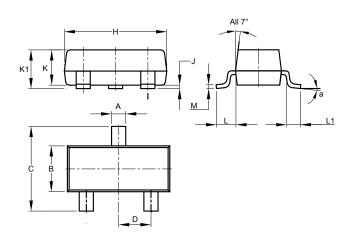






# **Package Outline Dimensions**

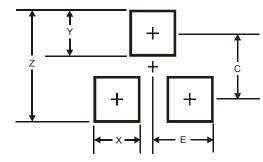
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT23						
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
κ	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	8°						
All	Dimens	ions in	mm				

## Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.



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