



Intel® Xeon® Processor D-1539

12M Cache, 1.60 GHz

Specifications

Product Images

Ordering and Compliance

Ordering and spec information

Intel® Xeon® Processor D-1539 (12M Cache, 1.60 GHz) FC-BGA14C, Tray

Spec Code	SR2DH
Ordering Code	GG8067402569000
Step	V2
RCP	\$590.00

Trade compliance information

ECCN	5A992CN3
CCATS	G071701++
US HTS	8542310001

Downloads and Software

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Refer to Datasheet for formal definitions of product properties and features.

"Announced" SKUs are not yet available. Please refer to the Launch Date for market availability.

Some products can support AES New Instructions with a Processor Configuration update, in particular, i7-2630QM/i7-2635QM, i7-2670QM/i7-2675QM, i5-2430M/i5-2435M, i5-2410M/i5-2415M. Please contact OEM for the BIOS that includes the latest Processor configuration update.

‡ This feature may not be available on all computing systems. Please check with the system vendor to determine if your system delivers this feature, or reference the system specifications (motherboard, processor, chipset, power supply, HDD, graphics controller, memory, BIOS, drivers, virtual machine monitor-VMM, platform software, and/or operating system) for feature compatibility. Functionality, performance, and other benefits of this feature may vary depending on system configuration.

See <http://www.intel.com/content/www/us/en/architecture-and-technology/hyper-threading/hyper-threading-technology.html?wapkw=hyper+threading> for more information including details on which processors support Intel® HT Technology.

Max Turbo Frequency refers to the maximum single-core processor frequency that can be achieved with Intel® Turbo Boost Technology. See www.intel.com/technology/turboboost/ for more information.

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System and Maximum TDP is based on worst case scenarios. Actual TDP may be lower if not all I/Os for chipsets are used.

Low Halogen: Applies only to brominated and chlorinated flame retardants (BFRs/CFRs) and PVC in the final product. Intel components as well as purchased components on the finished assembly meet JS-709 requirements, and the PCB / substrate meet IEC 61249-2-21 requirements. The replacement of halogenated flame retardants and/or PVC may not be better for the environment.

For benchmarking data see <http://www.intel.com/performance>.

Intel processor numbers are not a measure of performance. Processor numbers differentiate features within each processor family, not across different processor families. See <http://www.intel.com/content/www/us/en/processors/processor-numbers.html> for details.

Processors that support 64-bit computing on Intel® architecture require an Intel 64 architecture-enabled BIOS.

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Table with 2 columns: Field (Product Collection, Code Name, Vertical Segment, Processor Number, Status, Launch Date, Lithography, Recommended Customer Price, Use Conditions) and Value.

Performance

Table with 2 columns: Field (# of Cores, # of Threads, Processor Base Frequency, Max Turbo Frequency, Cache, TDP) and Value.

Supplemental Information

Table with 2 columns: Field (Embedded Options Available, Datasheet, Product Brief) and Value.

Memory Specifications

Table with 2 columns: Field (Max Memory Size, Memory Types, Maximum Memory Speed, Max # of Memory Channels, ECC Memory Supported) and Value.

Expansion Options

Table with 2 columns: Field (Scalability, PCI Express Revision, PCI Express Configurations, Max # of PCI Express Lanes) and Value.

I/O Specifications

Table with 2 columns: Field (# of USB Ports, USB Revision, Total # of SATA Ports, Integrated LAN, General Purpose I/O, UART) and Value.

Networking Specifications

Table with 2 columns: Field (Interfaces Supported) and Value.

Package Specifications

Table with 2 columns: Field (Sockets Supported, Max CPU Configuration, Operating Temperature Range, Package Size, Low Halogen Options Available) and Value.

Advanced Technologies

Table with 2 columns: Field (Intel Turbo Boost Technology, Intel Hyper-Threading Technology, Intel Virtualization Technology, Intel VT-x with Extended Page Tables, Intel TSK-NI, Intel 64-bit, Instruction Set, Instruction Set Extensions, Idle States, Enhanced Intel SpeedStep Technology, Thermal Monitoring Technologies) and Value.

Security & Reliability

Table with 2 columns: Field (Intel AES New Instructions, Secure Key, Intel Trusted Execution Technology, Execute Disable Bit, Intel OS Guard) and Value.

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