## RQK2501YGDQA

Silicon N Channel MOS FET

## Features

- High drain to source voltage and Low gate drive
$\mathrm{V}_{\text {DSs }}$ : 250 V and 2.5 V gate drive
- Low drive current
- High speed switching
- Small traditional package (MPAK)


## Outline

RENESAS Package code: PLSP0003ZB-A
(Package name: MPAK)


## Absolute Maximum Ratings

| Item | Symbol | Ratings | Unit |
| :--- | :---: | :---: | :---: |
| Drain to source voltage | $V_{\text {DSS }}$ | 250 | V |
| Gate to source voltage | $\mathrm{V}_{\mathrm{GSS}}$ | $\pm 10$ | V |
| Drain current | $\mathrm{I}_{\mathrm{D}}$ | 0.4 | A |
| Drain peak current | $\mathrm{I}_{\mathrm{D}(\text { pulse })}$ Note1 | 1.6 | A |
| Body - drain diode reverse drain current | $\mathrm{I}_{\mathrm{DR}}$ | 0.4 | A |
| Channel dissipation | Pch $^{\text {Note2 }}$ | 0.8 | W |
| Channel temperature | Tch | 150 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | Tstg | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

Notes: 1. $\mathrm{PW} \leq 10 \mu \mathrm{~s}$, Duty cycle $\leq 1 \%$
2. When using the glass epoxy board (FR-4 $40 \times 40 \times 1 \mathrm{~mm}$ )

## Electrical Characteristics

$\left(\mathrm{Ta}=25^{\circ} \mathrm{C}\right)$

| Item | Symbol | Min | Typ | Max | Unit | Test conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Drain to source breakdown voltage | $\mathrm{V}_{\text {(BR)DSS }}$ | 250 | - | - | V | $\mathrm{I}_{\mathrm{D}}=10 \mathrm{~mA}, \mathrm{~V}_{\mathrm{GS}}=0$ |
| Gate to source breakdown voltage | $\mathrm{V}_{\text {(BR)GSS }}$ | +10 | - | - | V | $\mathrm{I}_{\mathrm{G}}=+100 \mu \mathrm{~A}, \mathrm{~V}_{\mathrm{DS}}=0$ |
| Gate to source breakdown voltage | $\mathrm{V}_{\text {(BR)GSS }}$ | -10 | - | - | V | $\mathrm{I}_{\mathrm{G}}=-100 \mu \mathrm{~A}, \mathrm{~V}_{\mathrm{DS}}=0$ |
| Gate to source leak current | Igss | - | - | +10 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{GS}}=+8 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0$ |
| Gate to source leak current | IGss | - | - | -10 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{GS}}=-8 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0$ |
| Zero gate voltage drain current | Idss | - | - | 1 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{DS}}=250 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0$ |
| Gate to source cutoff voltage | $\mathrm{V}_{\mathrm{GS} \text { (off) }}$ | 0.5 | - | 1.5 | V | $\mathrm{V}_{\mathrm{DS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=1 \mathrm{~mA}$ |
| Drain to source on state resistance | $\mathrm{R}_{\mathrm{DS} \text { (on) }}$ | - | 4.0 | 5.4 | $\Omega$ | $\mathrm{I}_{\mathrm{D}}=0.2 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=4 \mathrm{~V}^{\text {Note3 }}$ |
| Drain to source on state resistance | $\mathrm{R}_{\mathrm{DS} \text { (on) }}$ | - | 4.1 | 5.6 | $\Omega$ | $\mathrm{I}_{\mathrm{D}}=0.2 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=2.5 \mathrm{~V}^{\text {Note3 }}$ |
| Forward transfer admittance | $\left\|y_{\text {fs }}\right\|$ | 0.6 | 0.95 | - | S | $\mathrm{I}_{\mathrm{D}}=0.2 \mathrm{~A}, \mathrm{~V}_{\mathrm{DS}}=10 \mathrm{~V}^{\text {Note } 3}$ |
| Input capacitance | Ciss | - | 80 | - | pF | $\begin{aligned} & V_{D S}=25 \mathrm{~V} \\ & V_{G S}=0 \\ & f=1 \mathrm{MHz} \end{aligned}$ |
| Output capacitance | Coss | - | 10 | - | pF |  |
| Reverse transfer capacitance | Crss | - | 3 | - | pF |  |
| Turn - on delay time | $\mathrm{t}_{\mathrm{d}(\mathrm{on})}$ | - | 15 | - | ns | $\begin{aligned} & \mathrm{V}_{\mathrm{DD}}=125 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=4 \mathrm{~V} \\ & \mathrm{I}_{\mathrm{D}}=0.2 \mathrm{~A} \\ & \mathrm{R}_{\mathrm{L}}=625 \Omega \\ & \mathrm{Rg}=10 \Omega \end{aligned}$ |
| Rise time | $\mathrm{t}_{\mathrm{r}}$ | - | 16 | - | ns |  |
| Turn - off delay time | $\mathrm{t}_{\mathrm{d} \text { (off) }}$ | - | 40 | - | ns |  |
| Fall time | $\mathrm{t}_{\mathrm{f}}$ | - | 38 | - | ns |  |
| Total gate charge | Qg | - | 4.0 | - | nC | $\begin{aligned} & \mathrm{V}_{\mathrm{DD}}=200 \mathrm{~V} \\ & \mathrm{~V}_{\mathrm{GS}}=4 \mathrm{~V} \\ & \mathrm{I}_{\mathrm{D}}=0.4 \mathrm{~A} \end{aligned}$ |
| Gate to Source charge | Qgs | - | 0.5 | - | nC |  |
| Gate to drain charge | Qgd | - | 2.6 | - | nC |  |
| Body - drain diode forward voltage | $V_{\text {DF }}$ | - | 0.8 | 1.2 | V | $\mathrm{I}_{\mathrm{F}}=0.4 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=0^{\text {Note } 3}$ |

Notes: 3. Pulse test

## Main Characteristics



Drain to Source Saturation Voltage vs. Gate to Source Voltage


Static Drain to Source on State Resistance vs. Case Temperature (1)



Forward Transfer Admittance vs. Drain Current


Static Drain to Source on State Resistance vs. Drain Current


Static Drain to Source on State Resistance vs. Case Temperature (2)


Zero Gate Voltage Drain current vs. Case Temperature




## Package Dimensions

| JEITA Package Code | RENESAS Code | Previous Code | MASS (Typ) [g] |
| :---: | :---: | :---: | :---: |
| SC-59A | PLSP0003ZB-A | $\operatorname{MPAK}(\mathrm{T}) / \operatorname{MPAK}(\mathrm{T}) \mathrm{V}$ | 0.011 |



A-A Section

| Reference <br> Symbol | Dimensions in millimeters |  |  |
| :---: | :---: | :---: | :---: |
|  | Min | Nom | Max |
| A | 1.0 | - | 1.3 |
| $\mathrm{~A}_{1}$ | 0 | - | 0.1 |
| $\mathrm{~A}_{2}$ | 1.0 | 1.1 | 1.2 |
| $\mathrm{~A}_{3}$ | - | 0.25 | - |
| b | 0.35 | 0.4 | 0.5 |
| c | 0.1 | 0.16 | 0.26 |
| D | 2.7 | - | 3.1 |
| E | 1.35 | 1.5 | 1.65 |
| e | - | 0.95 | - |
| $\mathrm{H}_{\mathrm{E}}$ | 2.2 | 2.8 | 3.0 |
| L | 0.35 | - | 0.75 |
| $\mathrm{~L}_{1}$ | 0.15 | - | 0.55 |
| $\mathrm{LP}_{\mathrm{p}}$ | 0.25 | - | 0.65 |
| X | - | - | 0.05 |
| Q | - | 0.3 | - |

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## Ordering Information

| Orderable Part Number | Quantity | Shipping Container |
| :--- | :--- | :---: |
| RQK2501YGDQATL-E | 3000 pcs. | $\phi 178 \mathrm{~mm}$ reel, 8 mm Emboss taping |
| RQK2501YGDQATL-H |  |  |

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