

## **RQJ0302NGDQA**

# Silicon P Channel MOS FET Power Switching

R07DS0294EJ0600 Rev.6.00 Jan 10, 2014

#### **Features**

- Low on-resistance
  - $R_{\rm DS(on)}$  = 138 m $\Omega$  typ (V  $_{\rm GS}$  = –10 V,  $I_{\rm D}$  = –1.1 A)
- Low drive current
- High speed switching
- 4.5 V gate drive

#### **Outline**

RENESAS Package code: PLSP0003ZB-A (Package name: MPAK)



3 0 2 0 1

1. Source

2. Gate

3. Drain

Note: Marking is "NG".

## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{DSS}$	-30	V
Gate to source voltage	$V_{GSS}$	+10 / -20	V
Drain current	I <sub>D</sub>	-2.2	Α
Drain peak current	I <sub>D(Pulse)</sub> Note1	-5	Α
Body - drain diode reverse drain current	I <sub>DR</sub>	-2.2	Α
Channel dissipation	Pch Note2	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. When using the glass epoxy board (FR-4:  $40 \times 40 \times 1$  mm)

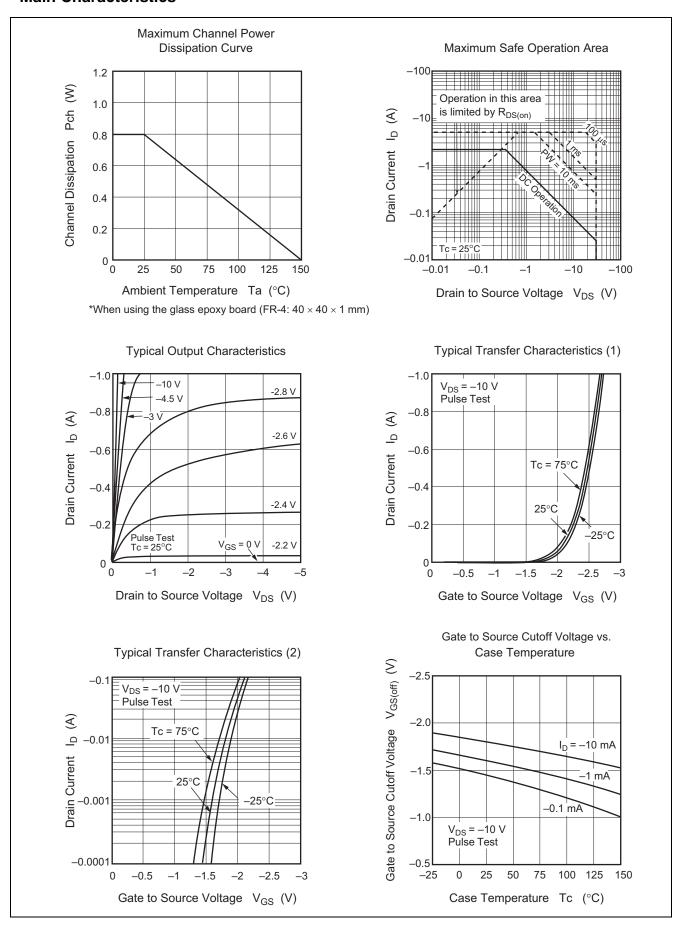
## **Electrical Characteristics**

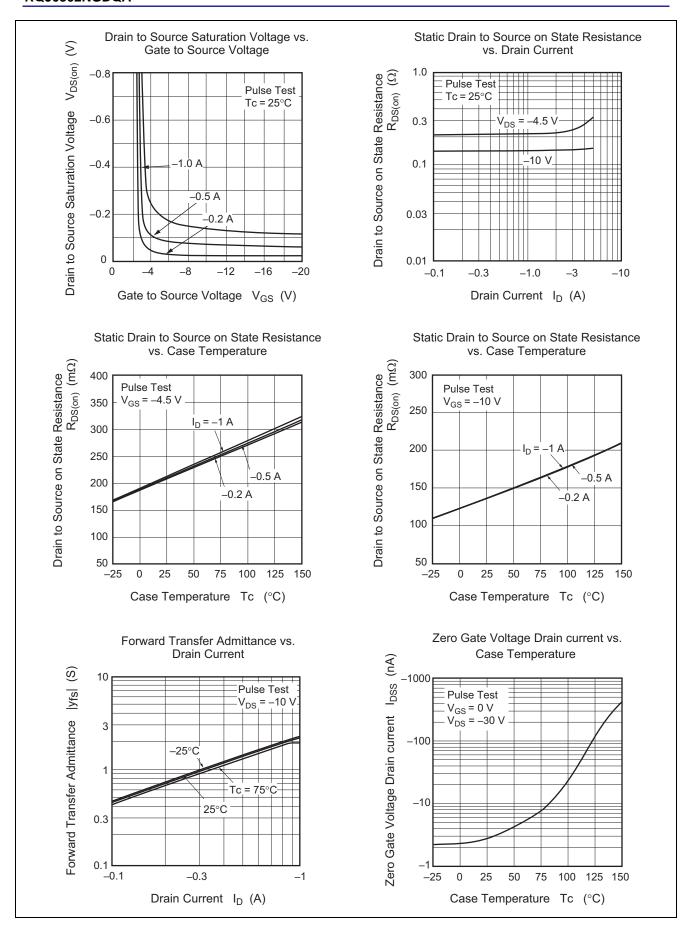
 $(Ta = 25^{\circ}C)$ 

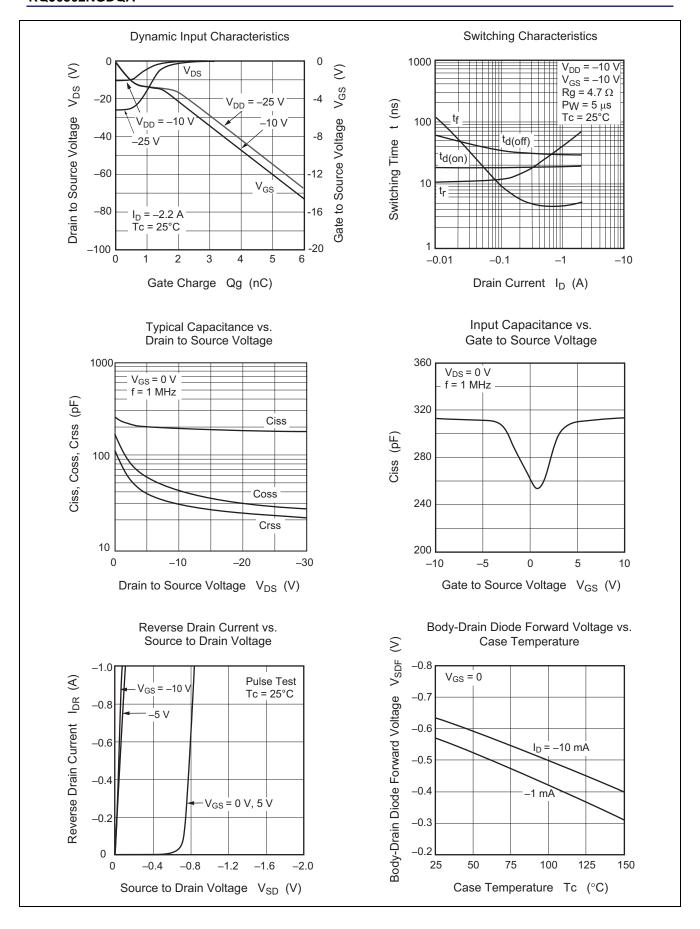
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-30	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	+10	_	_	V	$I_G = +100  \mu A,  V_{DS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	-20	_	_	V	$I_G = -100 \mu\text{A},  V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	+10	μΑ	$V_{GS} = +8 \text{ V}, V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	-10	μΑ	$V_{GS} = -16 \text{ V}, V_{DS} = 0$
Drain to source leak current	I <sub>DSS</sub>	_	_	-1	μΑ	$V_{DS} = -30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	-1.0	_	-2.0	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$
Drain to source on state resistance	R <sub>DS(on)</sub>	_	138	173	mΩ	$I_D = -1.1 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note3}}$
	R <sub>DS(on)</sub>	_	216	303	mΩ	$I_D = -1.1 \text{ A}, V_{GS} = -4.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	y <sub>fs</sub>	1.2	2.1	_	S	$I_D = -1.1 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	195	_	pF	$V_{DS} = -10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	42	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	29	_	pF	
Turn - on delay time	t <sub>d(on)</sub>	_	19	_	ns	$I_D = -0.5 \text{ A}, V_{GS} = -10 \text{ V},$
Rise time	t <sub>r</sub>	_	25	_	ns	$R_L = 20 \Omega$ , $Rg = 4.7 \Omega$
Turn - off delay time	t <sub>d(off)</sub>	_	30	_	ns	
Fall time	t <sub>f</sub>	_	4.6	_	ns	
Total gate charge	Qg	_	4.2	_	nC	$V_{DD} = -10 \text{ V}, V_{GS} = -10 \text{ V},$
Gate to source charge	Qgs	_	0.7	_	nC	$I_D = -2.2A$
Gate to drain charge	Qgd	_	1.0	_	nC	
Body - drain diode forward voltage	$V_{DF}$	_	-0.9	_	V	$I_F = -1.5 \text{ A}, V_{GS} = 0^{\text{Note3}}$

Notes: 3. Pulse test

## **Main Characteristics**

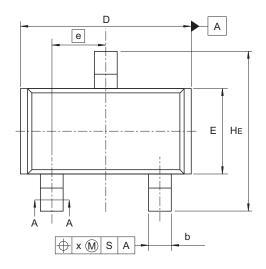


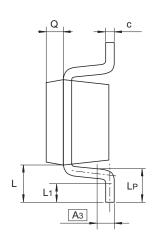


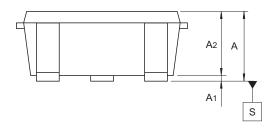


## **Package Dimensions**

JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
SC-59A	PLSP0003ZB-A	MPAK(T) / MPAK(T)V	0.011









Reference	Dimensions in millimeters		
Symbol	Min	Nom	Max
Α	1.0		1.3
A <sub>1</sub>	0	_	0.1
A <sub>2</sub>	1.0	1.1	1.2
A <sub>3</sub>	_	0.25	_
b	0.35	0.4	0.5
С	0.1	0.16	0.26
D	2.7	_	3.1
E	1.35	1.5	1.65
е	_	0.95	_
HE	2.2	2.8	3.0
L	0.35	_	0.75
L <sub>1</sub>	0.15	_	0.55
L <sub>P</sub>	0.25		0.65
Х			0.05
Q		0.3	

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## **Ordering Information**

Orderable Part Number	Quantity	Shipping Container
RQJ0302NGDQATL-H	3000 pcs.	φ178 mm reel, 8 mm Emboss taping

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