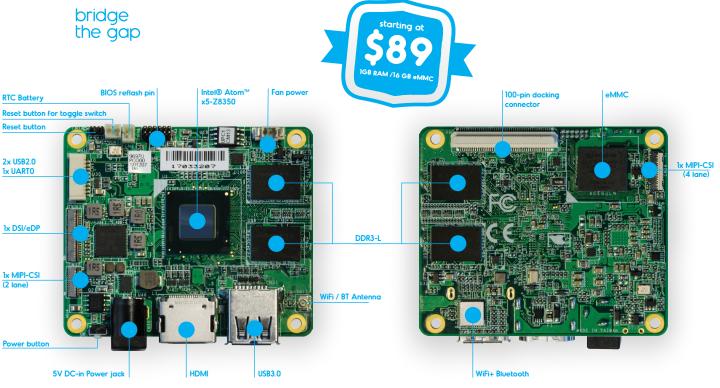


specification



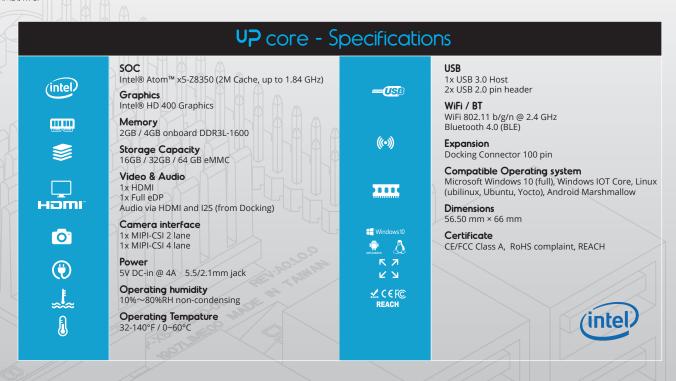
UP Core is a miniature board with the high performance and low power consumption features of the latest tablet technology: the Intel® $Atom^{IM}$ x5 Z8350 Processors (codename Cherry Trail) 64 bits up to 1.92GHz. The internal GPU is the new Intel Gen 8 HD 400 with 12 Execution Units up to 500MHz to deliver extremely high 3D graphic performance.

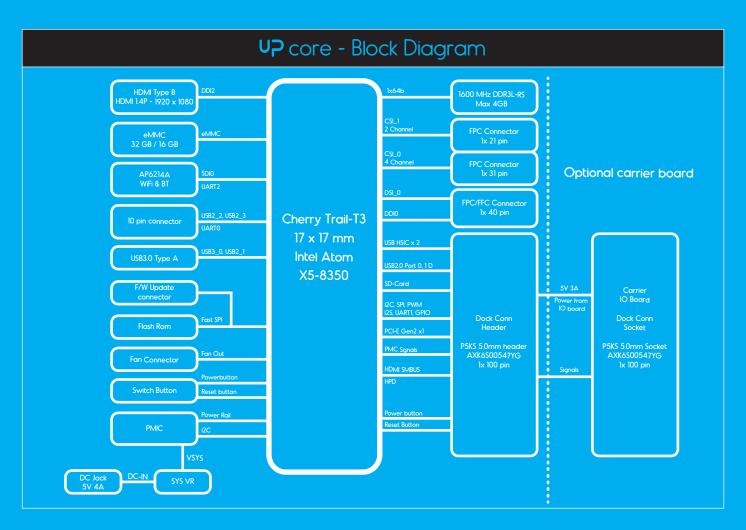
UP Core is equipped with 1GB/2GB/4GB DDR3L RAM and 16GB/32GB/64GB eMMC. With 100-pin docking connector, UP Core provides the freedom to makers to build up their carrier board. There are more interfaces available, such as 2x port USB2.0 + 1x UART on header, 1x USB 3.0 host, WiFi, Bluetooth 1x DSI/eDP port, 2x Camera (MIPI-CSI), 1x HMDI, RTC.

When it comes to security, UP Core has Intel security features needed for professional IoT applications such Intel AES New Instructions and Intel IdentityProtection Technology.

It's UP to you to choose which operation system is best for your application. The CPU is supported by Android 6.Marshmallow, Microsoft Windows 10 and we support and enable Linux, through our UP Community.

UP Core has a standard industrial PC operating temperature range of 32-140° F / 0-60°C, which makes it flexible for many applications.





UP core - Pin out

1 5V	② Ground	4) RESERVE	PCIE_RX0_DP	8) I2C1_SOC_SCL
2 5V	22 DDI2_DDC_CLK		62 Ground	82 SD3_WP
3 5V	33 GPIO7/HAT_SPI2_MOSI	43 Ground	63 PCIE_RX0_DN	83 Ground
4 5V	☑ DDI2_DDC_DAT	44 ISH_GPIO9		84 SD3_CLK
s 5V	35 GPIO8/SPI_MISO	45 GPIO18/I2S2_CLK	65 Ground	85 CPLD DOUT/ISH_I2C1_DATA
⊗ 5V	²⁶ HDMI_D	← GPIO25/PWM0 ←		8 SD3_SD0
🤨 5V	② GPIO9/SPI_CLK			87 ISH_I2C1_CLK
8 5V	28 HDMI_R	48 GPIO13/PWM1	68 Ground	88 SD3_SD1
Ground	ॐ GPIO22/SPI_CS0N			89 Ground
10 Ground	30 DDI2_TYPE_C_HPD	50 Ground	USB_OTG_R_ID ■	∞ SD3_SD2
n pmu_rstbtn_n	³⁾ GPIO23/SPI_CS1N	⑤ GPIO28/I2S2_DATAOUT	n Ground	9 RESERVE
12 UART1_RTS	32 ISH_GPIO0	52 USB_HSIC_1_DATA	72 Ground	2 SD3_SD3
¹³ PMU_PWRBTN_N	33 Ground	53 Ground	⁷³ I2C0_SOC_SDA	® RESERVE
4 UART1_CTS	³⁴ CPLD CLEAR/ISH_GPIO1	USB_HSIC_1_STROBE ■ Output ■	✓ SD3_CD	
⁽⁵⁾ PMU_SLP_SOIX_N	35 RESERVE	65 PCIE_TX0_DP	75 I2C0_SOC_SCL	95 RESERVE
6 GPIO16/UART1_TX	36 ISH_GPIO2	5₀ Ground	[™] SD3_CMD	
PCIE_CLKREQ0	37 RESERVE		77 Ground	77 RESERVE
® GPIO17/UART1_RX	33 ISH_GPIO3	58 USB_HSIC_2_DATA	⁷⁸ SD3_1P8_EN	CPLD_RST/GPIO_SUS8
99 PMC_SUSCLK0	RESERVE	59 Ground	12C1_SOC_SDA	
20 Ground	40 ISH_GPIO4		SD3_PWREN	© CPLD_STROBE/GPIO_SUS9

Part number : Coming soon

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