

RSR030N06FRA

Nch 60V 3A Power MOSFET

V_{DSS}	60V
R _{DS(on)} (Max.)	85m Ω
I _D	3A
P_{D}	1W

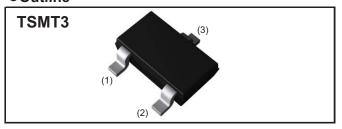
Features

- 1) Low on resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSMT3).
- 4) Pb-free lead plating; RoHS compliant
- 5) AEC-Q101 Qualified

Application

DC/DC converters

Outline



•Inner circuit

(1) Gate
(2) Source
(3) Drain

*1 ESD PROTECTION DIODE
*2 BODY DIODE

Packaging specifications

	Packaging	Taping			
	Reel size (mm)	180			
Type	Tape width (mm)	8			
Туре	Basic ordering unit (pcs)	3,000			
	Taping code	TL			
	Marking	PY			

● Absolute maximum ratings(T_a = 25°C)

Parameter	Symbol	Value	Unit
Drain - Source voltage	$V_{ extsf{DSS}}$	60	V
Continuous drain current	I _D *1	±3	А
Pulsed drain current	I _{D,pulse} *2	±12	А
Gate - Source voltage	V_{GSS}	±20	V
Dower discination	P _D *3	1.0	W
Power dissipation	P _D *4	0.54	W
Junction temperature	T _j	150	°C
Range of storage temperature	T_{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Unit
r arameter	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - ambient	R _{thJA} *3	-	-	125	°C/W
Thermal resistance, junction - ambient	R _{thJA} *4	-	-	231	°C/W

•Electrical characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
r al allietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = 1mA$	60	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	67	-	mV/°C
Zero gate voltage drain current	I _{DSS}	$V_{DS} = 60V, V_{GS} = 0V$	ı	ı	1	μА
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±10	μА
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V$, $I_D = 1mA$	1.0	-	2.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{(GS)th}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	-4.4	-	mV/°C
		V _{GS} =10V, I _D =3A	-	60	85	
Static drain - source	D *5	V _{GS} =4.5V, I _D =3A	-	70	100	O
on - state resistance	$R_{DS(on)}$	V_{GS} =4.0V, I_D =3A	-	75	105	mΩ
		V _{GS} =10V, I _D =3A, T _j =125°C	-	110	154	
Gate input resistannce	R _G	f = 1MHz, open drain	-	5	-	Ω
Transconductance	g _{fs} *5	$V_{DS} = 10V, I_{D} = 9A$	2.1	5.5	-	S

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw $\leq 10 \mu s, \, Duty \, cycle \leq 1\%$

^{*3} Mounted on a ceramic board (30×30×0.8mm)

^{*4} Mounted on a FR4(12×20×0.8mm)

^{*5} Pulsed

●Electrical characteristics(T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C _{iss}	V _{GS} = 0V	-	380	-	
Output capacitance	C _{oss}	V _{DS} = 10V	-	95	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	45	-	
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 30V, V_{GS} = 10V$	-	8	-	
Rise time	t _r *5	I _D = 1.5A	-	12	-	no
Turn - off delay time	t _{d(off)} *5	$R_L = 20\Omega$	-	30	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	10	-	

•Gate Charge characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Symbol Conditions -		Тур.	Max.	Offic
Total gate charge	+-	$V_{DD} \simeq 30V$, $I_D = 3A$ $V_{GS} = 5V$	-	5.0	-	
Total gate charge Q _g ⁻⁵	\mathcal{Q}_{g}	$V_{DD} \simeq 30V$, $I_D = 3A$ $V_{GS} = 10V$	-	9.0	18	nC
Gate - Source charge	Q _{gs} *5	$V_{DD} \simeq 30V$, $I_D = 3A$ $V_{GS} = 5V$	-	1.6	-	
Gate - Drain charge	Q _{gd} *5	$V_{GS} = 5V$	-	1.4	-	

●Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Symbol Conditions -		Values			Unit
r ai ai ii etei	Symbol	ymbol Conditions		Тур.	Max.	Offic
Inverse diode continuous, forward current	l _S *1	T _a = 25°C	-	-	0.8	А
Forward voltage	V _{SD} *5	$V_{GS} = 0V$, $I_s = 3A$	-	-	1.2	V

Fig.1 Power Dissipation Derating Curve

120
100
xem Global Au
20
0 50 100 150 200

Junction Temperature: Tj [°C]

Fig.2 Maximum Safe Operating Area 100 Operation in this area is limited by R_{DS}(on) $P_{W} = 100 \mu s$ $(V_{GS} = 10V)$ 10 Drain Current: I_D [A] $P_W = 1 ms$ 1 $P_W = 10 ms$ DC Operation 0.1 T_a=25°C Single Pulse Mounted on a ceramic board. $(30 \text{mm} \times 30 \text{mm} \times 0.8 \text{mm})$ 0.01 10 0.1 100

Drain - Source Voltage : V_{DS} [V]

Fig.4 Single Pulse Maxmum Power dissipation

Resistance vs. Pulse Width 10 Normalized Transient Thermal Resistance : r_(t) T_a=25°C Single Pulse 1 top D=1 D=0.5 D=0.1 0.1 D=0.05 D=0.01 bottom Signlep 0.01 Rth(ch-a)=125°C/W $Rth(ch-a)(t)=r(t) \times Rth(ch-a)$ Mounted on ceramic board $(30mm \times 30mm \times 0.8mm)$ 0.001 0.0001 0.01 100 Pulse Width: Pw [s]

Fig.3 Normalized Transient Thermal

1000

T_a=25°C
Single Pulse

1000

100

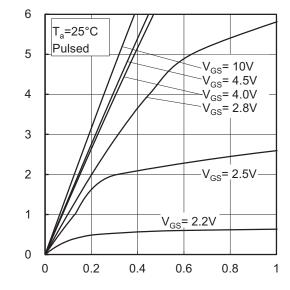
0.1
0.0001

Pulse Width: P_w [s]

Drain Current : I_D [A]

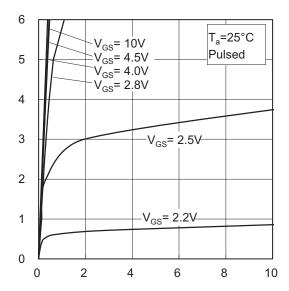
•Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage : V_{DS} [V]

Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

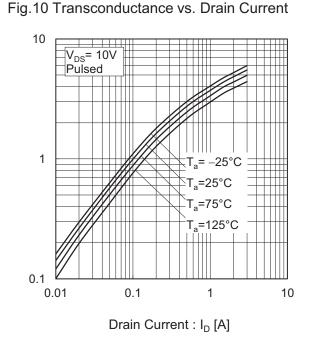
Drain Current: I_D [A]

Fig.7 Breakdown Voltage vs. Junction Temperature Drain - Source Breakdown Voltage: V_{(BR)DSS} [V] 100 V_{DS}=0V $I_D = 1 \text{mA}$ 80 pulsed 60 40 20 0 -50 0 50 100 Junction Temperature : T_i [°C]

Fig.8 Typical Transfer Characteristics 10 $V_{DS} = 10V$ Pulsed 1 Drain Current: I_D [A] $T_a = 125^{\circ}C^{\frac{1}{2}}$ T_a= 75°C T_a= 25°C 0.1 $T_a = -25$ °C 0.01 0.001 0 0.5 1 1.5 2.5 3

Gate - Source Voltage : V_{GS} [V]

Fig.9 Gate Threshold Voltage vs. Junction Temperature 3 $V_{DS}=10V$ Gate Threshold Voltage : $V_{GS(th)}[V]$ I_D=1mA pulsed 2 1 0 -50 0 50 150 100 Junction Temperature : T_i [°C]



Transconductance: g_{fs} [S]

150

Fig.11 Drain CurrentDerating Curve

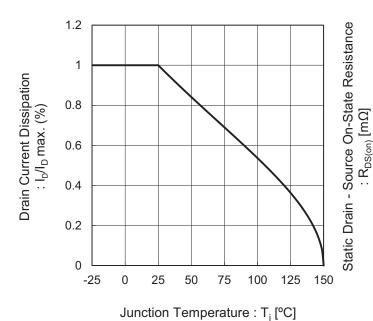
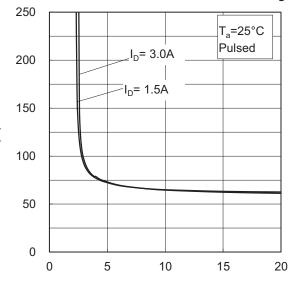


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



Gate - Source Voltage : V_{GS} [V]

Fig.13 Static Drain - Source On - State Resistance vs. Drain Current(I)

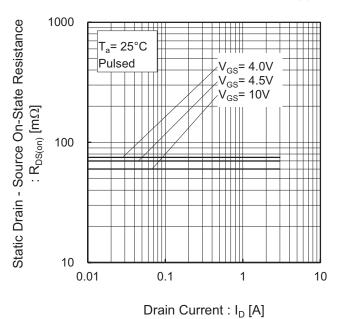
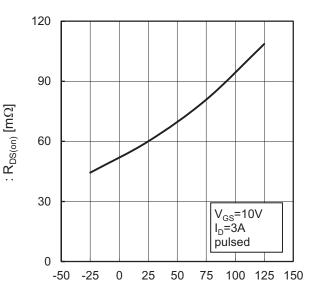
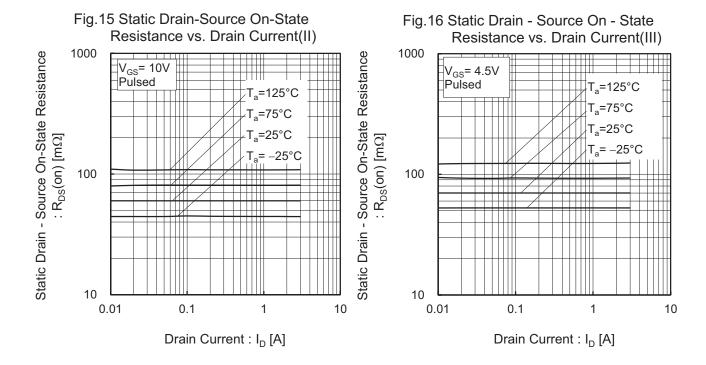


Fig.14 Static Drain - Source On - State Resistance vs. Junction Temperature



Junction Temperature : T_i [°C]

Static Drain - Source On-State Resistance



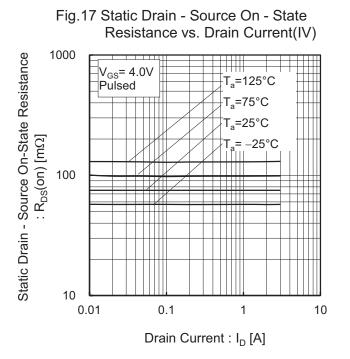


Fig.18 Typical Capacitance
vs. Drain - Source Voltage

1000

Ciss

T_a=25°C

f=1MHz
V_{GS}=0V

100

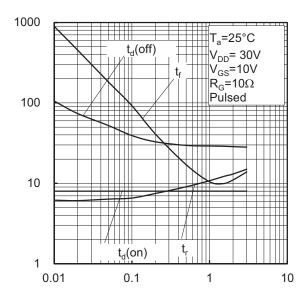
0.01

0.1

1 10

100

Fig.19 Switching Characteristics



Drain Current : I_D [A]

Drain - Source Voltage : V_{DS} [V]

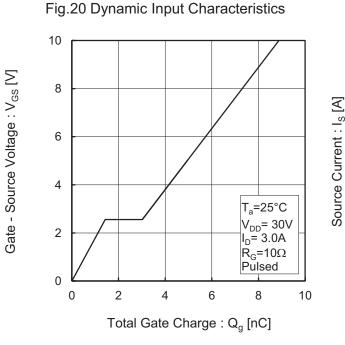
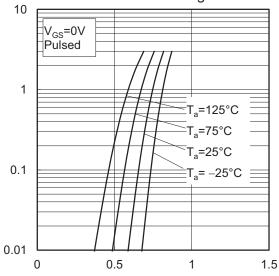


Fig.21 Source Current
vs. Source Drain Voltage



Source-Drain Voltage: V_{SD} [V]

Switching Time: t [ns]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

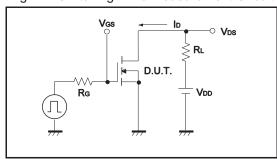


Fig.2-1 Gate Charge Measurement Circuit

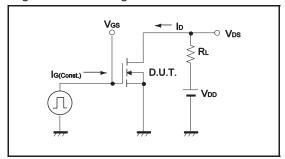


Fig.1-2 Switching Waveforms

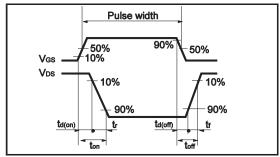
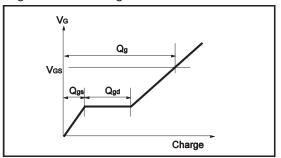


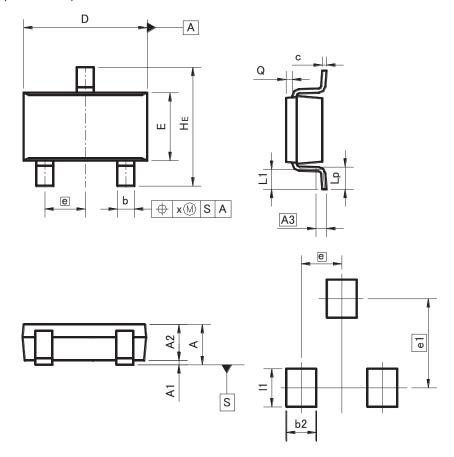
Fig.2-2 Gate Charge Waveform



10/11

● **Dimensions** (Unit: mm)

TSMT3



Patterm of terminal position areas

DIM	MILIMI	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	_	1.00	-	0.039
A1	0.00	0.10	0	0.004
A2	0.75	0.95	0.03	0.037
A3	0.5	25	0.0	01
b	0.35	0.50	0.014	0.02
С	0.10	0.26	0.004	0.01
D	2.80	3.00	0.11	0.118
E	1.50	1.80	0.059	0.071
е	0.0	95	0.0	04
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.05	0.25	0.002	0.01
х	_	0.20	_	0.008

DIM	MILIMI	ETERS	INCHES		
ואונט	MIN MAX		MIN	MAX	
e1	2.10		0.08		
b2		0.70	_	0.028	
11	_	0.90	-	0.035	

Dimension in mm/inches

Notice

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JAPAN	USA	EU	CHINA
CLASSII	СГУССШ	CLASSIIb	CL ACC III
CLASSIV	CLASSⅢ	CLASSIII	CLASSⅢ

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 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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Notice-PAA-E Rev.003



RSR030N06FRA - Web Page

Part Number	RSR030N06FRA
Package	TSMT3
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

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