

LOW-JITTER SAW OSCILLATOR (SPSO)
OUTPUT : LV-PECL, LVDS



Product Number (please contact us)
 EG-2121CB P: X1M000211xxxx00
 EG-2121CB L: X1M000231xxxx00
 EG-2102CB P: X1M000201xxxx00
 EG-2102CB L: X1M000221xxxx00

EG-2121/2102CB

- Frequency range : 100 MHz to 700 MHz
- Supply voltage : 2.5 V ... EG-2121CB
3.3 V ... EG-2102CB
- Output : LV-PECL or LVDS
- Function : Output enable (OE)
- External dimensions : 5.0 x 3.2 x 1.4 mm
- Low jitter and low phase noise by SAW unit.



Actual size



Specifications (characteristics)

Item	Symbol	LV-PECL		LVDS		Conditions / Remarks	
		EG-2121CB P	EG-2102CB P	EG-2121CB L	EG-2102CB L		
Output frequency range	f ₀	100 MHz to 700 MHz				Please contact us about available frequencies.	
Supply voltage	V _{CC}	2.5 V ±0.125 V	3.3 V ±0.33 V	2.5 V ±0.125 V	3.3 V ±0.33 V		
Storage temperature	T _{stg}	-55 °C to +125 °C				Storage as single product.	
Operating temperature	T _{use}	P:0 °C to +70 °C, R:-5 °C to +85 °C, S:-20 °C to +70 °C					
Frequency tolerance	f _{tol}	G: ±50 × 10 ⁻⁶ , H: ±100 × 10 ⁻⁶					
Current consumption	ICC	60 mA Max.		30 mA Max.		OE=V _{CC} , L _{ECL} =50 Ω or L _{LVDS} =100 Ω	
Disable current	I _{dis}	2 mA Max.		15 mA Max.		OE=GND	
Symmetry	SYM	45 % to 55 %				At outputs crossing point	
Output voltage (LV-PECL)	V _{OH}	1.55 V Typ., 2.35 V Typ.		-		DC characteristics	
		V _{CC} -1.025 V to V _{CC} -0.88 V					
	V _{OL}	0.80 V Typ., 1.60 V Typ.		-			
Output voltage (LVDS)	V _{OD}	-		350 mV Typ., 247 mV to 454 mV		DC characteristics	
	dV _{OD}	-		50 mV Max.			
	V _{OS}	-		1.25 V Typ., 1.125 V to 1.375 V			
	dV _{OS}	-		150 mV Max.			
Output load condition (ECL) / (LVDS)	L _{ECL}	50 Ω		-		Terminated to V _{CC} -2.0 V	
	L _{LVDS}	-		100 Ω		Connected between OUT to $\overline{\text{OUT}}$	
Input voltage	V _{IH}	70 % V _{CC} Min.				OE terminal	
	V _{IL}	30 % V _{CC} Max.					
Rise time / Fall time	t _r / t _f	400 ps Max.				Between 20 % and 80 % of (V _{OH} -V _{OL}). Between 20 % and 80 % of Differential Output Peak to Peak voltage.	
Start-up time	t _{str}	10 ms Max.				Time at minimum supply voltage to be 0 s	
Phase Jitter	t _{PJ}	0.23 ps Max.		0.27 ps Max.		100 MHz ≤ f ₀ < 150 MHz	
		0.22 ps Max.		0.24 ps Max.		150 MHz ≤ f ₀ < 200 MHz	
		0.21 ps Max.		0.23 ps Max.		200 MHz ≤ f ₀ < 300 MHz	
		0.18 ps Max.		0.19 ps Max.		300 MHz ≤ f ₀ < 400 MHz	
		0.16 ps Max.		0.16 ps Max.		400 MHz ≤ f ₀ < 500 MHz	
		0.14 ps Max.		0.14 ps Max.		500 MHz ≤ f ₀ < 600 MHz	
0.10 ps Max.		0.10 ps Max.		600 MHz ≤ f ₀ ≤ 700 MHz			
Frequency aging	f _{aging}	± 10 × 10 ⁻⁶ / year Max.				+25 °C, First year, V _{CC} =2.5 V, 3.3 V	

Product Name **EG-2121 CB 212.500000MHz P H P A** (ⓐⓑⓒ: GRA, GSA are not available)

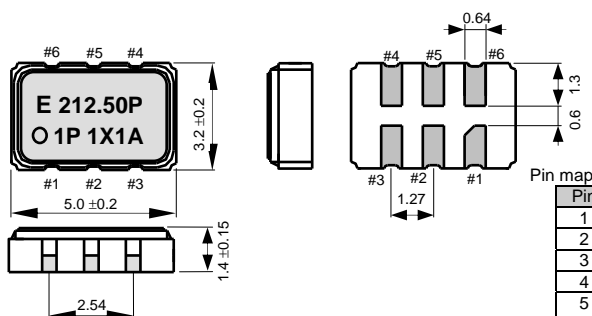
- (Standard form) ① ② ③ ④ⓐⓑⓒ
- ① Model ② Package type ③ Frequency
 - ④ Output (P:LV-PECL, L:LVDS)
 - ⓐ Frequency tolerance ③ Operating temperature
 - ⓑ Frequency aging (A*1: Frequency tolerance include aging, N*2: Frequency tolerance exclude aging)

ⓐ Frequency tolerance	ⓑ Operating temp.
G ±50 × 10 ⁻⁶	P 0 to +70 °C
H ±100 × 10 ⁻⁶	R -5 to +85 °C
	S -20 to +70 °C

*1 This includes initial frequency tolerance, temperature variation, supply voltage variation, reflow drift, and aging(+25 °C, 10 years).
 *2 This includes initial frequency tolerance, temperature variation, supply voltage variation, and reflow drift (except aging).

External dimensions

(Unit:mm)

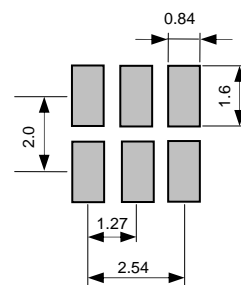


OE pin = HIGH : Specified frequency output.
 OE pin = LOW : Output is high impedance
 #2 and #3 are connected to the cover.

*) Standby function built-in.

Footprint (Recommended)

(Unit:mm)



To maintain stable operation, provide a 0.01 μF to 0.1 μF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between V_{CC} - GND).

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	► Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.
	► Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc.)

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