

RSQ015P10FRA

Pch -100V -1.5A Power MOSFET

V_{DSS}	-100V
R _{DS(on)} (Max.)	470mΩ
I _D	-1.5A
P_D	1.25W

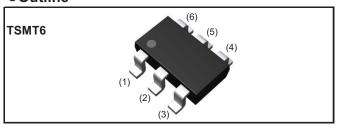
Features

- 1) Low on resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSMT6).
- 4) Pb-free lead plating; RoHS compliant
- 5) AEC-Q101 Qualified

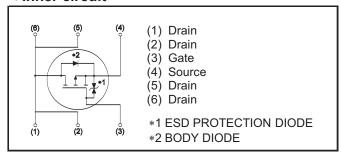
Application

DC/DC converters

Outline



•Inner circuit



Packaging specifications

	Packaging	Taping
	Reel size (mm)	180
Type	Tape width (mm)	8
Туре	Basic ordering unit (pcs)	3,000
	Taping code	TR
	Marking	ZN

● Absolute maximum ratings(T_a = 25°C)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V_{DSS}	-100	V
Continuous drain current	I _D *1	±1.5	А
Pulsed drain current	I _{D,pulse} *2	±6.0	А
Gate - Source voltage	V_{GSS}	±20	V
Dower dissination	P _D *3	1.25	W
Power dissipation	P _D *4	0.6	W
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Unit
raiametei	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - ambient	R _{thJA} *3	-	-	100	°C/W
Thermal resistance, junction - ambient	R _{thJA} *4	-	-	208	°C/W

•Electrical characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Min. Typ.			
Drain - Source breakdown voltage	V _{(BR)DSS}	$V_{GS} = 0V$, $I_D = -1mA$	-100	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = -1mA referenced to 25°C	ı	-109	1	mV/°C	
Zero gate voltage drain current	I _{DSS}	$V_{DS} = -100V, V_{GS} = 0V$	-	-	-1	μА	
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	ı	-	±10	μΑ	
Gate threshold voltage	V _{GS (th)}	$V_{DS} = -10V, I_{D} = -1mA$	-1.0	-	-2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{(GS)th}}{\Delta T_{j}}$	I _D = -1mA referenced to 25°C	-	3.2	-	mV/°C	
	*5	$V_{GS} = -10V, I_D = -1.5A$	-	350	470		
Static drain - source		V_{GS} = -4.5V, I_{D} = -0.75A	-	380	510	mΩ	
on - state resistance	$R_{DS(on)}$	$V_{GS} = -4.0V, I_D = -0.75A$	ı	400	540	11122	
		V _{GS} = -10V, I _D = -1.5A, T _j =125°C	-	610	850		
Gate input resistannce	R_G	f = 1MHz, open drain	-	8.5	-	Ω	
Transconductance	9 fs *5	$V_{DS} = -10V, I_{D} = -1.5A$	1.5	4.0	-	S	

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw $\leq 10 \mu s, \, Duty \, cycle \leq 1\%$

^{*3} Mounted on a ceramic board (30×30×0.8mm)

^{*4} Mounted on a FR4 (15×20×0.8mm)

^{*5} Pulsed

●Electrical characteristics(T_a = 25°C)

Parameter	Symbol	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	UIIII	
Input capacitance	C _{iss}	V _{GS} = 0V	-	950	-		
Output capacitance	C _{oss}	V _{DS} = -25V	-	45	-	pF	
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	20	-		
Turn - on delay time	$t_{d(on)}^{*5}$	$V_{DD} \simeq -50V$, $V_{GS} = -10V$	-	10	-		
Rise time	t _r *5	$I_D = -0.75A$	-	15	-	no	
Turn - off delay time	${\rm t_{d(off)}}^{*5}$	$R_L = 66\Omega$	-	60	-	ns	
Fall time	t _f *5	$R_G = 10\Omega$	-	10	-		

•Gate Charge characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
r ai ai nietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Total gate charge	Q_q^{*5}	$V_{DD} \simeq -50V, I_{D} = -1.5A$ $V_{GS} = -5V$	-	17.0	-	
Total gate charge		$V_{DD} \simeq -50V, I_D = -1.5A$ $V_{GS} = -10V$	-	32	-	nC
Gate - Source charge	Q _{gs} *5	$V_{DD} \simeq -50V, I_{D} = -1.5A$	-	4.5	-	
Gate - Drain charge	Q _{gd} *5	$V_{GS} = -5V$	-	5.0	-	

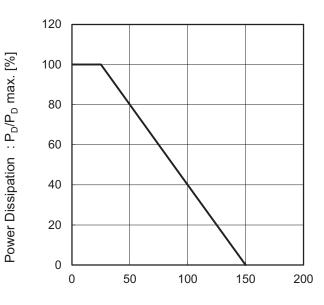
●Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Symbol Conditions -		Values			Unit
r ai ai i i e te i	Symbol	Conditions	Min.	Тур.	Max.	Offic
Inverse diode continuous, forward current	l _S *1	T _a = 25°C	-	-	-1.0	А
Forward voltage	V _{SD} *5	$V_{GS} = 0V, I_s = -1.5A$	-	-	-1.2	V

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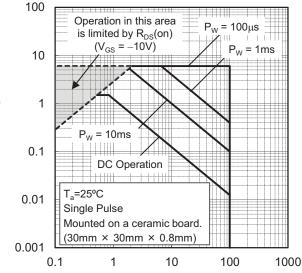
•Electrical characteristic curves

Fig.1 Power Dissipation Derating Curve



Drain Current: -I_D [A]

Fig.2 Maximum Safe Operating Area



Drain - Source Voltage : -V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

Junction Temperature : Tj [°C]

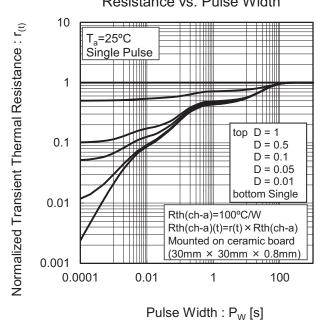
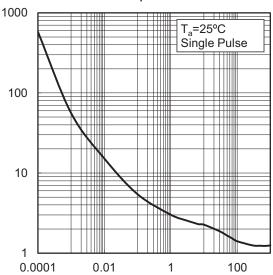


Fig.4 Single Pulse Maxmum Power dissipation



Pulse Width: Pw [s]

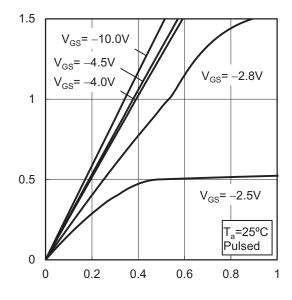
Peak Transient Power: P(W)

Drain Current: -I_D [A]

Drain - Source Breakdown Voltage : - $V_{(BR)DSS}$ [V]

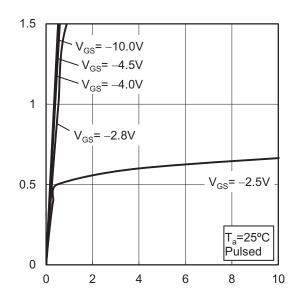
• Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)



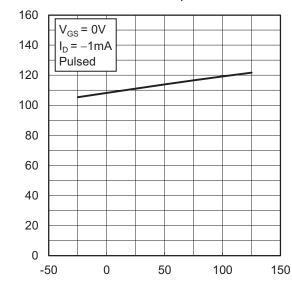
Drain - Source Voltage : -V_{DS} [V]

Fig.6 Typical Output Characteristics(II)



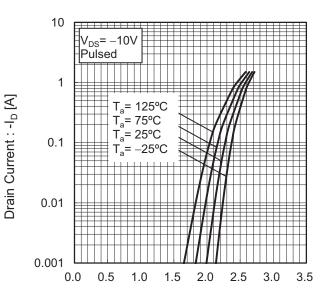
Drain - Source Voltage : -V_{DS} [V]

Fig.7 Breakdown Voltage vs. Junction Temperature



Junction Temperature : T_j [°C]

Fig.8 Typical Transfer Characteristics



Gate - Source Voltage : -V_{GS} [V]

Drain Current: -I_D [A]

Gate Threshold Voltage: -VGS(th) [V]

•Electrical characteristic curves

Fig.9 Gate Threshold Voltage

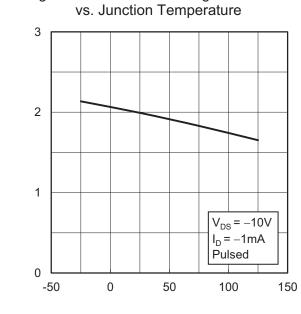
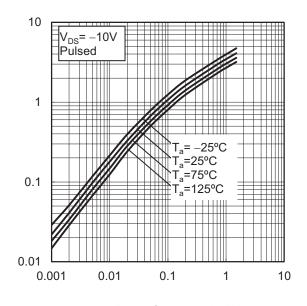


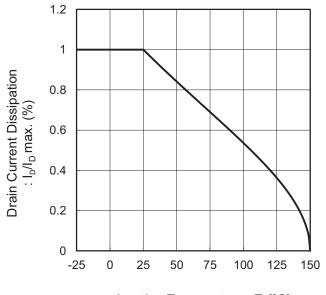
Fig.10 Transconductance vs. Drain Current



Junction Temperature : T_i [°C]

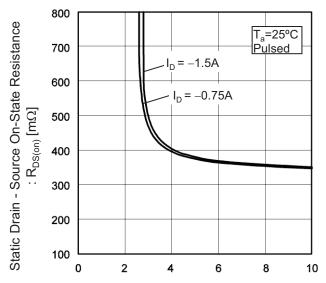
Drain Current : -I_D [A]

Fig.11 Drain CurrentDerating Curve



Junction Temperature : T_i [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

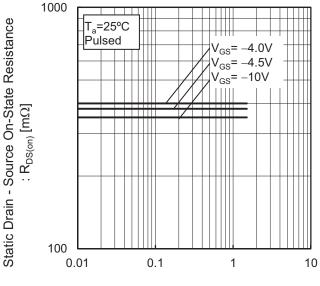


Gate - Source Voltage : -V_{GS} [V]

Transconductance: gfs [S]

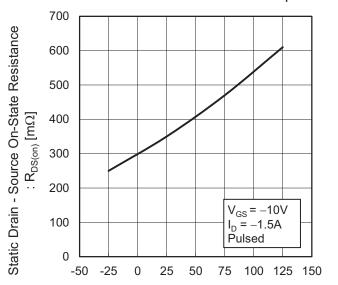
• Electrical characteristic curves

Fig.13 Static Drain - Source On - State Resistance vs. Drain Current(I)



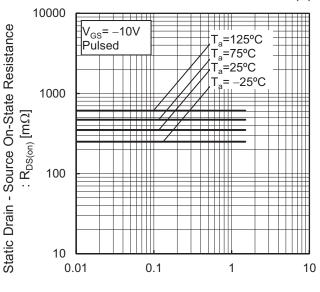
Drain Current : -I_D [A]

Fig.14 Static Drain - Source On - State
Resistance vs. Junction Temperature



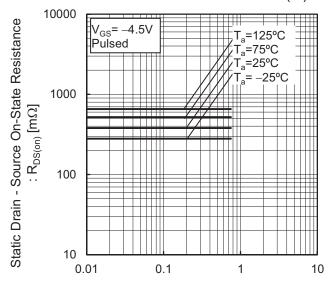
Junction Temperature : T_i [°C]

Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)



Drain Current : -I_D [A]

Fig.16 Static Drain-Source On-State
Resistance vs. Drain Current(III)



Drain Current : -I_D [A]

•Electrical characteristic curves

Fig.17 Static Drain - Source On - State Resistance vs. Drain Current(IV)

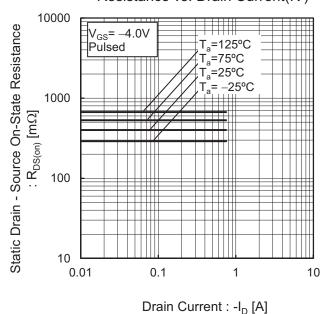
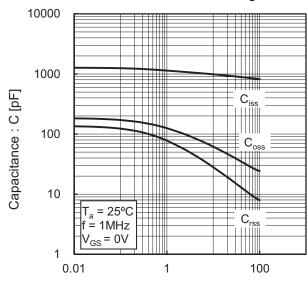


Fig.18 Typical Capacitance vs. Drain - Source Voltage



Drain - Source Voltage : -V_{DS} [V]

Fig.19 Switching Characteristics

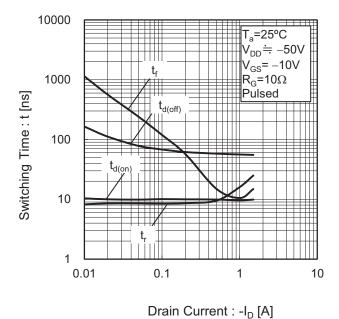
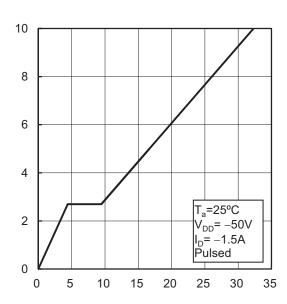


Fig.20 Dynamic Input Characteristics

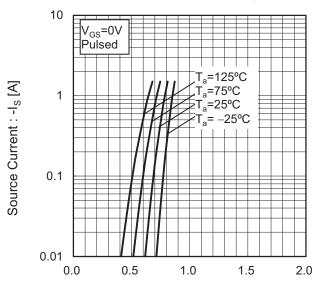


Total Gate Charge : Q_q [nC]

Gate - Source Voltage : - $\mathsf{V}_{\mathsf{GS}}\left[\mathsf{V}
ight]$

•Electrical characteristic curves

Fig.21 Source Current vs. Source Drain Voltage



Source-Drain Voltage : $-V_{SD}$ [V]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

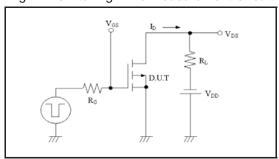


Fig.2-1 Gate Charge Measurement Circuit

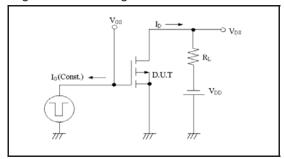


Fig.1-2 Switching Waveforms

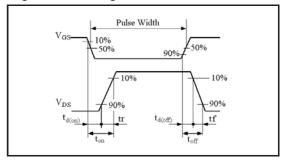
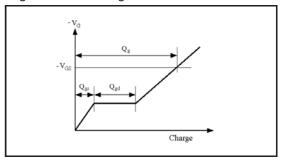
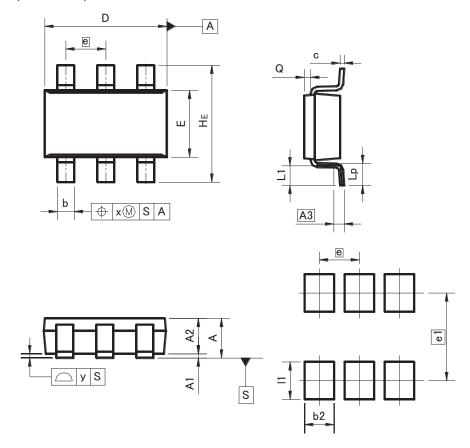


Fig.2-2 Gate Charge Waveform



● **Dimensions** (Unit: mm)





Patterm of terminal position areas

DIM	MILIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	-	1.00	-	0.039	
A1	0.00	0.10	0	0.004	
A2	0.75	0.95	0.03	0.037	
A3	0.3	25	0.0	01	
b	0.35	0.50	0.014	0.02	
С	0.10	0.26	0.004	0.01	
D	2.80	3.00	0.11	0.118	
E	1.50	1.80	0.059	0.071	
е	0.9	95	0.0	04	
HE	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.05	0.25	0.002	0.01	
Х	_	0.20	_	0.008	
У	_	0.10	_	0.004	

DIM	MILIMETERS		INCHES		
ואונט	MIN	MAX	MIN	MAX	
e1	2.10		0.08		
b2		0.70	ı	0.028	
l1	ı	0.90	ı	0.035	

Dimension in mm/inches

Notice

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CLASSII		CLASSIIb	CL ACC III
CLASSIV	CLASSⅢ	CLASSIII	CLASSⅢ

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 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
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- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

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- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
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- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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