

RVQ040N05FRA

Nch 45V 4A Power MOSFET

V _{DSS}	45V
R _{DS(on)} (Max.)	$53 \mathrm{m}\Omega$
I _D	4A
P_D	1.25W

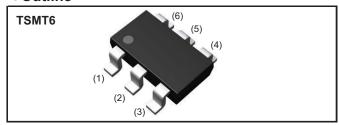
Features

- 1) Low on resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSMT6).
- 4) Pb-free lead plating; RoHS compliant
- 5) AEC-Q101 Qualified

Application

DC/DC converters

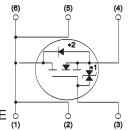
Outline



•Inner circuit

- (1) Drain
- (2) Drain
- (3) Gate
- (4) Source
- (5) Drain
- (6) Drain





Packaging specifications

	Packaging	Taping
Reel s	Reel size (mm)	180
Typo	Tape width (mm)	8
Туре	Basic ordering unit (pcs)	3,000
	Taping code	TR
	Marking	QG

● Absolute maximum ratings(T_a = 25°C)

Parameter	Symbol	Value	Unit
Drain - Source voltage	$V_{ extsf{DSS}}$	45	V
Continuous drain current	I _D *1	±4.0	А
Pulsed drain current	I _{D,pulse} *2	±16	А
Gate - Source voltage	V_{GSS}	±21	V
Power dissipation	P _D *3	1.25	W
Power dissipation	P _D *4	0.6	W
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

●Thermal resistance

Parameter	Symbol	Values			Unit
Parameter	Symbol	Min.	Тур.	Max.	Offic
Thermal resistance, junction - ambient	R _{thJA} *3	-	-	100	°C/W
Thermal resistance, junction - ambient	R _{thJA} *4	-	-	208	°C/W

•Electrical characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit	
r ai ai nietei	Symbol	Conditions	Min.	Тур.	Max.	Offic	
Drain - Source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V$, $I_D = 1mA$	45	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	ı	42	1	mV/°C	
Zero gate voltage drain current	I _{DSS}	$V_{DS} = 45V, V_{GS} = 0V$	ı	-	1	μΑ	
Gate - Source leakage current	I_{GSS}	$V_{GS} = \pm 21V, V_{DS} = 0V$	ı	-	±10	μΑ	
Gate threshold voltage	V _{GS (th)}	$V_{DS} = 10V$, $I_D = 1mA$	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{(GS)th}}{\Delta T_{j}}$	I _D = 1mA referenced to 25°C	-	-4.2	-	mV/°C	
		V _{GS} =10V, I _D =4A	-	38	53		
Static drain - source	. *5	V _{GS} =4.5V, I _D =4A	-	47	66	m()	
on - state resistance	$R_{DS(on)}$	V_{GS} =4V, I_D =4A	ı	53	74	mΩ	
		V _{GS} =10V, I _D =4A, T _j =125°C	ı	70	100		
Gate input resistannce	R_{G}	f = 1MHz, open drain	-	7	-	Ω	
Transconductance	9 fs *5	$V_{DS} = 10V, I_{D} = 4A$	3	6	-	S	

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw $\leq 10 \mu s, \, Duty \, cycle \leq 1\%$

^{*3} Mounted on a ceramic board (30×30×0.8mm)

^{*4} Mounted on a FR4 (15×20×0.8mm)

•Electrical characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input capacitance	C _{iss}	V _{GS} = 0V	-	530	-	
Output capacitance	C _{oss}	V _{DS} = 10V	-	120	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	65	-	
Turn - on delay time	$t_{d(on)}^{*5}$	$V_{DD} \simeq 25V, V_{GS} = 10V$	-	12	-	
Rise time	t _r *5	I _D = 2A	-	15	-	no
Turn - off delay time	t _{d(off)} *5	$R_L = 12.5\Omega$	-	40	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	12	-	

•Gate Charge characteristics($T_a = 25$ °C)

Parameter	Symbol	Conditions	Values			Unit		
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic		
Total gate charge	+-	$V_{DD} \simeq 25V$, $I_D = 4A$ $V_{GS} = 5V$	-	6.3	8.8			
Total gate charge	Q_g	∢ g	\ \ \	$V_{DD} \simeq 25V$, $I_D = 4A$ $V_{GS} = 10V$	-	10	-	nC
Gate - Source charge	Q _{gs} *5	$V_{DD} \simeq 25V$, $I_D = 4A$ $V_{GS} = 5V$	-	2.0	-			
Gate - Drain charge	Q _{gd} *5	$V_{GS} = 5V$	-	2.6	-			

●Body diode electrical characteristics (Source-Drain)(T_a = 25°C)

Parameter	Symbol Conditions		Values			Unit
r ai ai ii etei	Symbol	Symbol Conditions		Тур.	Max.	Offic
Inverse diode continuous, forward current	l _S *1	T _a = 25°C	-	-	1.6	А
Forward voltage	V _{SD} *5	$V_{GS} = 0V, I_s = 4.0A$	-	-	1.2	V

^{*5} Pulsed

Fig.1 Power Dissipation Derating Curve

120

100

80

40

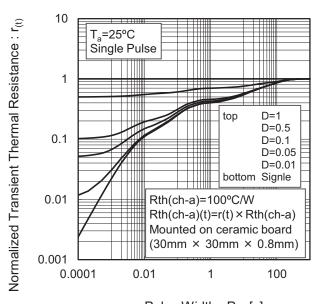
20

Junction Temperature : T_i [°C]

Fig.2 Maximum Safe Operating Area 100 Operation in this area $P_{W} = 100 \mu s$ is limited by R_{DS}(on) / (V_{GS} = 10V) $P_W = 1 ms$ 10 Drain Current: I_D [A] 1 $P_W = 10 ms$ DC Operation 0.1 T_a=25°C Single Pulse Mounted on a ceramic board. $(30mm \times 30mm \times 0.8mm)$ 0.01 0.1 10 100

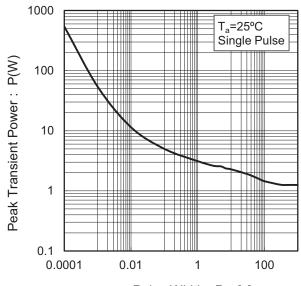
Drain - Source Voltage : V_{DS} [V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width



Pulse Width : Pw [s]

Fig.4 Single Pulse Maximum Power dissipation

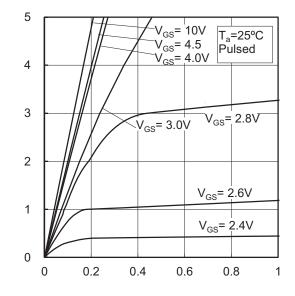


Pulse Width: Pw [s]

Drain Current : I_D [A]

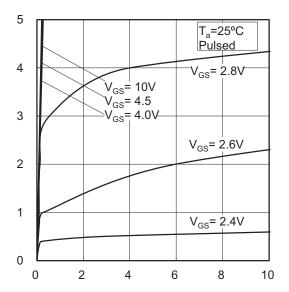
•Electrical characteristic curves

Fig.5 Typical Output Characteristics(I)



Drain - Source Voltage : V_{DS} [V]

Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : V_{DS} [V]

Fig.7 Breakdown Voltage vs. Junction Temperature Drain - Source Breakdown Voltage: V_{(BR)DSS} [V] 100 V_{GS}=0V $I_D = 1 \text{mA}$ 80 pulsed 60 40 20 0 -50 0 50 100 150 Junction Temperature : T_i [°C]

Gate - Source Voltage : V_{GS} [V]

Fig.8 Typical Transfer Characteristics

Fig.9 Gate Threshold Voltage vs. Junction Temperature 3 V_{DS}=10V Gate Threshold Voltage : $V_{GS(th)}[V]$ I_D=1mA pulsed 2 1 0 -50 0 50 100 150 Junction Temperature : T_i [°C]

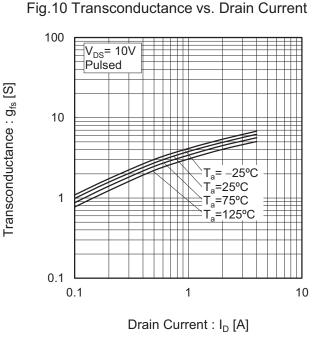


Fig.11 Drain Current Derating Curve

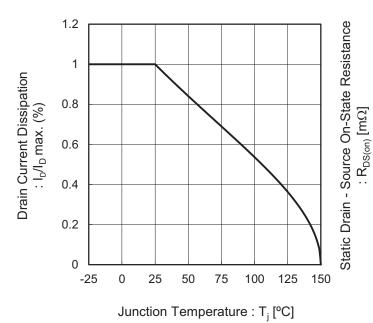
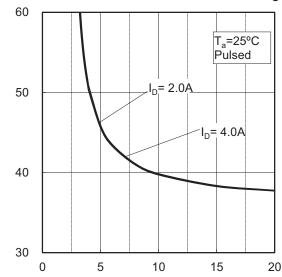
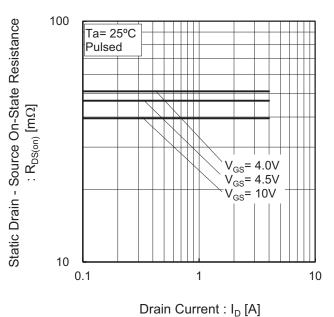


Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage



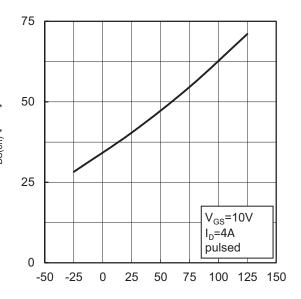
Gate - Source Voltage : V_{GS} [V]

Fig.13 Static Drain - Source On - State Resistance vs. Drain Current(I)

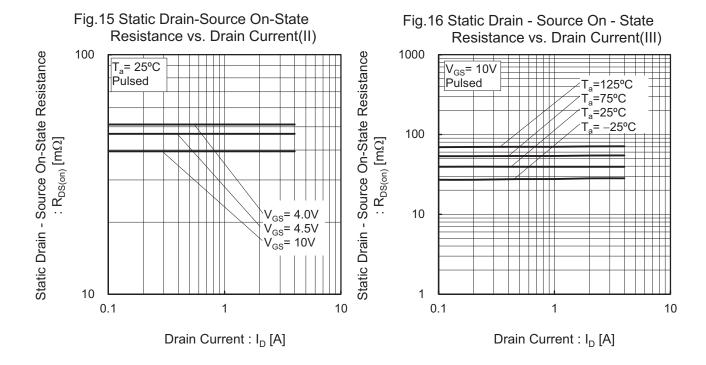


Static Drain - Source On-State Resistance : $R_{DS(on)} \, [m\Omega]$

Fig.14 Static Drain - Source On - State Resistance vs. Junction Temperature



Junction Temperature : T_i [°C]



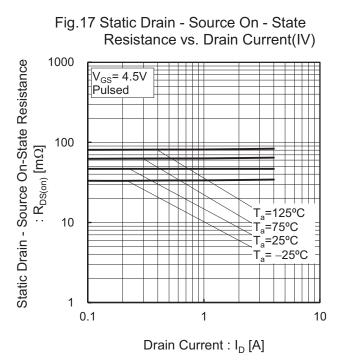
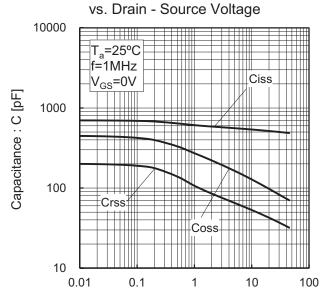
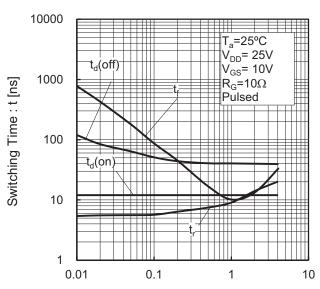


Fig.18 Typical Capacitance



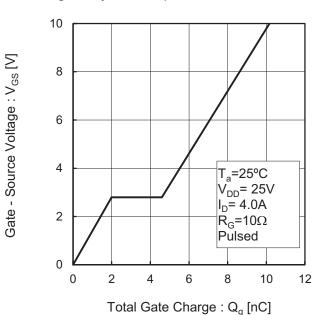
Drain - Source Voltage : V_{DS} [V]

Fig.19 Switching Characteristics



Drain Current : I_D [A]

Fig.20 Dynamic Input Characteristics



Source Current : I_S [A]

Fig.21 Source Current vs. Source Drain Voltage 10 V_{GS}=0V Pulsed 1 T_a=125°C T_a=75°C T_a=25°C 0.1 T_a= -25°C 0.01 0 0.5 1 1.5

Source-Drain Voltage: V_{SD} [V]

Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

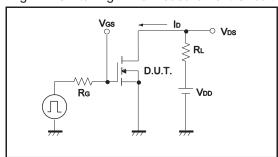


Fig.2-1 Gate Charge Measurement Circuit

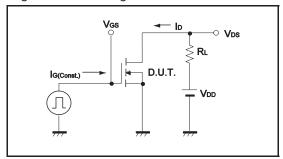


Fig.1-2 Switching Waveforms

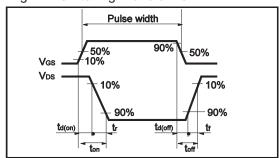
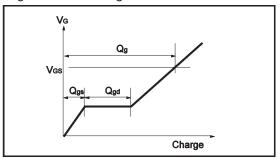
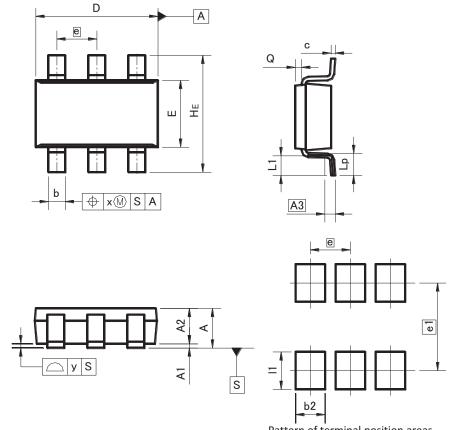


Fig.2-2 Gate Charge Waveform



●Dimensions (Unit:mm)





Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	_	1.00	_	0.039
A1	0.00	0.10	0.000	0.004
A2	0.75	0.95	0.030	0.037
A3	0.3	25	0.0	10
b	0.35	0.50	0.014	0.020
С	0.10	0.26	0.004	0.010
D	2.80	3.00	0.110	0.118
E	1.50	1.80	0.059	0.071
е	0.9	95	0.037	
HE	2.60	3.00	0.102	0.118
L1	0.30	0.60	0.012	0.024
Lp	0.40	0.70	0.016	0.028
Q	0.05	0.25	0.002	0.010
Х	_	0.20	_	0.008
У	_	0.10	_	0.004

DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
b2		0.70	_	0.028
e1	2.10		0.0	83
- 11	_	0.90	_	0.035

Dimension in mm / inches

Notice

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JAPAN	USA	EU	CHINA
CLASSII	СГУССШ	CLASSIIb	CL ACC III
CLASSIV	CLASSⅢ	CLASSIII	CLASSⅢ

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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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Notice-PAA-E Rev.003



RVQ040N05FRA - Web Page

Part Number	RVQ040N05FRA
Package	TSMT6
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes

X-ON Electronics

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