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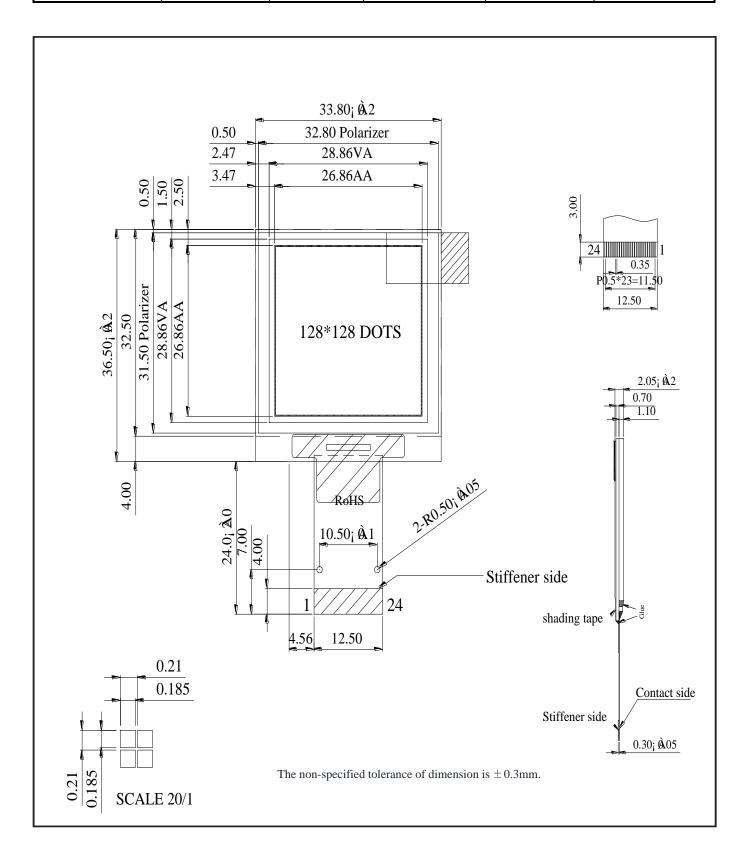
MCOT128128C1V-YM	128	x 128	128 Yellow		Yellow OL		Yellow OL		OLED Module
		Specifi	cation						
Version: 1		-	Date: 16/05	5/201	17				
		Revis	sion						
0	29/06/2016	First re	elease						

Display F			
Resolution	128 x 128		
Appearance	Yellow on Black) HC
Logic Voltage	3V		NoHS ompliant
Interface	Parallel / SPI / I2C	\ \ CC	ompliant
Module Size	33.80 x 36.50 x 2.05 mm		-
Operating Temperature	-40°C ~ +80°C	Box Quantity	Weight / Display
Construction	TAB		

Display Accessories				
Part Number	Description			
MPBV7	30 Way FFC to cable and wires. Driven by any driver board that can be wired to a 1mm pitch SHDR-30V-S-B receptacle.			
MCIB12	UC32 Breakout Board with SD card and LED back light driver. Used in conjunction with MPBV7.			

Optional Variants			
Appearance	Voltage		
White on Black			

Mechanical Specifications						
Module Size	dule Size 36.98 x 41.23 x 2.05 (With Backlight) W x H x D mm					
Viewing Area	32.00 x 32.00	W x H mm	Hole-to-Hole		W x H mm	
Dot Size	0.185 x 0.185	W x H mm	Dot Pitch	0.21 x 0.21	W x H mm	



MCOT128128C1V-YM	128 x 128	Yellow	OLED Module
		Specification	
Version: 1		Date: 16/05/2017	
		Revision	

1 VSS Ground. Connect to external ground. 2 VCC Power Supply for driving voltage. Positive power voltage supply pin. 3 VCOMH VSS. 4 VCI Signal deselected voltage level. Capacitor between here and VSS. 5 Low Voltage power supply. Should match with MCU interface voltage level and must connect to external source. 6 Must always be 2 VDD. 6 VDD Power Supply pin for core logic operation. 7 MCU bus interface selection pins. Select appropriate logic setting, as described below. (Note: "0" is connected to VSS and "1" is connected to VCI) 1 IZC = BS1: 1 BS2: 0 1 IZC = BS1: 1 BS2: 0 1 IZC = BS1: 1 BS2: 1 2 B-bit 80XX = BS1: 0 BS2: 1 3 B-bit 80XX = BS1: 0 BS	Pin layout							
2 VCC Power Supply for driving voltage. Positive power voltage supply pin. 3 VCOMH COM signal deselected voltage level. Capacitor between here and VSS. 4 VCI voltage power supply. Should match with MCU interface voltage level and must connect to external source. Must always be ≥ VDD. 5 VDD Power Supply pin for core logic operation. MCU bus interface selection pins. Select appropriate logic setting, as described below. (Note: "0" is connected to VSS and "1" is connected to VCI) 12 C = BS1: 1 BS2: 0 4-wire SPI = BS1: 0 BS2: 0 4-wire SPI = BS1: 0 BS2: 1 8 VSS Ground Pin, must connect to external ground. 9 IREF Segment output current reference pin. 10 CS# Chip Select Input connecting to MCU. Chip is enabled for MCU communication when CS# is pulled Low. Reset Signal Input. Initialisation is executed when pulled Low. Keep pulled High during normal operation. 11 RES# Initialisation is executed when pulled Low. Keep pulled High during normal operation. 12 D/C command control pin connect to MCU. High= Data at D(7:0) interpreted as data. Low= Data at D(7:0) transferred to command register. 12 Lor mode = SA0 for slave address selection. 3-Wire SPI = Connect to VSS. Read / Write input pin, connecting to MCU interface. 800 Mode= RW (RWW) selection input, read mode carried out when pulled Low and chip is selected. 800 Mode= RW (RWW) selection input, read mode carried out when pulled Low and chip is selected. 800 Mode= RW (RWH) signal pin, Read/Write initiated when pin is pulled Low and chip is selected. 800 Mode= RW (RWH) signal pin, read operation initiated when pin is pulled Low and chip is selected. 800 Mode= Canalle (E) signal pin, Read/Write initiated when pin is pulled Low and chip is selected. 800 Mode= Canalle (E) signal pin, Read/Write initiated when pin is pulled Low and chip is selected. 800 Mode= Canalle (E) signal pin, Read/Write initiated when pin is pulled the Low and chip is selected. 800 Mode= Canalle (E) signal pin, Sead via the pin is pulled the Low and chip is selected. 800 Mod	Pin	Symbol	Description	Remarks				
ycommunication ycomm	1	VSS	Ground. Connect to external ground.					
VCUINT VSS. Low Voltage power supply. Should match with MCU interface voltage level and must connect to external source. Must always be ≥ VDD. Power Supply pin for core logic operation. MCU bus interface selection pins. Select appropriate logic setting, as described below: (Note: "0" is connected to VSS and "1" is connected to VCI) IZC = BS1: 1 BS2: 0 4-wire SP1 = BS1: 0 BS2: 0 4-wire SP1 = BS1: 0 BS2: 1 8-bit 80XX = BS1: 0 BS2: 1 8-bit 80XX = BS1: 1 BS2: 1 8 VSS Ground Pin, must connect to external ground. 9 IREF Segment output current reference pin. Chip Select Input connecting to MCU. Chip is enabled for MCU communication when CS# is pulled Low. Reset Signal Input. Initialisation is executed when pulled Low. Keep pulled High during normal operation. Data / Command control pin connect to MCU. High= Data at D(7:0) interpreted as data. Low= Data at D(7:0) transferred to command register. I2C mode = SAd for slave address selection. 3-Wirs SP1 = Connect to VSS Read / Write input pin, connecting to MCU interface. 8800 Modes RW (RW/#) selection input, read mode carried out when pulled High, write mode when Low. 8800 Modes RW (RW/#) selection input, read mode carried out when pulled High, write mode when Low. 8800 Modes RW (RW/#) selection input, read mode carried out when pulled High, write mode when Low. 8800 Modes RW (RW/#) spleation input, read mode carried out when pulled High, write mode when Low. 8800 Modes RW (RW/#) spleation input, read mode carried out when pulled High and chip is selected. 8800 Modes Read (RCP) signal pin, read operation initiated when pin is pulled Low and chip is selected. 8800 Modes Read (RCP) signal pin, read operation initiated when pin is pulled Low and chip is selected. 9800 Modes Read (RCP) signal pin, read operation initiated when pin is pulled Low and chip is selected. 9800 Modes Read (RCP) signal pin, read operation initiated when pin is pulled Low and chip is selected. 9800 Modes Read (RCP) signal pin. 9800 Modes Read (RCP) signal pin. 9800 Modes	2	VCC						
4 VCI voltage level and must connect to external source. Must always be ≥ VDD. 5 VDD Power Supply pin for core logic operation. 6 BS1 MCU bus interface selection pins. Select appropriate logic setting, as described below: (Note: "0" is connected to VSS and "1" is connected to VCI) I2C = BS1: 1 BS2: 0 7 BS2 4-wire SPI = BS1: 0 BS2: 0 8 - VSS Ground Pin, must connect to external ground. 9 IREF Segment output current reference pin. 10 CS# Chip Select Input connecting to MCU. Chip is enabled for MCU communication when CS# is pulled Low. 11 RES# Initialisation is executed when pulled Low. Keep pulled High during normal operation. 12 D/C Command control pin connect to MCU. High= Data at D(7:0) transferred to command register. 12 D/C Command control pin connect to MCU interface. 13 W/R# Read / Write input pin, connecting to MCU interface. 14 Read / Write input pin, connecting to MCU interface. 15 B00 Mode= RAW (R/WH) selection input, read mode carried out when pulled High, write mode when Low. 16 MCU Interface Input. 17 B00 Mode= Enable (E) signal pin, Read/Write initiated when pin is pulled Low and chip is selected. 1	3	VCOMH	VSS.					
MCU bus interface selection pins. Select appropriate logic setting, as described below: (Note: "0" is connected to VCI)	4	VCI	voltage level and must connect to external source.					
as described below: (Note: "0" is connected to VSS and "1" is connected to VCI) 12C = BS1: 1 BS2: 0 4-wire SPI = BS1: 0 BS2: 0 8-bit 68XX = BS1: 1 BS2: 1 8 VSS Ground Pin, must connect to external ground. 9 IREF Segment output current reference pin. 10 CS# Chip Select Input connecting to MCU. Chip is enabled for MCU communication when CS# is pulled Low. Reset Signal Input. 11 RES# Initialisation is executed when pulled Low. Keep pulled High during normal operation. Data / Command control pin connect to MCU. High= Data at D(7:0) interpreted as data. Low= Data at D(7:0) transferred to command register. 12 D/C command register. 12 D/C while spile of the state of th	5	VDD	Power Supply pin for core logic operation.					
IZC = BS1: 1 BS2: 0	6	BS1	as described below: (Note: "0" is connected to VSS and "1" is					
9 IREF Segment output current reference pin. 10 CS# Chip Select Input connecting to MCU. Chip is enabled for MCU communication when CS# is pulled Low. Reset Signal Input. 11 RES# Initialisation is executed when pulled Low. Keep pulled High during normal operation. 12 D/C Data / Command control pin connect to MCU. High= Data at D(7:0) interpreted as data. Low= Data at D(7:0) interpreted as data. Low= Data at D(7:0) transferred to command register. 12 I2 mode = SA0 for slave address selection. 3-Wire SPI = Connect to VSS Read / Write input pin, connecting to MCU interface. 6800 Mode= RW (RW#) selection input, read mode carried out when pulled High, write mode when Low. 8080 Mode= WR (W/R#) input, data write initiated when pin is pulled Low and chip is selected. MCU Interface Input. 6800 Mode= Enable (E) signal pin, Read/Write initiated when pin is pulled Low and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 12 Cor SPI selected = Connect to VSS. 15 D0 16 D1 17 D2 18 D3 SPI Mode= D0 will be Serial Clock input (SCLK). D1 will be the Serial Data input (SDIN) and D2 should be kept NC. 12 Mode= D2 and D1 should be tied together and serve as SDAout, SDAin in application and D0 is Serial Clock input (SCL).	7	BS2	I2C = BS1: 1 BS2: 0 4-wire SPI = BS1: 0 BS2: 0 8-bit 68XX = BS1: 0 BS2: 1					
Chip Select Input connecting to MCU. Chip is enabled for MCU communication when CS# is pulled Low. Reset Signal Input. Initialisation is executed when pulled Low. Keep pulled High during normal operation. Data / Command control pin connect to MCU. High= Data at D(7:0) interpreted as data. Low= Data at D(7:0) transferred to command register. I2C mode = SA0 for slave address selection. 3-Wire SPI = Connect to VSS Read / Write input pin, connecting to MCU interface. 6800 Mode= RW (R/W#) selection input, read mode carried out when pulled High, write mode when Low. 8080 Mode= WR (W/R#) input, data write initiated when pin is pulled Low and chip is selected. MCU Interface Input. 6800 Mode= Enable (E) signal pin, Read/Write initiated when pin is pulled High and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 8080 Mod	8	VSS	Ground Pin, must connect to external ground.					
Chip is enabled for MCU communication when CS# is pulled Low. Reset Signal Input. RES# Initialisation is executed when pulled Low. Keep pulled High during normal operation. Data / Command control pin connect to MCU. High= Data at D(7:0) interpreted as data. Low= Data at D(7:0) transferred to command register. 12 D/C command register. 12 E mode = SA0 for slave address selection. 3-Wire SPI = Connect to VSS Read / Write input pin, connecting to MCU interface. 6800 Mode= R/W (R/W#) selection input, read mode carried out when pulled High, write mode when Low. 8080 Mode= WR (W/R#) input, data write initiated when pin is pulled Low and chip is selected. MCU Interface Input. 6800 Mode= Enable (E) signal pin, Read/Write initiated when pin is pulled High and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 12 D0 Bi-directional data bus connecting to MCU data bus. Unused pins to tie low. SPI Mode= D0 will be Serial Clock input (SCLK), D1 will be the Serial Data input (SDIN) and D2 should be kept NC. 12 D6 D7 Power Supply for panel driving voltage. Supplied externally.	9	IREF	Segment output current reference pin.					
Initialisation is executed when pulled Low. Keep pulled High during normal operation. Data / Command control pin connect to MCU. High= Data at D(7:0) interpreted as data. Low= Data at D(7:0) transferred to command register. 12 D/C	10	CS#						
D/C	11	RES#	Initialisation is executed when pulled Low. Keep pulled High					
Read / Write input pin, connecting to MCU interface. 6800 Mode= R/W (R/W#) selection input, read mode carried out when pulled High, write mode when Low. 8080 Mode= WR (W/R#) input, data write initiated when pin is pulled Low and chip is selected. MCU Interface Input. 6800 Mode= Enable (E) signal pin, Read/Write initiated when pin is pulled High and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 12C or SPI selected = Connect to VSS. 15 D0 16 D1 17 D2 Bi-directional data bus connecting to MCU data bus. Unused pins to tie low. SPI Mode= D0 will be Serial Clock input (SCLK). D1 will be the Serial Data input (SDIN) and D2 should be kept NC. 12C Mode= D2 and D1 should be tied together and serve as SDAout, SDAin in application and D0 is Serial Clock input (SCL). 21 D6 22 D7 Power Supply for panel driving voltage. Supplied externally.	12	D/C	D(7:0) interpreted as data. Low= Data at D(7:0) transferred to command register. I2C mode = SA0 for slave address selection.					
MCU Interface Input. 6800 Mode= Enable (E) signal pin, Read/Write initiated when pin is pulled High and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected. 12C or SPI selected = Connect to VSS. 15 D0 16 D1 17 D2 Bi-directional data bus connecting to MCU data bus. Unused pins to tie low. SPI Mode= D0 will be Serial Clock input (SCLK). D1 will be the Serial Data input (SDIN) and D2 should be kept NC. 12C Mode= D2 and D1 should be tied together and serve as SDAout, SDAin in application and D0 is Serial Clock input (SCL). 21 D6 22 D7 23 VCC Power Supply for panel driving voltage. Supplied externally.	13	W/R#	Read / Write input pin, connecting to MCU interface. 6800 Mode= R/W (R/W#) selection input, read mode carried out when pulled High, write mode when Low. 8080 Mode= WR (W/R#) input, data write initiated when pin is					
16 D1 17 D2 18 D3 SPI Mode= D0 will be Serial Clock input (SCLK). D1 will be the Serial Data input (SDIN) and D2 should be kept NC. I2C Mode= D2 and D1 should be tied together and serve as SDAout, SDAin in application and D0 is Serial Clock input (SCL). 21 D6 22 D7 23 VCC Power Supply for panel driving voltage. Supplied externally.	14	RD#	MCU Interface Input. 6800 Mode= Enable (E) signal pin, Read/Write initiated when pin is pulled High and chip is selected. 8080 Mode= Read (RD#) signal pin, read operation initiated when pin is pulled Low and chip is selected.					
Bi-directional data bus connecting to MCU data bus. Unused pins to tie low. SPI Mode= D0 will be Serial Clock input (SCLK). D1 will be the Serial Data input (SDIN) and D2 should be kept NC. I2C Mode= D2 and D1 should be tied together and serve as SDAout, SDAin in application and D0 is Serial Clock input (SCL). D6 22 D7 VCC Power Supply for panel driving voltage. Supplied externally.	15	D0						
to tie low. SPI Mode= D0 will be Serial Clock input (SCLK). D1 will be the Serial Data input (SDIN) and D2 should be kept NC. I2C Mode= D2 and D1 should be tied together and serve as SDAout, SDAin in application and D0 is Serial Clock input (SCL). D6 D7 VCC Power Supply for panel driving voltage. Supplied externally.	16	D1						
18 D3 SPI Mode= D0 will be Serial Clock input (SCLK). D1 will be the Serial Data input (SDIN) and D2 should be kept NC. I2C Mode= D2 and D1 should be tied together and serve as SDAout, SDAin in application and D0 is Serial Clock input (SCL). 21 D6 SDAOut, SDAin in application and D0 is Serial Clock input (SCL). 22 D7 Power Supply for panel driving voltage. Supplied externally.	17	D2						
Serial Data input (SDIN) and D2 should be kept NC. I2C Mode= D2 and D1 should be tied together and serve as SDAout, SDAin in application and D0 is Serial Clock input (SCL). D6 D7 VCC Power Supply for panel driving voltage. Supplied externally.	18	D3						
20 D5 SDAout, SDAin in application and D0 is Serial Clock input (SCL). 21 D6 22 D7 23 VCC Power Supply for panel driving voltage. Supplied externally.	19	D4	Serial Data input (SDIN) and D2 should be kept NC.					
21 D6 22 D7 23 VCC Power Supply for panel driving voltage. Supplied externally.	20	D5						
22 D7 23 VCC Power Supply for panel driving voltage. Supplied externally.	21	D6						
	22	D7						
	23	VCC	Power Supply for panel driving voltage. Supplied externally.					
	24	VSS						

MCOT128128C1V-YM	128 x 128	Yellow	OLED Module
		Specification	
Version: 1		Date: 16/05/2017	
		Revision	

Absolute Maximums Ratings						
Item	Symbol	Minimum	Typical	Maximum	Unit	
Supply Voltage for Display	VCC	-0.50		19.00	V	
Supply Voltage for Logic	VDD	-0.50		2.75	V	
Supply Voltage for Operation	VCI	-0.30		4.00	V	
Operating Temperature	TOP	-40		80	°C	
Storage Temperature	TSTG	-40		80	°C	

Electronic Characteristics							
Item	Item Symbol Condition Minimum Typical Maximum Unit						
Input High Voltage	VIH		0.80		VDD	V	
Input Low Voltage	VIL		GND		0.20	V	
Output High Voltage	VOH		0.90		VDD	V	
Output Low Voltage	VOL		GND		0.10	V	
Supply Voltage for Logic	VDD		2.80	3.00	3.30	V	
Supply Voltage for Display	VCC		14.00	14.50	15.00	V	
50% Checkboard Operating Current.	IDD	VDD=14.5V	23.00	24.00	26.00	mA	

	OLED Characteristics						
Item	Item Symbol Condition Minimum Typical Maximum Unit						
Viewing Angle	(V)θ		160			Deg	
Viewing Angle	(Η)φ		160			Deg	
Contrast Ratio	CR	Dark	2000:1				
Dooponee Time	T Rise			10		μs	
Response Time	T Fall			10		μs	
Display with 50% Checkboard Brightness		60	80		cd/m ²		
CIEx(Blue) (CIE193		(CIE1931)	0.45	0.47	0.49		
CIEy(Blu	ie)	(CIE1931)	0.48	0.50	0.52		

OLED Life Time					
Item	Conditions	Typical	Remark		
Operating Life Time	Ta=25°C. Initial checkboard brightness, 50%.	20,000 Hours			

MCOT128128C1V-YM	128 x 128	Yellow	OLED Module
Specification Specification			
Version: 1		Date: 16/05/2017	
Revision			

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