



# PAA110 Dual Single-Pole, Normally Open OptoMOS<sup>®</sup> Relays

Parameters	Ratings	Units
Load Voltage	400	V <sub>P</sub>
Load Current	150	mA <sub>rms</sub> / mA <sub>DC</sub>
On-Resistance (max)	22	Ω

### Features

- $3750V_{rms}$  Input/Output Isolation
- Low Drive Power Requirements (TTL/CMOS Compatible)
- High Reliability
- Arc-Free With No Snubbing Circuits
- VDE Compatible
- FCC Compatible
- No EMI/RFI Generation
- Small 8-Pin Package
- Machine Insertable, Wave Solderable
- Surface Mount and Tape Reel Version Available.

### **Applications**

- Telecommunications
  - Telecom Switching
  - Tip/Ring Circuits
  - Modem Switching (Laptop, Notebook, Pocket Size)
  - Hook Switch
  - Dial Pulsing
  - Ground Start
  - Ringing Injection
- Instrumentation
  - Multiplexers
  - Data Acquisition
  - Electronic Switching
  - I/O Subsystems
- Meters (Watt-Hour, Water, Gas)
- Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

#### Description

PAA110 is a 400V, 150mA, 22 $\Omega$ , dual normally open (1-Form-A) optically isolated Solid State Relay. Its efficient MOSFET switches and photovoltaic die use IXYS Integrated Circuits Division's patented OptoMOS architecture to provide 3750V<sub>rms</sub> of input to output isolation. The optically coupled output is controlled by a highly efficient GaAIAs infrared LED

This performance leader provides high peak load voltage handling capability and improved peak load current handling.

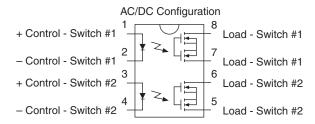
#### Approvals

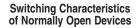
- UL Recognized Component: File E76270
- CSA Certified Component: Certificate 1175739
- EN/IEC 60950-1 Certified Component: TUV Certificate B 09 07 49410 004

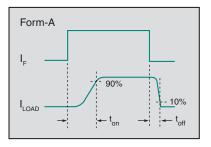
#### **Ordering Information**

Part #	Description
PAA110	8-Pin DIP (50/Tube)
PAA110P	8-Pin Flatpack (50/Tube)
PAA110PTR	8-Pin Flatpack (1000/Reel)
PAA110S	8-Pin Surface Mount (50/Tube)
PAA110STR	8-Pin Surface Mount (1000/Reel)

### Pin Configuration











### Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	400	V <sub>P</sub>
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	Α
Input Power Dissipation <sup>1</sup>	150	mW
Total Power Dissipation <sup>2</sup>	800	mW
Isolation Voltage, Input to Output	3750	V <sub>rms</sub>
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

<sup>1</sup> Derate linearly 1.33 mW / °C

<sup>2</sup> Derate linearly 6.67 mW / °C

### **Electrical Characteristics @ 25°C**

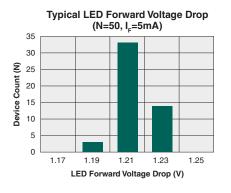
Parameter	Conditions	Symbol	Min	Тур	Max	Units
Output Characteristics						
Load Current *						
Continuous, AC/DC Configuration	-	I,	-	-	150	mA <sub>rms</sub> / mA <sub>DC</sub>
Peak	t=10ms	ILPK	-	-	±400	mA <sub>P</sub>
On-Resistance, AC/DC Configuration	l <sub>i</sub> =150mA	R <sub>ON</sub>	-	15	22	Ω
Off-State Leakage Current	V <sub>1</sub> =400V <sub>P</sub>	ILEAK	-	-	1	μA
Switching Speeds						
Turn-On		t <sub>on</sub>	-	-	1	
Turn-Off	I <sub>F</sub> =5mA, V <sub>L</sub> =10V	t <sub>off</sub>	-	-	0.25	ms
Output Capacitance	V <sub>L</sub> =50V, f=1MHz	C <sub>OUT</sub>	-	25	-	pF
Input Characteristics				1		
Input Control Current to Activate	I <sub>L</sub> =150mA	l <sub>F</sub>	-	-	5	mA
Input Control Current to Deactivate	-	l <sub>F</sub>	0.4	0.7	-	mA
Input Voltage Drop	I <sub>F</sub> =5mA	V <sub>F</sub>	0.9	1.2	1.4	V
Reverse Input Current	V <sub>R</sub> =5V	I <sub>B</sub>	-	-	10	μA
Common Characteristics				·		
Input to Output Capacitance	-	C <sub>I/O</sub>	-	3	-	pF

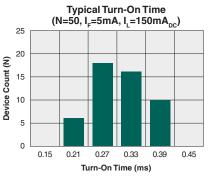
\*NOTE: If both poles operate simultaneously, then load current must be derated in order not to exceed the package power dissipation value.

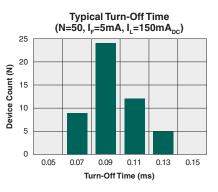


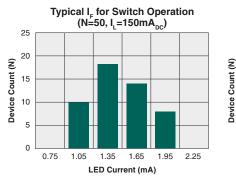
## PAA110

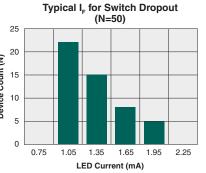
### PERFORMANCE DATA @25°C (Unless Otherwise Noted)\*

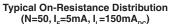


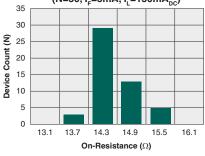


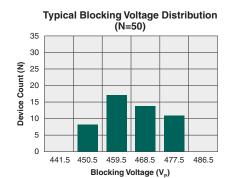


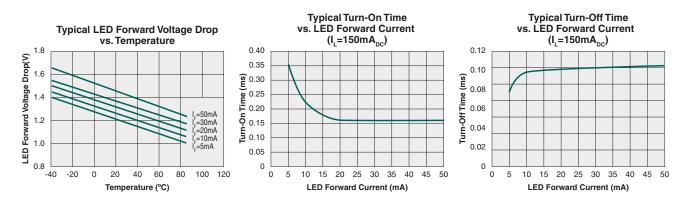








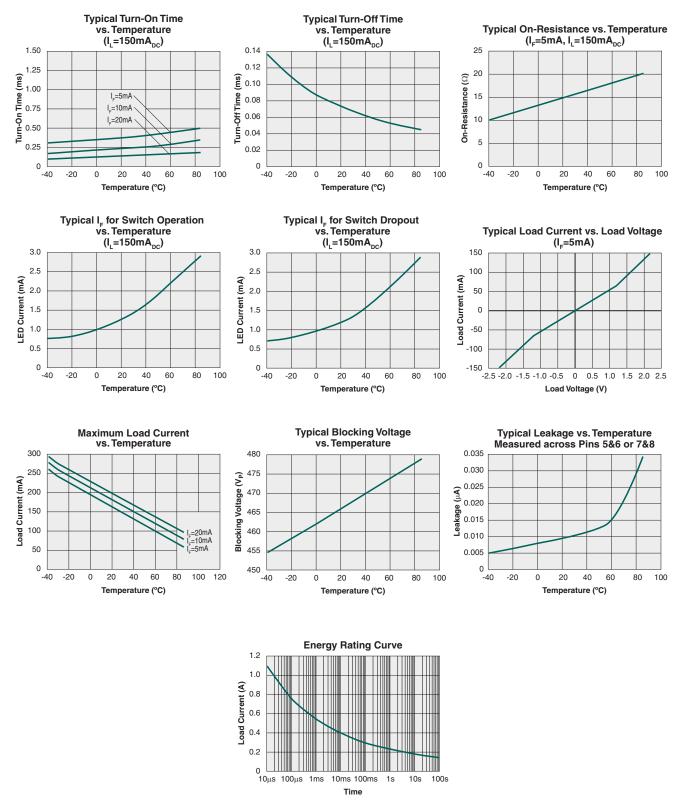




\*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.



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\*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.



### **Manufacturing Information**

#### **Moisture Sensitivity**

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits Division classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating	
PAA110 / PAA110S / PAA110P	MSL 1	

#### **ESD Sensitivity**



This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

#### **Reflow Profile**

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
PAA110 / PAA110S	250°C for 30 seconds
PAA110P	260°C for 30 seconds

#### **Board Wash**

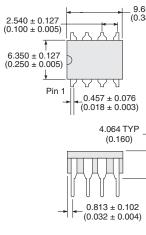
IXYS Integrated Circuits Division recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since IXYS Integrated Circuits Division employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake could be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

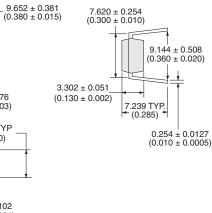




### **Mechanical Dimensions**



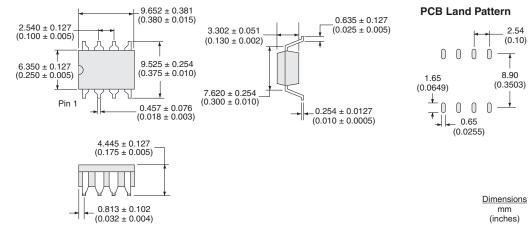




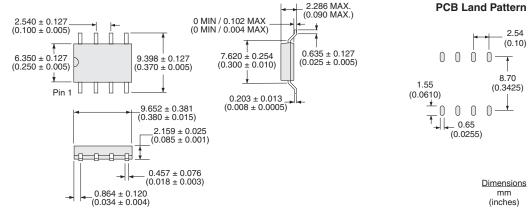
PCB Hole Pattern  $\begin{array}{c} 8-0.800 \text{ DIA.} \\ (8-0.031 \text{ DIA.}) \\ \hline \\ 6.350 \pm 0.127 \\ (0.250 \pm 0.005) \\ \hline \\ 7.620 \pm 0.127 \\ (0.300 \pm 0.005) \\ \hline \\ 7.620 \pm 0.127 \\ (0.300 \pm 0.005) \\ \hline \\ 7.620 \pm 0.127 \\ (0.300 \pm 0.005) \\ \hline \end{array}$ 

> Dimensions mm (inches)

#### **PAA110S**

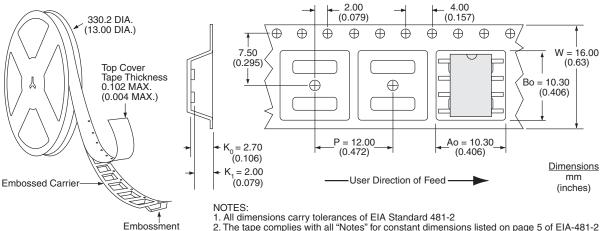


#### **PAA110P**



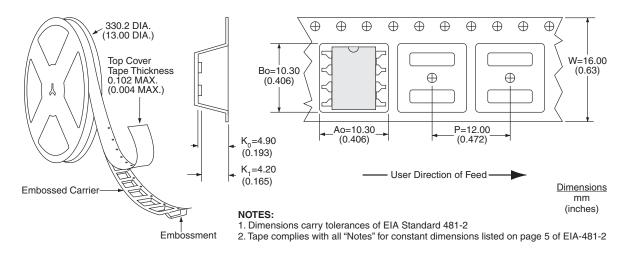


### PAA110PTR Tape & Reel



2. The tape complies with all "Notes" for constant dimensions listed on page 5 of EIA-481-2

### PAA110STR Tape & Reel



#### For additional information please visit our website at: www.ixysic.com

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