## Low-power dual buffer with open-drain output

Rev. 8 — 17 September 2015

**Product data sheet** 

### 1. General description

The 74AUP2G07 provides two non-inverting buffers with open-drain output. The output of the device is an open drain and can be connected to other open-drain outputs to implement active-LOW wired-OR or active-HIGH wired-AND functions.

Schmitt-trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire  $V_{CC}$  range from 0.8 V to 3.6 V.

This device ensures a very low static and dynamic power consumption across the entire  $V_{CC}$  range from 0.8 V to 3.6 V.

This device is fully specified for partial power-down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

### 2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
  - JESD8-12 (0.8 V to 1.3 V)
  - JESD8-11 (0.9 V to 1.65 V)
  - JESD8-7 (1.2 V to 1.95 V)
  - JESD8-5 (1.8 V to 2.7 V)
  - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
  - HBM JESD22-A114F Class 3A exceeds 5000 V
  - MM JESD22-A115-A exceeds 200 V
  - CDM JESD22-C101E exceeds 1000 V
- Low static-power consumption;  $I_{CC} = 0.9 \ \mu A \ (maximum)$
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V<sub>CC</sub>
- I<sub>OFF</sub> circuitry provides partial power-down mode operation
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

# nexperia

## 3. Ordering information

#### Table 1. Ordering information

Type number	Package						
	Temperature range Name		Description	Version			
74AUP2G07GW	–40 °C to +125 °C	SC-88	plastic surface-mounted package; 6 leads	SOT363			
74AUP2G07GM	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 $\times$ 1.45 $\times$ 0.5 mm	SOT886			
74AUP2G07GF	–40 °C to +125 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 $\times$ 1 $\times$ 0.5 mm	SOT891			
74AUP2G07GN	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $0.9 \times 1.0 \times 0.35$ mm	SOT1115			
74AUP2G07GS	–40 °C to +125 °C	XSON6	extremely thin small outline package; no leads; 6 terminals; body $1.0 \times 1.0 \times 0.35$ mm	SOT1202			
74AUP2G07GX	–40 °C to +125 °C	X2SON6	plastic thermal extremely thin small outline package; no leads; 6 terminals; body 1 $\times$ 0.8 $\times$ 0.35 mm	SOT1255			

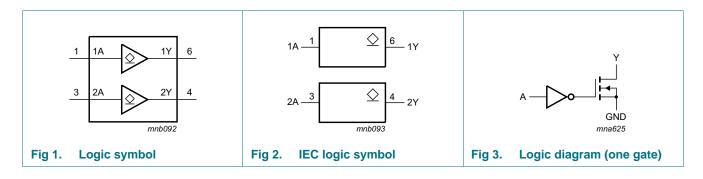
### 4. Marking

#### Table 2. Marking

Type number	Marking code <sup>[1]</sup>
74AUP2G07GW	p7
74AUP2G07GM	p7
74AUP2G07GF	p7
74AUP2G07GN	p7
74AUP2G07GS	p7
74AUP2G07GX	p7

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

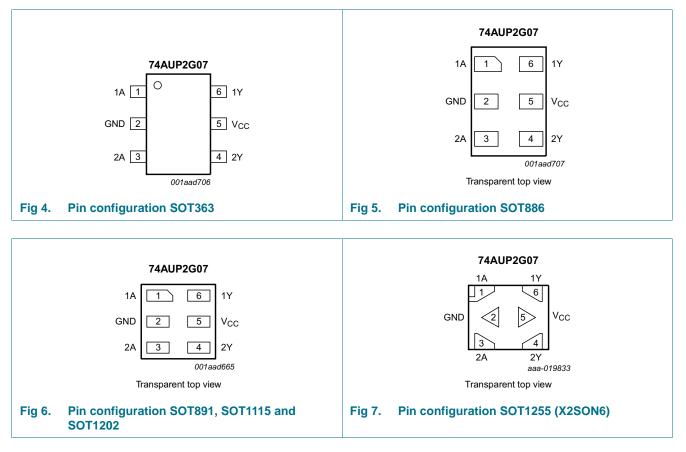
### 5. Functional diagram



Low-power dual buffer with open-drain output

### 6. Pinning information

### 6.1 Pinning



### 6.2 Pin description

Table 3.   Pin description		
Symbol	Pin	Description
1A	1	data input
GND	2	ground (0 V)
2A	3	data input
2Y	4	data output
V <sub>CC</sub>	5	supply voltage
1Y	6	data output

74AUP2G07 Product data sheet

### 7. Functional description

Input	Output
nA	nY
L	L
Н	Z

[1] H = HIGH voltage level; L = LOW voltage level; Z = high-impedance OFF state.

### 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-0.5	+4.6	V
I <sub>IK</sub>	input clamping current	V <sub>1</sub> < 0 V	-50	-	mA
VI	input voltage	[1]	-0.5	+4.6	V
I <sub>OK</sub>	output clamping current	V <sub>O</sub> < 0 V	-50	-	mA
Vo	output voltage	Active mode and Power-down mode [1]	-0.5	+4.6	V
I <sub>O</sub>	output current	$V_{O} = 0 V$ to $V_{CC}$	-	20	mA
I <sub>CC</sub>	supply current		-	50	mA
I <sub>GND</sub>	ground current		-50	-	mA
T <sub>stg</sub>	storage temperature		-65	+150	°C
P <sub>tot</sub>	total power dissipation	$T_{amb} = -40 \text{ °C to } +125 \text{ °C}$ [2]	-	250	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SC-88 package: above 87.5 °C the value of P<sub>tot</sub> derates linearly with 4.0 mW/K.

For X2SON6 and XSON6 packages: above 118 °C the value of  $P_{tot}$  derates linearly with 7.8 mW/K.

### 9. Recommended operating conditions

#### Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode and Power-down mode	0	3.6	V
T <sub>amb</sub>	ambient temperature		-40	+125	°C
$\Delta t / \Delta V$	input transition rise and fall rate	V <sub>CC</sub> = 0.8 V to 3.6 V	0	200	ns/V

74AUP2G07 Product data sheet

## **10. Static characteristics**

#### Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T <sub>amb</sub> = 2	5 °C					
VIH	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	$0.70\times V_{CC}$	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	$0.65 \times V_{CC}$	-	-	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
VIL	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	$0.30\times V_{CC}$	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		V <sub>CC</sub> = 2.3 V to 2.7 V	-	-	0.7	V
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	-	-	0.9	V
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_O$ = 20 $\mu\text{A};V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		$I_{O} = 1.1 \text{ mA}; V_{CC} = 1.1 \text{ V}$	-	-	$0.3 \times V_{CC}$	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.31	V
		$I_{O}$ = 1.9 mA; $V_{CC}$ = 1.65 V	-	-	0.31	V
		$I_{O}$ = 2.3 mA; $V_{CC}$ = 2.3 V	-	-	0.31	V
		$I_0 = 3.1 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.44	V
		$I_0 = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.31	V
		$I_0 = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.44	V
l <sub>l</sub>	input leakage current	$V_I = GND$ to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.1	μA
I <sub>OZ</sub>	OFF-state output current	$V_{I} = V_{IH}$ ; $V_{O} = 0$ V to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.1	μΑ
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O}$ = 0 V to 3.6 V; $V_{CC}$ = 0 V	-	-	±0.2	μΑ
$\Delta I_{OFF}$	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.2	μΑ
I <sub>CC</sub>	supply current	$\label{eq:VI} \begin{array}{l} V_{I} = GND \text{ or } V_{CC};  I_{O} = 0 \; A; \\ V_{CC} = 0.8 \; V \; to \; 3.6 \; V \end{array}$	-	-	0.5	μΑ
Δl <sub>CC</sub>	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	40	μΑ
CI	input capacitance	$V_{CC}$ = 0 V to 3.6 V; $V_I$ = GND or $V_{CC}$	-	0.7	-	pF
Co	output capacitance	$V_{O} = GND; V_{CC} = 0 V$	-	0.9	-	pF
T <sub>amb</sub> = -	40 °C to +85 °C		I			1
VIH	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	$0.70 \times V_{CC}$	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	$0.65 \times V_{CC}$	-	-	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.6	-	-	V
		V <sub>CC</sub> = 3.0 V to 3.6 V	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	$0.30 \times V_{CC}$	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	$0.35 \times V_{CC}$	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	-	-	0.7	V
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	_		0.9	V

Rev. 8 — 17 September 2015

#### Low-power dual buffer with open-drain output

#### Table 7. Static characteristics ...continued

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_O$ = 20 $\mu\text{A};V_{CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	$0.3\times V_{CC}$	V
		$I_0 = 1.7 \text{ mA}; V_{CC} = 1.4 \text{ V}$	-	-	0.37	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.35	V
		$I_0 = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.33	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.45	V
		$I_0 = 2.7 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.33	V
		$I_0 = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.45	V
I <sub>I</sub>	input leakage current	$V_I = GND$ to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.5	μA
I <sub>OZ</sub>	OFF-state output current	$V_{I} = V_{IH}$ ; $V_{O} = 0$ V to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.5	μA
I <sub>OFF</sub>	power-off leakage current	$V_{I}$ or $V_{O}$ = 0 V to 3.6 V; $V_{CC}$ = 0 V	-	-	±0.5	μA
$\Delta I_{OFF}$	additional power-off leakage current		-	-	±0.6	μA
I <sub>CC</sub>	supply current	$V_1 = GND \text{ or } V_{CC}; I_0 = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.9	μΑ
Δl <sub>CC</sub>	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	50	μA
T <sub>amb</sub> = -	40 °C to +125 °C					
V <sub>IH</sub>	HIGH-level input voltage	V <sub>CC</sub> = 0.8 V	$0.75 \times V_{CC}$	-	-	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	$0.70 \times V_{CC}$	-	-	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.6	-	-	V
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	2.0	-	-	V
V <sub>IL</sub>	LOW-level input voltage	V <sub>CC</sub> = 0.8 V	-	-	$0.25\times V_{CC}$	V
		V <sub>CC</sub> = 0.9 V to 1.95 V	-	-	$0.30\times V_{CC}$	V
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	-	-	0.7	V
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	-	-	0.9	V
V <sub>OL</sub>	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{O}$ = 20 $\mu$ A; $V_{CC}$ = 0.8 V to 3.6 V	-	-	0.11	V
		I <sub>O</sub> = 1.1 mA; V <sub>CC</sub> = 1.1 V	-	-	$0.33 \times V_{CC}$	V
		I <sub>O</sub> = 1.7 mA; V <sub>CC</sub> = 1.4 V	-	-	0.41	V
		I <sub>O</sub> = 1.9 mA; V <sub>CC</sub> = 1.65 V	-	-	0.39	V
		$I_0 = 2.3 \text{ mA}; V_{CC} = 2.3 \text{ V}$	-	-	0.36	V
		I <sub>O</sub> = 3.1 mA; V <sub>CC</sub> = 2.3 V	-	-	0.50	V
		I <sub>O</sub> = 2.7 mA; V <sub>CC</sub> = 3.0 V	-	-	0.36	V
		I <sub>O</sub> = 4.0 mA; V <sub>CC</sub> = 3.0 V	-	-	0.50	V
lı	input leakage current	$V_I = GND$ to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.75	μA
I <sub>OZ</sub>	OFF-state output current	$V_{I} = V_{IH}$ ; $V_{O} = 0$ V to 3.6 V; $V_{CC} = 0$ V to 3.6 V	-	-	±0.75	μA
I <sub>OFF</sub>	power-off leakage current	$V_{1} \text{ or } V_{0} = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$	-	-	±0.75	μA

#### Low-power dual buffer with open-drain output

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).								
Symbol	Parameter	Conditions	Min	Тур	Max	Unit		
$\Delta I_{OFF}$	additional power-off leakage current	$V_{I} \text{ or } V_{O} = 0 \text{ V to } 3.6 \text{ V;}$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.75	μA		
I <sub>CC</sub>	supply current	$\label{eq:VI} \begin{array}{l} V_{I} = GND \text{ or } V_{CC}; \ I_{O} = 0 \ A; \\ V_{CC} = 0.8 \ V \ to \ 3.6 \ V \end{array}$	-	-	1.4	μA		
$\Delta I_{CC}$	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	75	μA		

#### Table 7. Static characteristics ...continued

## **11. Dynamic characteristics**

#### Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 9.

Symbol	Parameter	Conditions		25 °C			–40 °C to +125 °C		
			Min	Typ <mark>[1]</mark>	Max	Min	Max (85 °C)	Max (125 °C)	
C <sub>L</sub> = 5 p	F								
t <sub>pd</sub>	propagation delay	nA to nY; see Figure 8 [2]							
		$V_{CC} = 0.8 V$	-	11.6	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	2.1	4.1	7.5	1.7	9.1	10.0	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$	1.6	3.0	5.1	1.3	6.1	6.7	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$	1.6	2.7	4.0	1.2	5.0	5.5	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.1	2.1	3.2	0.9	4.0	4.4	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	1.4	2.2	2.8	1.1	3.3	3.6	ns
C <sub>L</sub> = 10	pF								
t <sub>pd</sub>	propagation delay	nA to nY; see Figure 8 [2]							
		$V_{CC} = 0.8 V$	-	14.7	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.0	5.1	9.0	2.4	11.2	12.3	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$	2.3	3.8	6.1	2.0	7.4	8.1	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$	2.4	3.6	4.8	1.8	6.1	6.7	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	1.7	2.8	3.8	1.3	4.8	5.3	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	2.2	3.1	4.2	1.6	4.5	5.0	ns
C <sub>L</sub> = 15	pF								
t <sub>pd</sub>	propagation delay	nA to nY; see Figure 8 [2]							
		$V_{CC} = 0.8 V$	-	17.7	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	3.5	6.1	10.4	3.2	13.1	14.5	ns
		$V_{CC} = 1.4 \text{ V to } 1.6 \text{ V}$	3.0	4.5	6.8	2.6	8.6	9.4	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$	2.8	4.4	6.7	2.2	7.8	8.6	ns
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	2.4	3.4	4.5	1.9	5.3	5.8	ns
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	2.2	4.0	5.7	1.9	6.1	6.7	ns

#### Low-power dual buffer with open-drain output

Symbol	Parameter	Conditions		25 °C		-4	0 °C to +'	125 °C	Unit
			Min	Typ <mark>[1]</mark>	Max	Min	Max (85 °C)	Max (125 °C)	
C <sub>L</sub> = 30 p	ρF	· · · · · · · · · · · · · · · · · · ·							
t <sub>pd</sub>	propagation delay	nA to nY; see Figure 8 [2]							
		V <sub>CC</sub> = 0.8 V	-	26.7	-	-	-	-	ns
		V <sub>CC</sub> = 1.1 V to 1.3 V	4.8	9.0	15.6	4.3	18.8	20.7	ns
		V <sub>CC</sub> = 1.4 V to 1.6 V	4.1	6.7	9.4	3.7	11.8	13.0	ns
		$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$	3.8	6.8	9.7	3.2	11.0	12.1	ns
		$V_{CC}$ = 2.3 V to 2.7 V	3.7	5.2	6.7	3.0	7.1	7.8	ns
		$V_{CC} = 3.0 \text{ V} \text{ to } 3.6 \text{ V}$	3.6	6.4	9.7	2.8	10.4	11.4	ns
C <sub>L</sub> = 5 pl	F, 10 pF, 15 pF and	30 pF							
C <sub>PD</sub>	power dissipation	$f_i = 1 \text{ MHz}; V_1 = \text{GND to } V_{\text{CC}}$ [3][4]							
	capacitance	V <sub>CC</sub> = 0.8 V	-	0.5	-	-	-	-	pF
		V <sub>CC</sub> = 1.1 V to 1.3 V	-	0.6	-	-	-	-	pF
		V <sub>CC</sub> = 1.4 V to 1.6 V	-	0.6	-	-	-	-	pF
		$V_{CC} = 1.65 \text{ V} \text{ to } 1.95 \text{ V}$	-	0.7	-	-	-	-	pF
		$V_{CC}$ = 2.3 V to 2.7 V	-	0.9	-	-	-	-	pF
L		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	-	1.2	-	-	-	-	pF

#### Table 8. Dynamic characteristics ...continued

Voltages are referenced to GND (ground = 0 V); for test circuit see <u>Figure 9</u>.

[1] All typical values are measured at nominal  $V_{CC}$ .

 $\label{eq:tpd} \ensuremath{\left[2\right]} \quad t_{pd} \mbox{ is the same as } t_{PZL} \mbox{ and } t_{PLZ}.$ 

[3] All specified values are the average typical values over all stated loads.

[4]  $C_{PD}$  is used to determine the dynamic power dissipation (P<sub>D</sub> in  $\mu$ W).

 $P_{D}$  =  $C_{PD} \times V_{CC}{}^2 \times f_i \times N$  where:

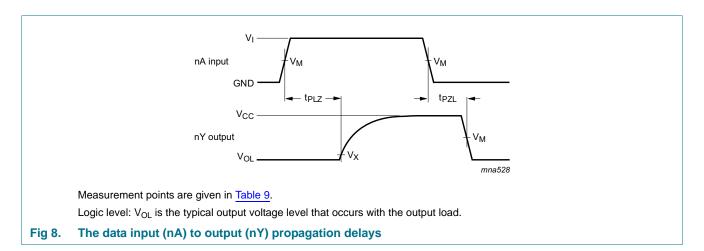
 $f_i$  = input frequency in MHz;

 $V_{CC}$  = supply voltage in V;

N = number of inputs switching.

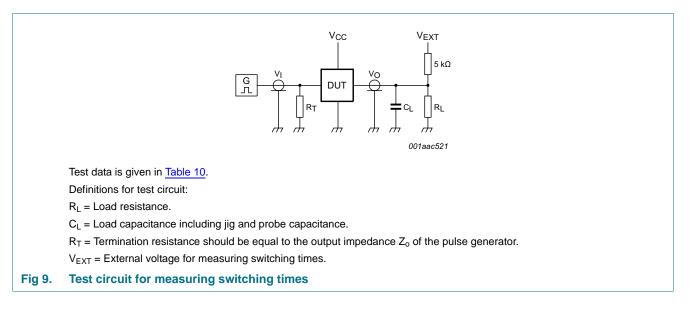
#### Low-power dual buffer with open-drain output

### 12. Waveforms



#### Table 9. Measurement points

Supply voltage	pply voltage Input Output		
V <sub>cc</sub>	V <sub>M</sub>	V <sub>M</sub>	V <sub>X</sub>
0.8 V to 1.6 V	$0.5  imes V_{CC}$	$0.5  imes V_{CC}$	V <sub>OL</sub> + 0.1 V
1.65 V to 2.7 V	$0.5  imes V_{CC}$	$0.5  imes V_{CC}$	V <sub>OL</sub> + 0.15 V
3.0 V to 3.6 V	$0.5\times V_{CC}$	$0.5  imes V_{CC}$	V <sub>OL</sub> + 0.3 V



#### Table 10. Test data

Supply voltage	e Input		Load		V <sub>EXT</sub>		
V <sub>cc</sub>	VI	t <sub>r</sub> , t <sub>f</sub>	CL	R <sub>L</sub> [1]	t <sub>PLH</sub> , t <sub>PHL</sub>	t <sub>PZH</sub> , t <sub>PHZ</sub>	t <sub>PZL</sub> , t <sub>PLZ</sub>
0.8 V to 3.6 V	V <sub>CC</sub>	≤ 3 ns	5 pF, 10 pF, 15 pF and 30 pF	5 k $\Omega$ or 1 M $\Omega$	open	GND	$2\times V_{CC}$

[1] For measuring enable and disable times  $R_L = 5 k\Omega$ , for measuring propagation delays, set-up and hold times and pulse width  $R_L = 1 M\Omega$ .

Low-power dual buffer with open-drain output

### 13. Package outline

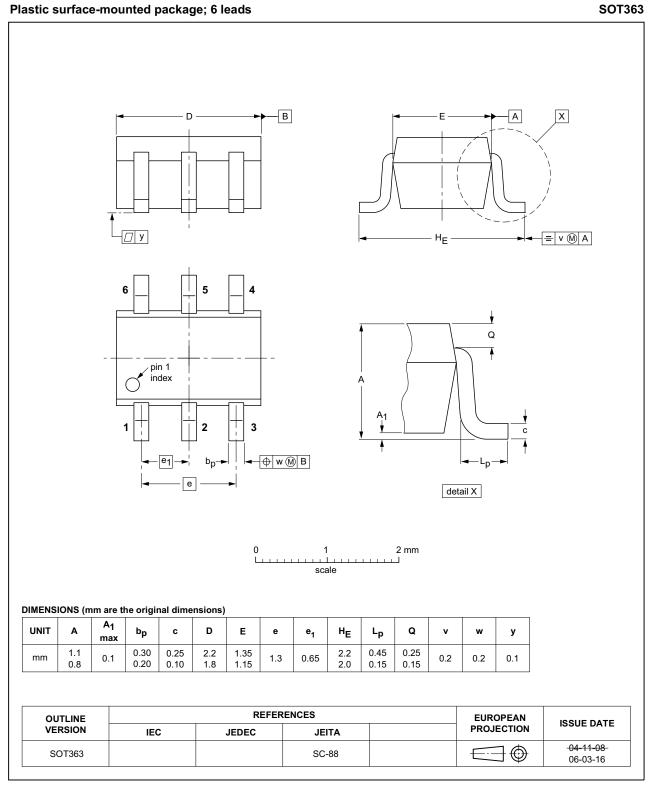


Fig 10. Package outline SOT363 (SC-88)

#### Low-power dual buffer with open-drain output

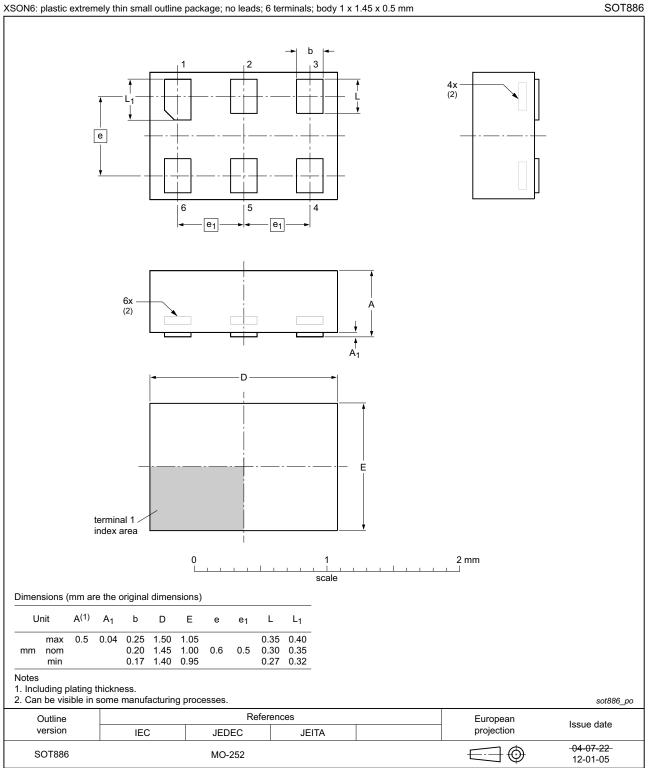


Fig 11. Package outline SOT886 (XSON6)

All information provided in this document is subject to legal disclaimers.

#### Low-power dual buffer with open-drain output

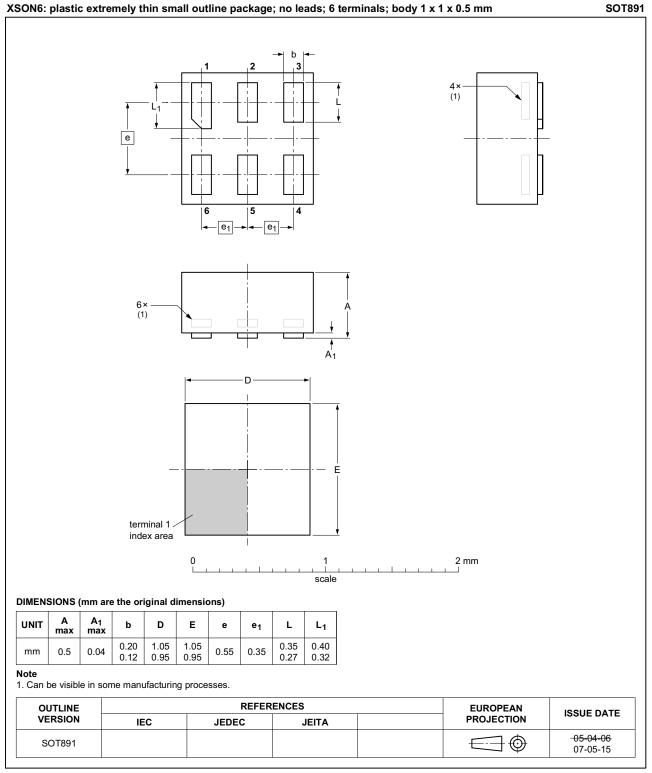
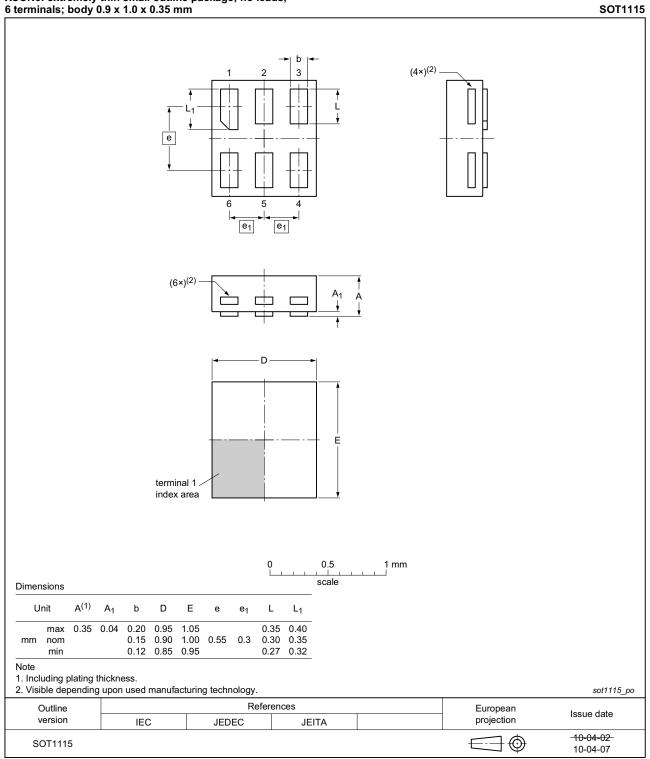


Fig 12. Package outline SOT891 (XSON6)

All information provided in this document is subject to legal disclaimers.

#### Low-power dual buffer with open-drain output

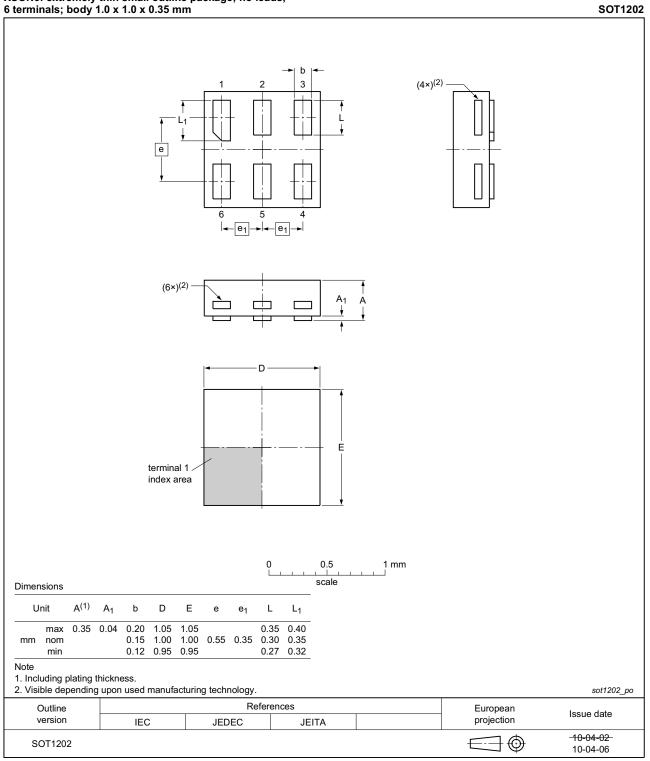


## XSON6: extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm

Fig 13. Package outline SOT1115 (XSON6)

All information provided in this document is subject to legal disclaimers.

Low-power dual buffer with open-drain output

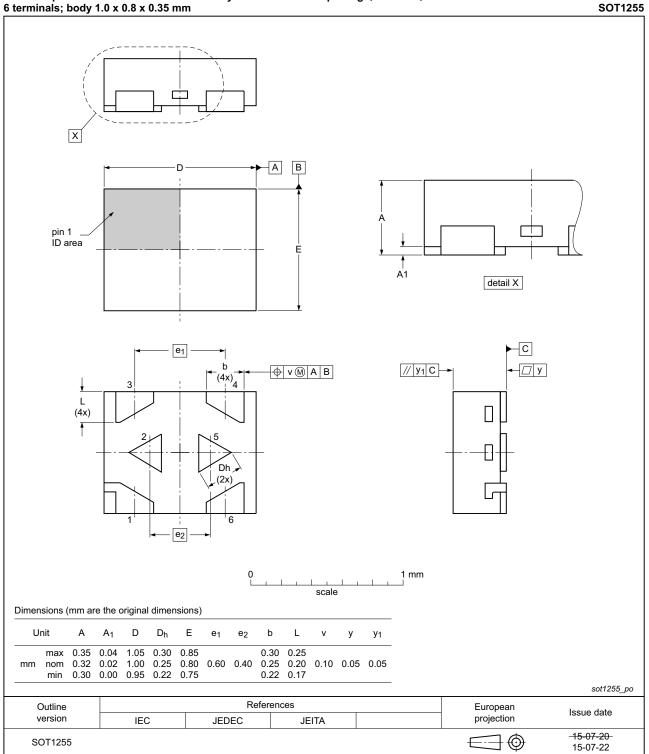


XSON6: extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm

Fig 14. Package outline SOT1202 (XSON6)

All information provided in this document is subject to legal disclaimers.

#### Low-power dual buffer with open-drain output



#### X2SON6: plastic thermal enhanced extremely thin small outline package; no leads; 6 terminals; body 1.0 x 0.8 x 0.35 mm

Fig 15. Package outline SOT1255 (X2SON6)

All information provided in this document is subject to legal disclaimers.

## 14. Abbreviations

Table 11. Abbreviations		
Acronym	Description	
CDM	Charged Device Model	
DUT	Device Under Test	
ESD	ElectroStatic Discharge	
НВМ	Human Body Model	
MM	Machine Model	

## **15. Revision history**

#### Table 12.Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP2G07 v.8	20150917	Product data sheet	-	74AUP2G07 v.7
Modifications:	<ul> <li>Added type nu</li> </ul>	umber 74AUP2G07GX (SOT1	255/X2SON6).	
74AUP2G07 v.7	20121129	Product data sheet	-	74AUP2G07 v.6
Modifications:	<ul> <li>Package outling</li> </ul>	ne drawing of SOT886 (Figure	<u>e 11</u> ) modified.	
74AUP2G07 v.6	20111202	Product data sheet	-	74AUP2G07 v.5
74AUP2G07 v.5	20100909	Product data sheet	-	74AUP2G07 v.4
74AUP2G07 v.4	20090611	Product data sheet	-	74AUP2G07 v.3
74AUP2G07 v.3	20071016	Product data sheet	-	74AUP2G07 v.2
74AUP2G07 v.2	20070612	Product data sheet	-	74AUP2G07 v.1
74AUP2G07 v.1	20061121	Product data sheet	-	-

### **16. Legal information**

#### 16.1 Data sheet status

Document status[1][2]	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

#### 16.2 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any

representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and

customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

#### 16.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia.

In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of Nexperia.

**Right to make changes** — Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof. Suitability for use — Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of a Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale - Nexperia

products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nexperia.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Product data sheet

#### Nexperia

## 74AUP2G07

#### Low-power dual buffer with open-drain output

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Non-automotive qualified products** — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of

non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the

product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond

Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

#### 16.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

### 17. Contact information

For more information, please visit: http://www.nexperia.com

For sales office addresses, please send an email to: salesaddresses@nexperia.com

### Low-power dual buffer with open-drain output

### **18. Contents**

1	General description 1
2	Features and benefits 1
3	Ordering information 2
4	Marking 2
5	Functional diagram 2
6	Pinning information 3
6.1	Pinning 3
6.2	Pin description 3
7	Functional description 4
8	Limiting values 4
9	Recommended operating conditions 4
10	Static characteristics 5
11	Dynamic characteristics 7
12	Waveforms
13	Package outline 10
14	Abbreviations
15	Revision history 16
16	Legal information 17
16.1	Data sheet status 17
16.2	Definitions 17
16.3	Disclaimers
16.4	Trademarks 18
17	Contact information 18
18	Contents 19

## **X-ON Electronics**

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Buffers & Line Drivers category:

Click to view products by NXP manufacturer:

Other Similar products are found below :

5962-9217601MSA 634810D 875140G HEF4022BP HEF4043BP NL17SG125DFT2G NL17SZ126P5T5G NLU1GT126CMUTCG NLU3G16AMX1TCG NLV27WZ125USG MC74HCT365ADTR2G BCM6306KMLG 54FCT240CTDB Le87401NQC Le87402MQC 028192B 042140C 051117G 070519XB 065312DB 091056E 098456D NL17SG07DFT2G NL17SG17DFT2G NL17SG34DFT2G NL17SZ07P5T5G NL17SZ125P5T5G NLU1GT126AMUTCG NLV27WZ16DFT2G 5962-8982101PA 5962-9052201PA 74LVC07ADR2G MC74VHC1G125DFT1G NL17SH17P5T5G NL17SZ125CMUTCG NLV17SZ07DFT2G NLV37WZ17USG NLVHCT244ADTR2G NC7WZ17FHX 74HCT126T14-13 NL17SH125P5T5G NLV14049UBDTR2G NLV37WZ07USG 74VHC541FT(BE) RHFAC244K1 74LVC1G17FW4-7 74LVC1G126FZ4-7 BCM6302KMLG 74LVC1G07FZ4-7 74LVC1G125FW4-7