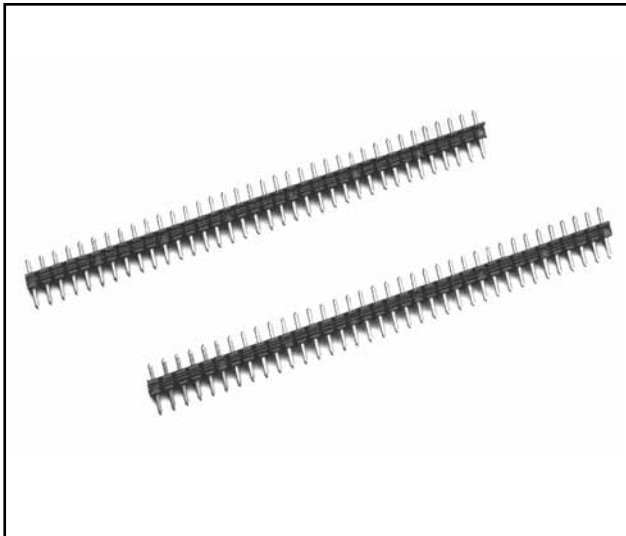


# 3M™ Pin Strip Header

.100" and .100" x .100", .093" Mating Length, Straight, Solder Tails

929 Series



- Low profile for close board stacking
- Solder stand-offs to facilitate production wave solder and board cleaning
- Board pin retention feature available
- See the Regulatory Information Appendix (RIA) in the "RoHS Compliance" section of [www.3Mconnectors.com](http://www.3Mconnectors.com) for compliance information.

Date Modified: February 3, 2012

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## Physical

### Insulation:

Material: Glass Filled Polyester (PBT) or PCT

Flammability: UL 94V-0

Color: Black

### Contact:

Material: Copper Alloy

### Plating:

Underplating: 50  $\mu$  [ 1.27  $\mu$ m ] Nickel

Wiping Area & Solder Tails: See ordering information

**Marking:** None

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## Electrical

**Current Rating:** 2.5 A

**Insulation Resistance:**  $> 5 \times 10^9 \Omega$  at 500 V<sub>DC</sub>

**Withstanding Voltage:** 1500 V<sub>RMS</sub> at Sea Level

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## Environmental

**Temperature Rating:** -40°C to +105°C

**Process Rating:** PCT Insulator: 260°C per J-STD-020

PBT Insulator: Maximum insulator temperature 191°C for solder wave process only

**Moisture Sensitivity Level:** 1 (per J-STD-020C) for PCT insulator only

UL File No.: E68080



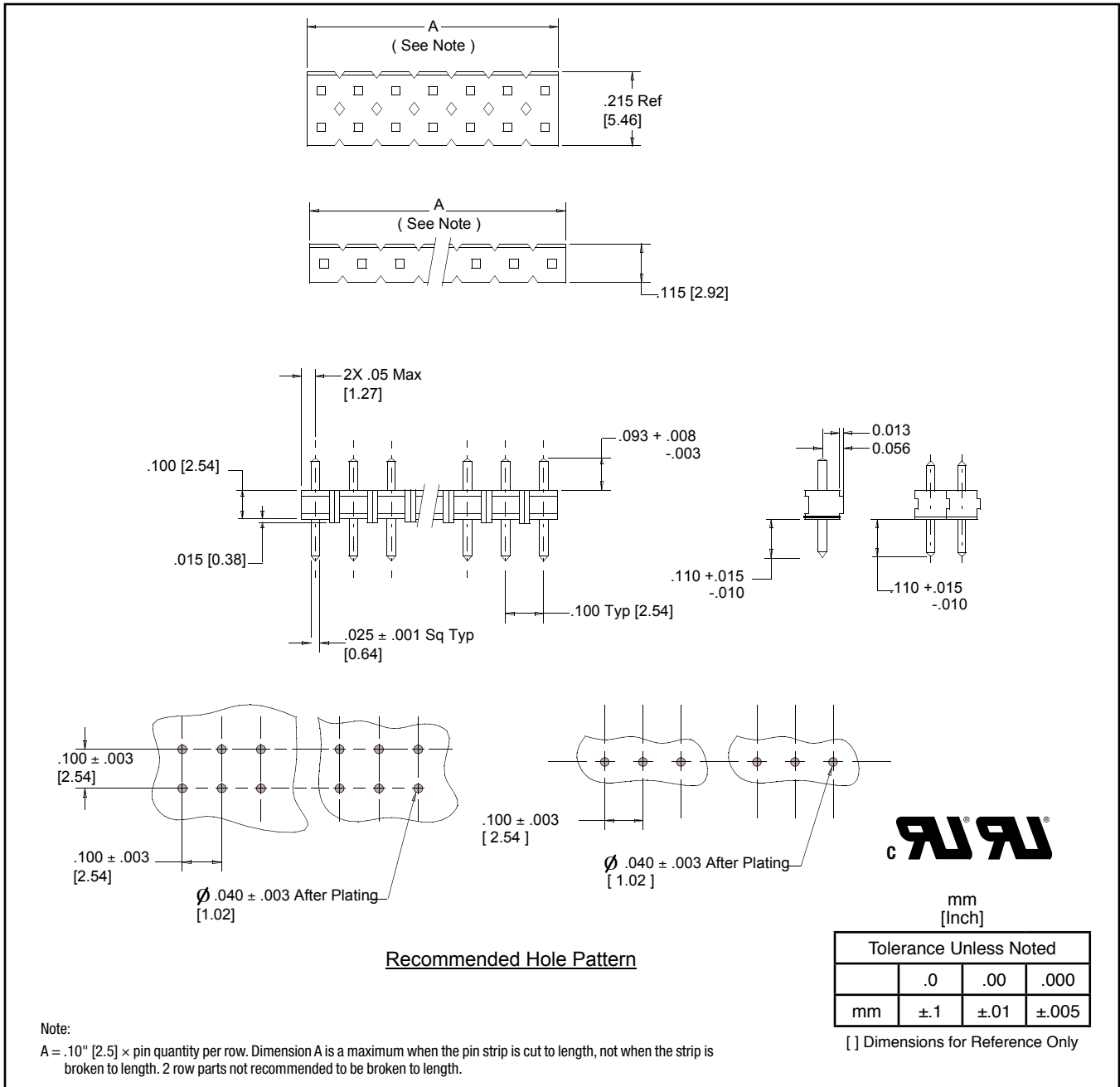
Electronic Solutions Division  
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**800-225-5373**

# 3M™ Pin Strip Header

.100" and .100" x .100", .093" Mating Length, Straight, Solder Tails

929 Series



## Ordering Information

**Tin**  
**9X9800-01-XX-XX**

Pin Quantity (01-36)

Board Retention Feature  
 2 = None  
 R = Kinked Tails

Plating Suffix:  
 blank = 100 μ" [2.54 μm] tin-lead, PBT insulator (RIA E3 & C2 apply)  
 RK = 200 μ" [5 μm] matte tin, PCT insulator (RIA E1 & C1 apply)

**Gold**  
**9X98XX-01-XX-XX**

05 = 1 row  
 15 = 2 row

Board Retention Feature  
 2 = None  
 R = Kinked Tails

Gold Plating Suffix  
 I = 10 μ" [0.25 μm] all over, PBT insulator (RIA E1 & C1 apply)  
 EU = 10 μ" [0.25 μm] all over, PCT insulator (RIA E1 & C1 apply)

Pin Quantity per row (01-36)  
 ((02-72) ÷ 2)

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 Sheet 2 of 2

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