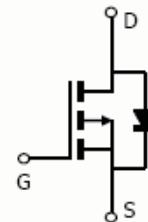


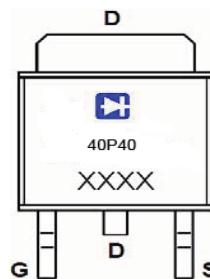
## P-Channel Enhancement Mode Power MOSFET

### Description

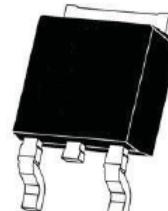
The RM40P40LD uses advanced trench technology and design to provide excellent RDS(ON) with low gate charge .This device is well suited for high current load applications.



Schematic diagram



Marking and pin assignment



TO-252-2L top view

### Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply
- Halogen-free
- P/N suffix V means AEC-Q101 qualified, e.g.:RM40P40LDV

**100% UIS TESTED!**

**100%  $\Delta V_{ds}$  TESTED!**

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
40P40	RM40P40LD	TO-252-2L	-	-	-

### Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	-40	A
Drain Current-Continuous( $T_c=100^\circ\text{C}$ )	$I_D$ ( $100^\circ\text{C}$ )	-25	A
Pulsed Drain Current	$I_{DM}$	-50	A
Maximum Power Dissipation	$P_D$	80	W
Derating factor		0.53	W/ $^\circ\text{C}$
Single pulse avalanche energy (Note 5)	$E_{AS}$	544	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 175	$^\circ\text{C}$

## Thermal Characteristic

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	R <sub>θJC</sub>	1.88	°C/W
--	------------------	------	------

## Electrical Characteristics ( $T_c=25^\circ\text{C}$ unless otherwise noted)

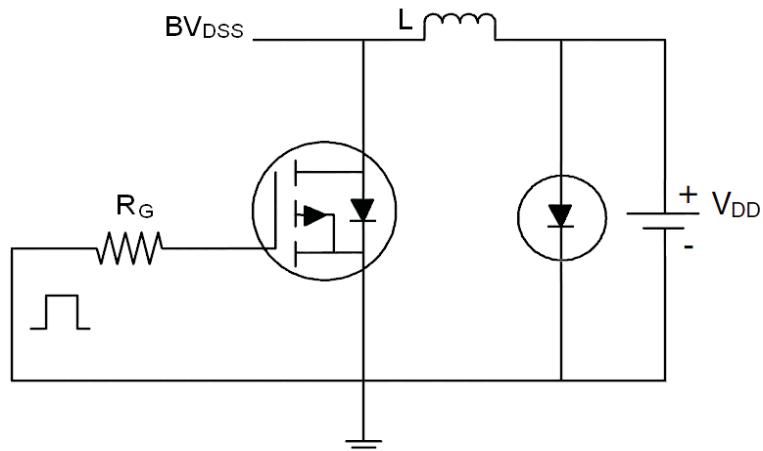
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-40	-	-	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-40\text{V}, V_{\text{GS}}=0\text{V}$	-	-	-1	$\mu\text{A}$
Gate-Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm100$	nA
<b>On Characteristics</b> <sup>(Note 3)</sup>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1.5	-1.9	-3.0	V
Drain-Source On-State Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-12\text{A}$	-	12	14	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}}=-5\text{V}, I_{\text{D}}=-12\text{A}$	34	-	-	S
<b>Dynamic Characteristics</b> <sup>(Note 4)</sup>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-20\text{V}, V_{\text{GS}}=0\text{V}, F=1.0\text{MHz}$	-	2960	-	PF
Output Capacitance	$C_{\text{oss}}$		-	370	-	PF
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	310	-	PF
<b>Switching Characteristics</b> <sup>(Note 4)</sup>						
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=-20\text{V}, I_{\text{D}}=-20\text{A}$ $V_{\text{GS}}=-10\text{V}, R_{\text{G}}=3\Omega$	-	10	-	nS
Turn-on Rise Time	$t_{\text{r}}$		-	18	-	nS
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	38	-	nS
Turn-Off Fall Time	$t_{\text{f}}$		-	24	-	nS
Total Gate Charge	$Q_{\text{g}}$	$V_{\text{DS}}=-20, I_{\text{D}}=-12\text{A}, V_{\text{GS}}=-10\text{V}$	-	72	-	nC
Gate-Source Charge	$Q_{\text{gs}}$		-	14	-	nC
Gate-Drain Charge	$Q_{\text{gd}}$		-	15	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <sup>(Note 3)</sup>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=-20\text{A}$	-		-1.2	V
Diode Forward Current <sup>(Note 2)</sup>	$I_{\text{s}}$		-	-	-40	A
Reverse Recovery Time	$t_{\text{rr}}$	$T_J = 25^\circ\text{C}, IF = -20\text{A}$ $di/dt = -100\text{A}/\mu\text{s}$ <sup>(Note 3)</sup>	-	40	-	nS
Reverse Recovery Charge	$Q_{\text{rr}}$		-	42	-	nC
Forward Turn-On Time	$t_{\text{on}}$	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

## Notes:

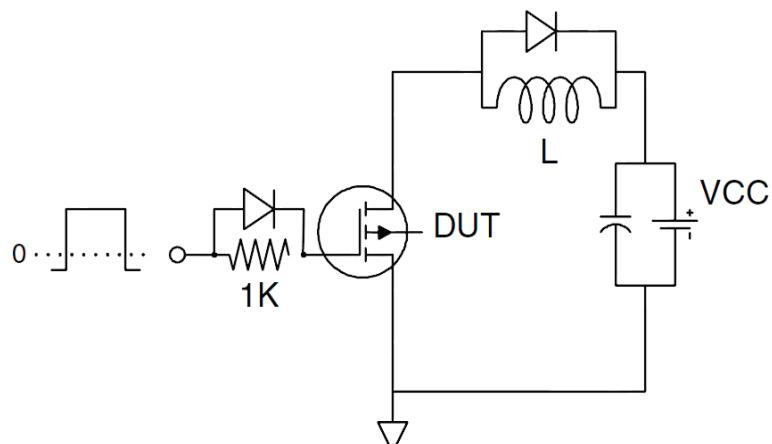
1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5. E<sub>AS</sub> condition:  $T_J=25^\circ\text{C}, V_{\text{DD}}=-20\text{V}, V_{\text{G}}=-10\text{V}, L=1\text{mH}, R_g=25\Omega, I_{\text{AS}}=33\text{A}$

## Test Circuit

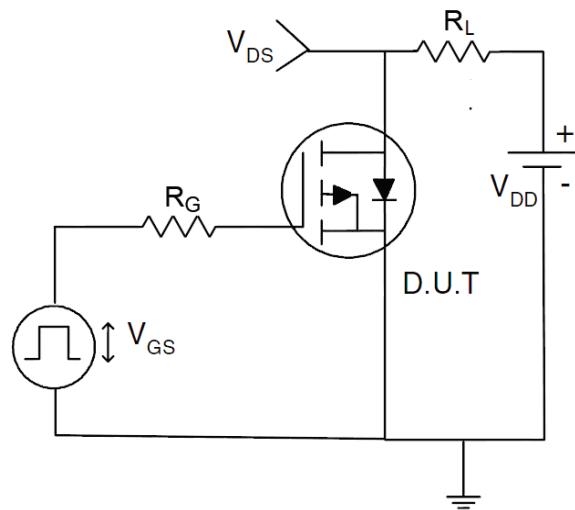
### 1) E<sub>AS</sub> Test Circuit



### 2) Gate Charge Test Circuit



### 3) Switch Time Test Circuit



## RATING AND CHARACTERISTICS CURVES (RM40P40LD)

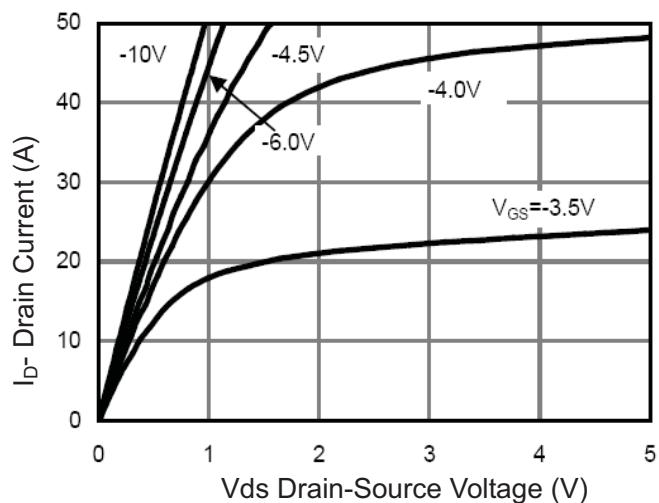


Figure 1 Output Characteristics

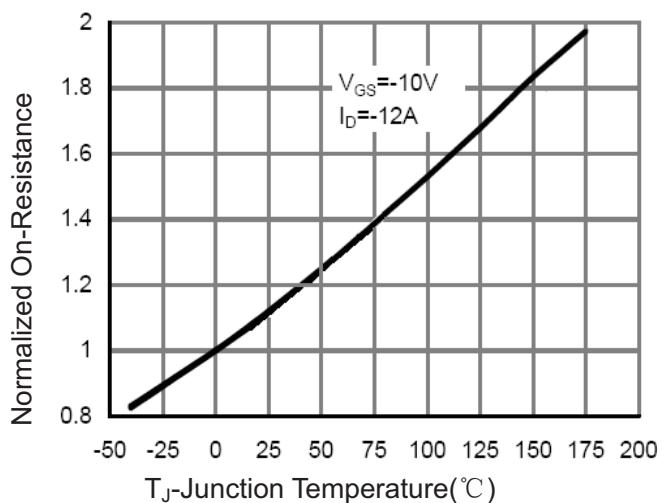


Figure 4 Rdson-Junction Temperature

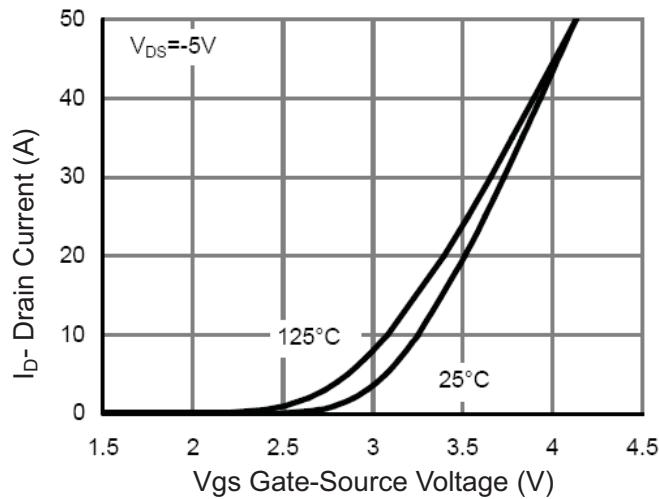


Figure 2 Transfer Characteristics

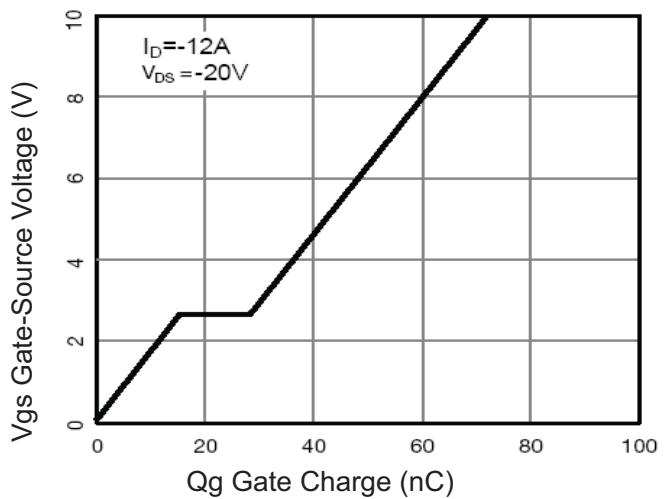


Figure 5 Gate Charge

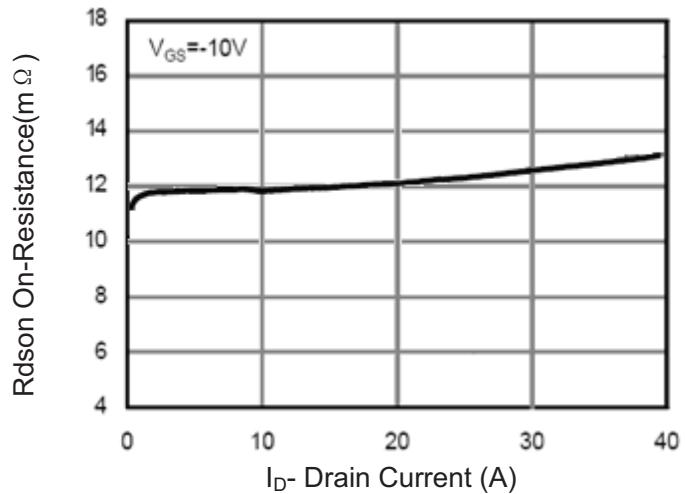


Figure 3 Rdson- Drain Current

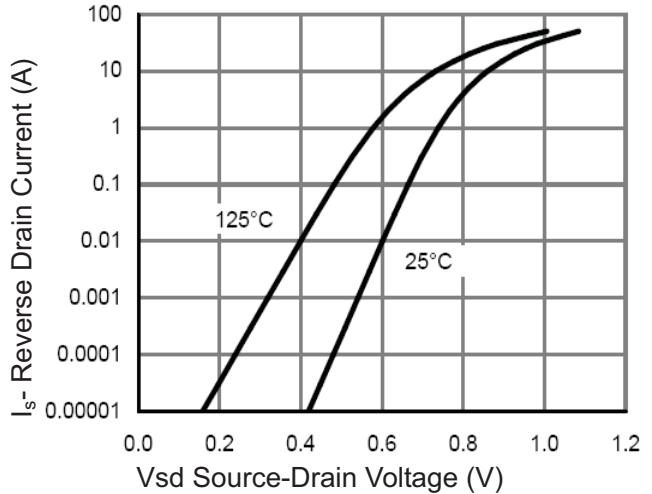


Figure 6 Source- Drain Diode Forward

## RATING AND CHARACTERISTICS CURVES (RM40P40LD)

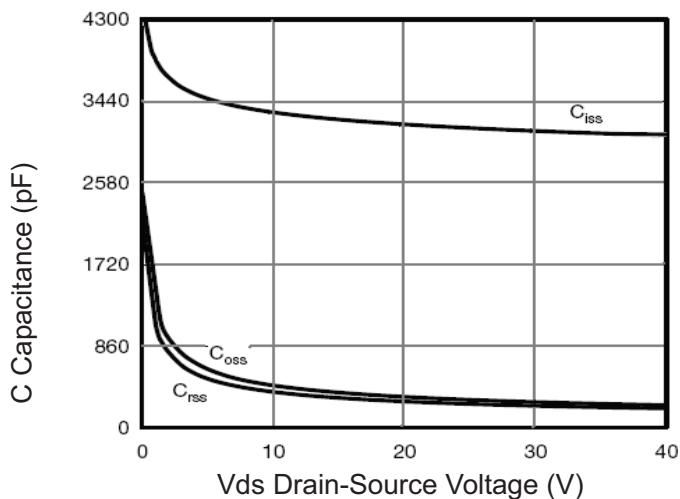


Figure 7 Capacitance vs Vds

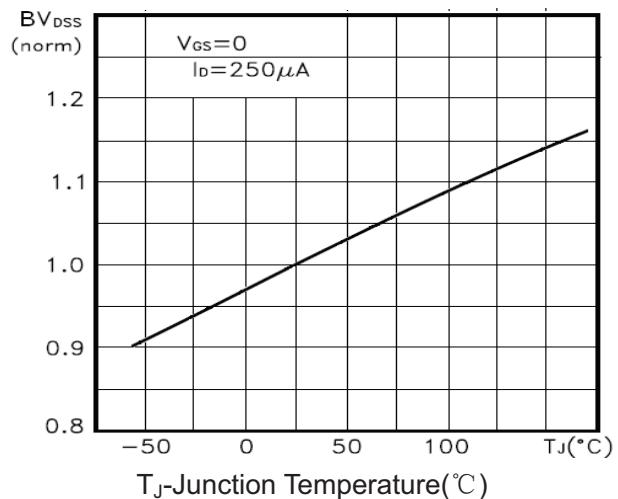


Figure 9  $BV_{DSS}$  vs Junction Temperature

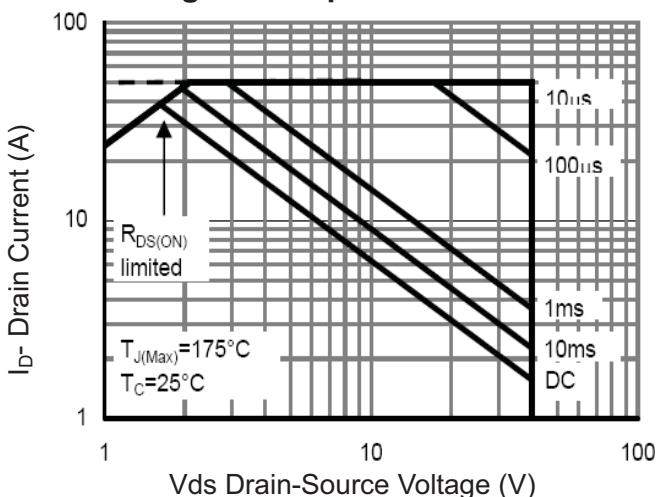


Figure 8 Safe Operation Area

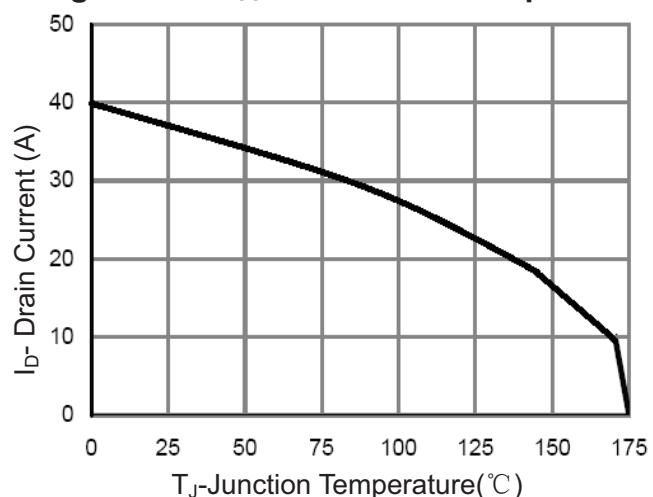


Figure 10  $I_D$  Current Derating vs Junction Temperature

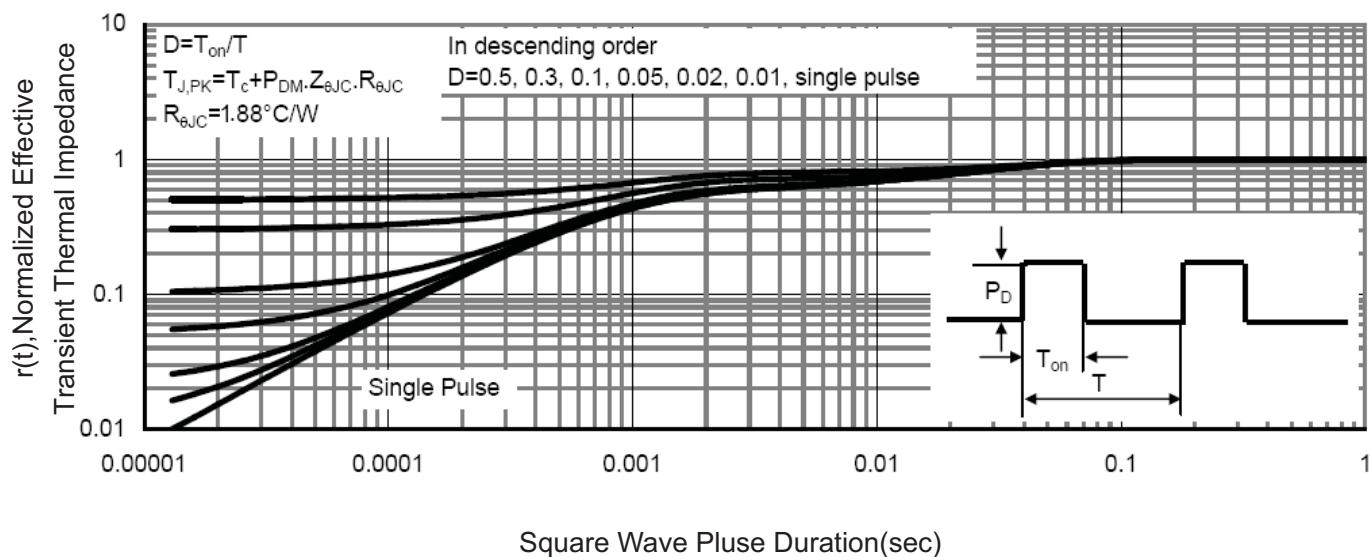
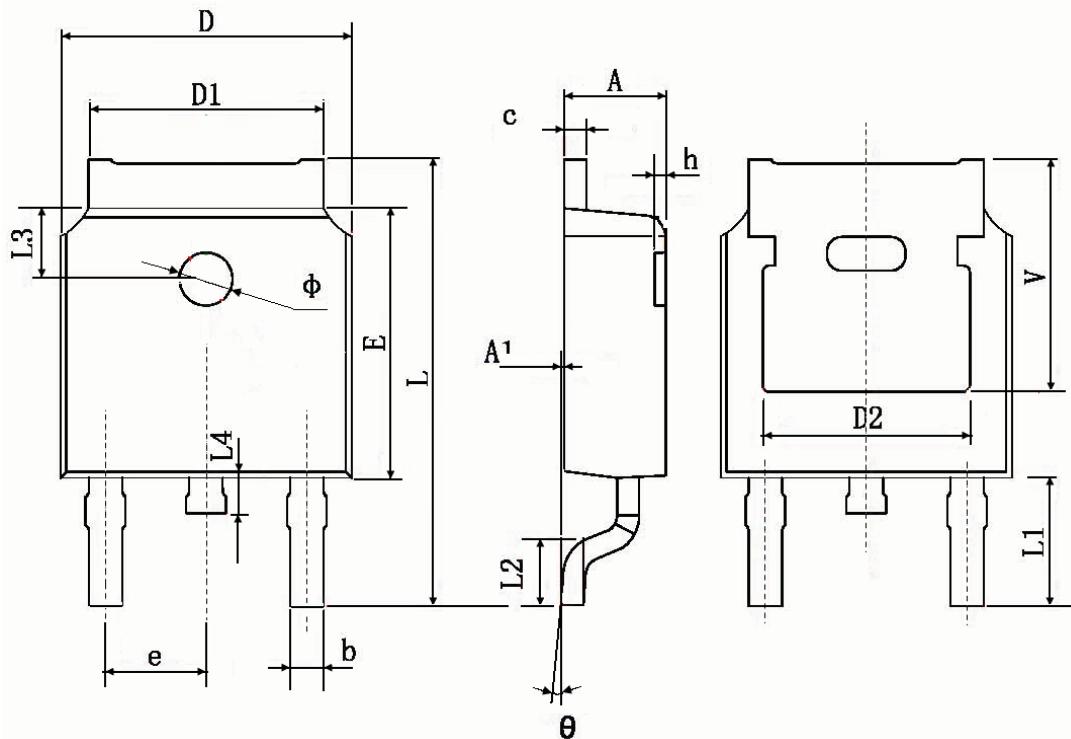


Figure 11 Normalized Maximum Transient Thermal Impedance

## TO-252 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	

<b>Package</b>	Tube (pcs/tube)	Tube (pcs/inner box)	Tube (pcs/cartoon)	Tape&Reel (pcs/reel)	Tape&Reel (pcs/inner box)	Tape&Reel (pcs/cartoon)
DFN5x6/DFN3x3	100	10,000	100,000	2,500	5,000	40,000
DFN1006	—	—	—	10,000	10,000	400,000
SOP-8	100	10,000	100,000	4,000	4,000	20,000
TSSOP-8	100	32,000	128,000	3,000	6,000	48,000
SOT-23-3L	—	—	—	3,000	30,000	120,000
SOT-23-6L	—	—	—	3,000	30,000	120,000
SOT-23(6R)	—	—	—	3,000	30,000	120,000
SOT-363	—	—	—	3,000	30,000	120,000
SOT-523	—	—	—	3,000	30,000	120,000
<b>SOT223</b>	—	—	—	<b>2,500</b>	<b>2,500</b>	<b>20,000</b>
TO-220	50	1,000	5,000	—	—	—
TO-220F	50	1,000	10,000	—	—	—
TO-247	30	300	1,200	—	—	—
TO-251	80	4,000	40,000	—	—	—
TO-251S(4R)	80	4,000	40,000	—	—	—
TO-252-2L(4R)	80	4,000	40,000	2,500	2,500	25,000
TO-263-2L	50	1,000	10,000	800	800	8,000
TO-3P	30	300	3,000	—	—	—
TO-92	—	—	—	1,000(袋装)	10,000	100,000

## **DISCLAIMER NOTICE**

Rectron Inc reserves the right to make changes without notice to any product specification herein, to make corrections, modifications, enhancements or other changes. Rectron Inc or anyone on its behalf assumes no responsibility or liability for any errors or inaccuracies. Data sheet specifications and its information contained are intended to provide a product description only. "Typical" parameters which may be included on RECTRON data sheets and/ or specifications can and do vary in different applications and actual performance may vary over time. Rectron Inc does not assume any liability arising out of the application or use of any product or circuit.

Rectron products are not designed, intended or authorized for use in medical, life-saving implant or other applications intended for life-sustaining or other related applications where a failure or malfunction of component or circuitry may directly or indirectly cause injury or threaten a life without expressed written approval of Rectron Inc. Customers using or selling Rectron components for use in such applications do so at their own risk and shall agree to fully indemnify Rectron Inc and its subsidiaries harmless against all claims, damages and expenditures.

# X-ON Electronics

Largest Supplier of Electrical and Electronic Components

***Click to view similar products for MOSFET category:***

***Click to view products by Rectron manufacturer:***

Other Similar products are found below :

[614233C](#) [648584F](#) [MCH3443-TL-E](#) [MCH6422-TL-E](#) [FDPF9N50NZ](#) [FW216A-TL-2W](#) [FW231A-TL-E](#) [APT5010JVR](#) [NTNS3A92PZT5G](#)  
[IRF100S201](#) [JANTX2N5237](#) [2SK2464-TL-E](#) [2SK3818-DL-E](#) [FCA20N60\\_F109](#) [FDZ595PZ](#) [STD6600NT4G](#) [FSS804-TL-E](#) [2SJ277-DL-E](#)  
[2SK1691-DL-E](#) [2SK2545\(Q,T\)](#) [405094E](#) [423220D](#) [MCH6646-TL-E](#) [TPCC8103,L1Q\(CM](#) [367-8430-0972-503](#) [VN1206L](#) [424134F](#)  
[026935X](#) [051075F](#) [SBVS138LT1G](#) [614234A](#) [715780A](#) [NTNS3166NZT5G](#) [751625C](#) [873612G](#) [IRF7380TRHR](#) [IPS70R2K0CEAKMA1](#)  
[RJK60S3DPP-E0#T2](#) [RJK60S5DPK-M0#T0](#) [APT5010JVFR](#) [APT12031JFLL](#) [APT12040JVR](#) [DMN3404LQ-7](#) [NTE6400](#) [JANTX2N6796U](#)  
[JANTX2N6784U](#) [JANTXV2N5416U4](#) [SQM110N05-06L-GE3](#) [SIHF35N60E-GE3](#) [2SK2614\(TE16L1,Q\)](#)